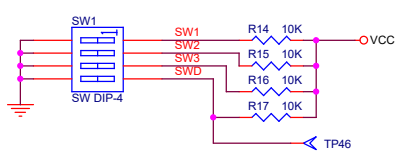
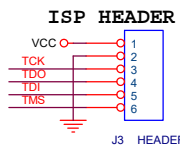
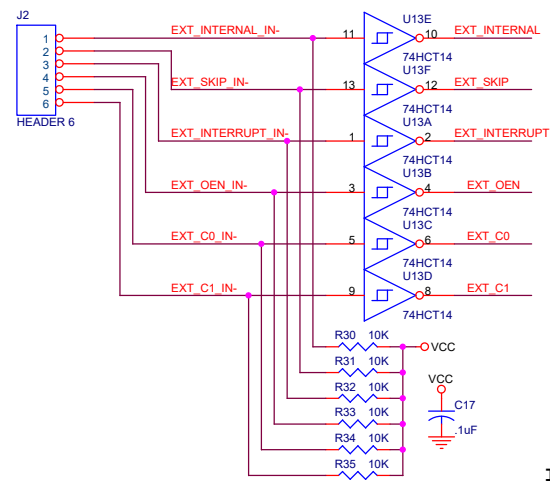
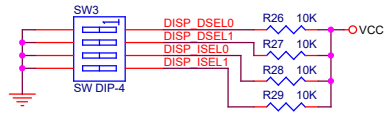
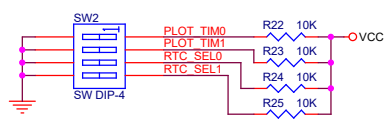
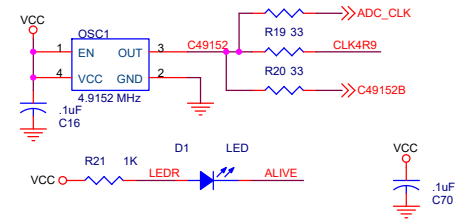


Title				<b>PDP-8e BUS Connections</b>	
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**BAUD RATE AND VC8E SELECTION**



Pin	Signal	Pin	Signal	Pin	Signal
44	CLK4R9	87	RESET	17	INTERNAL_IO
18	RESET_L	17	SKIP	20	SKIP
19	IO_PAUSE	22	INT_RST	21	INT_RST
25	TP_L1	29	BUS_OEN	29	C0
23	TP_L3	28	C1	28	C1
12	MA11	106	RIGHT	106	PEN-RIGHT
14	MA10	107	LEFT	107	PEN-LEFT
15	MA9	109	PEN-UP	109	PEN-UP
16	MA8	110	PEN-DOWN	110	PEN-DOWN
38	MA7	111	DRUM-UP	111	DRUM-UP
39	MA6	112	DRUM-DOWN	112	DRUM-DOWN
40	MA5	103	PLOT_TEST0	103	PLOT_TEST0
41	MA4	102	PLOT_TEST1	102	PLOT_TEST1
56	MA3	69	ADC_A0	69	ADC_A0
7	DIN11	67	ADC_A1	67	ADC_A1
8	DIN10	66	ADC_A2	66	ADC_A2
9	DIN9	64	ADC_MUX3	64	ADC_MUX3
10	DIN8	21	ADC_RD	21	ADC_RD
34	DIN7	22	ADC_WR	22	ADC_WR
35	DIN6	63	ADC_TEST0	63	ADC_TEST0
36	DIN5	62	ADC_TEST1	62	ADC_TEST1
37	DIN4	61	ADC_TEST2	61	ADC_TEST2
49	DIN3	60	ADC_TEST3	60	ADC_TEST3
51	DIN2	164	BAUD_CLK	164	UCLK
54	DIN1	151	TTYOE	151	TTYOE
55	DIN0	149	TXLOAD	149	TXLOAD
86	EXT_INTERNAL	150	RXCCLR	150	RXCCLR
85	EXT_SKIP	152	RRUN	152	RRUN
84	EXT_INTERRUPT	148	UART_STATEN	148	UART_STATEN
80	EXT_OEN	147	UART_TEST0	147	UART_TEST0
83	EXT_C0	146	UART_TEST1	146	UART_TEST1
82	EXT_C1	145	UART_TEST2	145	UART_TEST2
114	PLOT_TIM0	144	UART_TEST3	144	UART_TEST3
113	PLOT_TIM1	101	CS_X	101	ENX
192	DISP_DSEL0	100	CS_Y	100	ENY
191	DISP_DSEL1	99	WR	99	DISP_WR
189	DISP_ISEL0	88	INTEN_PULSE	88	INTEN_PULSE
188	DISP_ISEL1	89	COLOR	89	COLOR
187	DISP_ERASE_DONE	97	ERASE	97	ERASE
121	RTC_SEL0	95	NON_STORE	95	STORE
122	RTC_SEL1	90	WRITE_THROUGH	90	WRITE_THRU
161	BSEL0	174	DISP_TEST0	174	DISP_TEST0
160	BSEL1	173	DISP_TEST1	173	DISP_TEST1
159	BSEL2	203	PC8_FETCH	203	PC8_FETCH
158	RXD	202	PUNCH	202	PUNCH
154	RxDAv	200	PC8_BOEN-	200	PC8_BOEN-
155	RxDAv	201	PC8_TEST0	201	PC8_TEST0
206	PC8_RDR_DAV	199	PC8_TEST1	199	PC8_TEST1
205	PC8_PCH_DONE	198	PC8_TEST2	198	PC8_TEST2
46	TP61	197	PC8_TEST3	197	PC8_TEST3
50	TP63	120	RTC_TEST0	120	RTC_TEST0
58	TP65	119	RTC_TEST1	119	RTC_TEST1
74	TP66	118	RTC_TEST2	118	RTC_TEST2
75	TP68	117	RTC_TEST3	117	RTC_TEST3
76	TP70	3	LSB_DOUT11	3	DOUT11
77	TP72	4	DOUT10	4	DOUT10
78	TP74	5	DOUT9	5	DOUT9
165	PGND10	6	DOUT8	6	DOUT8
166	PGND11	30	DOUT7	30	DOUT7
178	PGND12	31	DOUT6	31	DOUT6
182	PGND13	32	DOUT5	32	DOUT5
183	PGND14	33	DOUT4	33	DOUT4
73	ADC_BUSY_L	43	DOUT3	43	DOUT3
70	ADC_EXT_CONV	45	DOUT2	45	DOUT2
98	TCK	47	DOUT1	47	DOUT1
176	TDO	48	DOUT0	48	DOUT0
177	TDI	162	PLACE1	162	PLACE1
178	TMS	133	PGND24	133	TP81
96	ALIVE	134	PGND25	134	TP83
115	TP84	135	PGND26	135	TP85
116	TP86	136	PGND27	136	TP87
123	TP88	137	PGND28	137	TP89
125	TP90	138	PGND29	138	TP91
126	TP92	2	GND1	2	GND1
128	TP95	13	GND2	13	GND2
11	VCCINT1	24	GND3	24	GND3
59	VCCINT2	27	GND4	27	GND4
124	VCCINT3	42	GND5	42	GND5
153	VCCINT4	52	GND6	52	GND6
204	VCCINT5	68	GND7	68	GND7
1	VCCIO1	81	GND8	81	GND8
26	VCCIO2	93	GND9	93	GND9
53	VCCIO3	104	GND10	104	GND10
65	VCCIO4	108	GND11	108	GND11
79	VCCIO5	120	GND12	120	GND12
92	VCCIO6	130	GND13	130	GND13
105	VCCIO7	141	GND14	141	GND14
132	VCCIO8	156	GND15	156	GND15
157	VCCIO9	163	GND16	163	GND16
172	VCCIO10	177	GND17	177	GND17
181	VCCIO11	180	GND18	180	GND18
184	VCCIO12	207	GND19	207	GND19

**OMNIBUS**

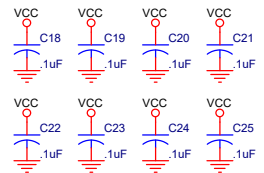
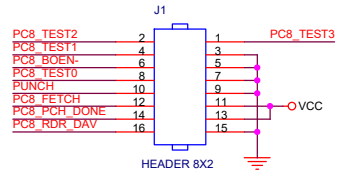
**TO PLOTTER**

**TO ADC**

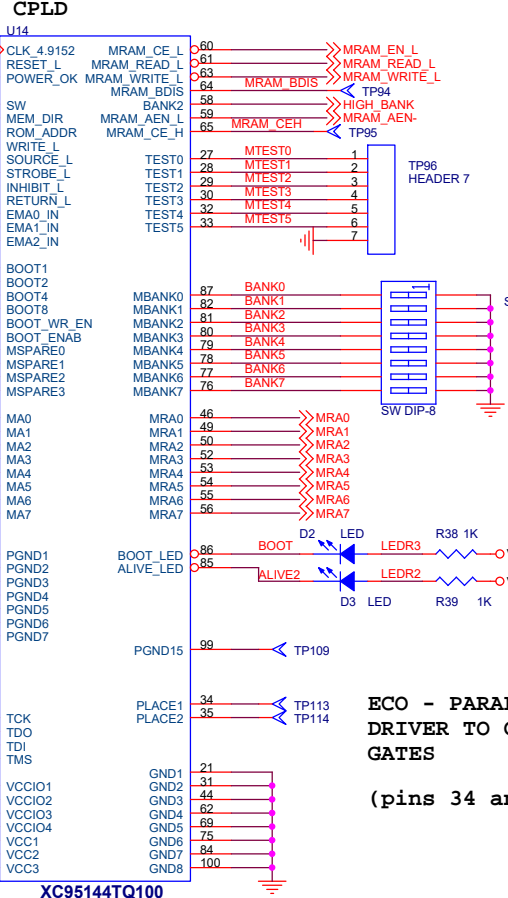
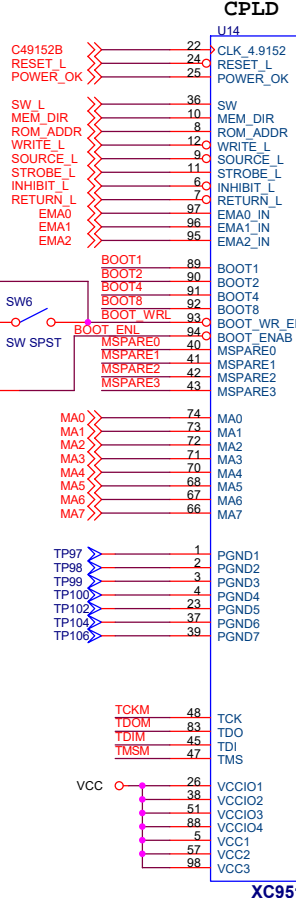
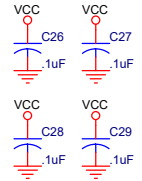
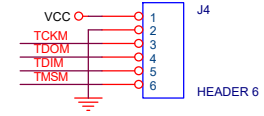
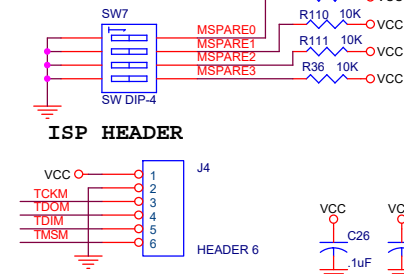
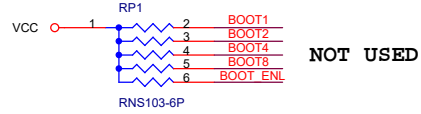
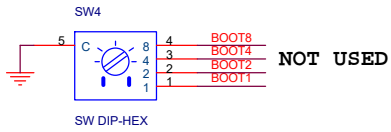
**TO UART**

**TO DISPLAY**

**TO HIGH SPEED R/P INTERFACE**

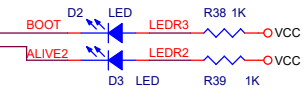


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SW5 = FIELD  
DISABLES

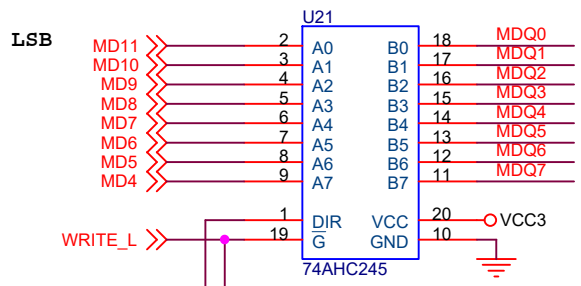
ON = DISABLE  
THAT FIELD



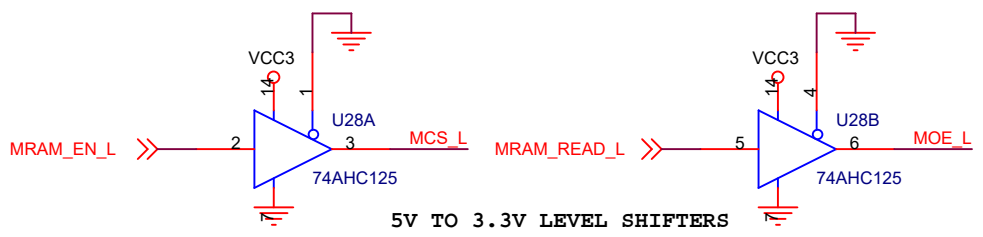
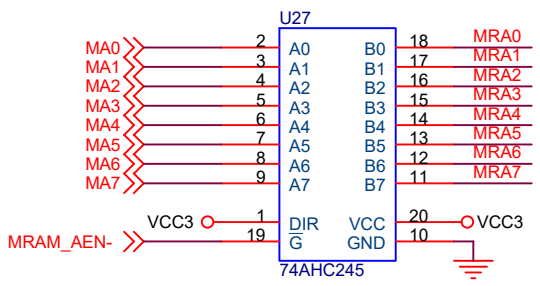
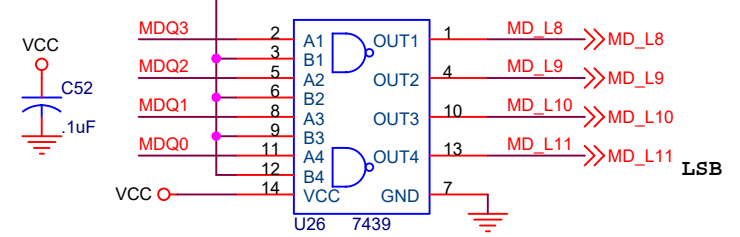
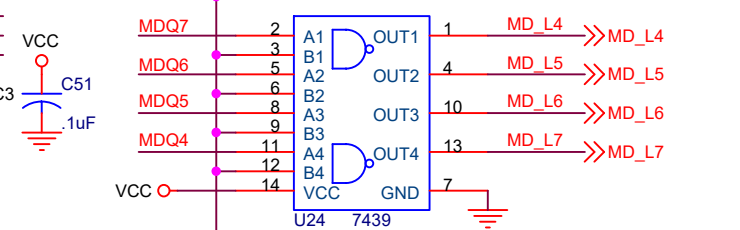
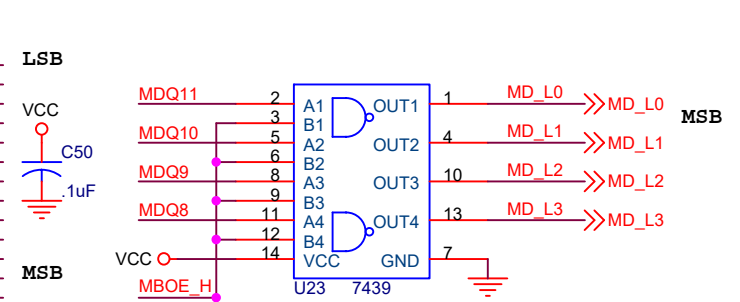
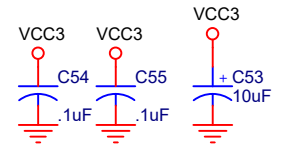
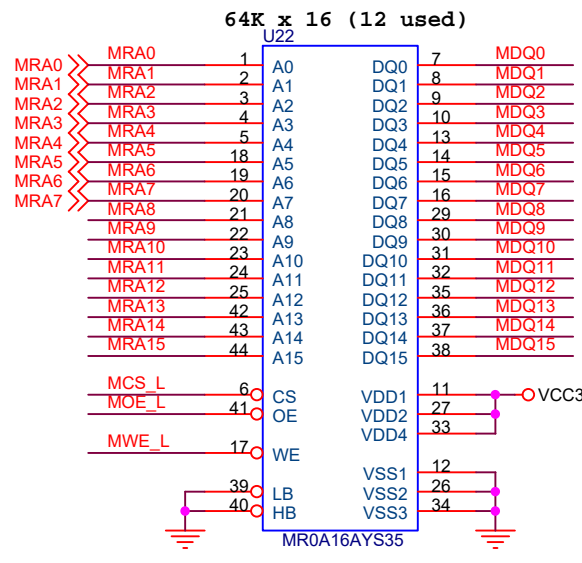
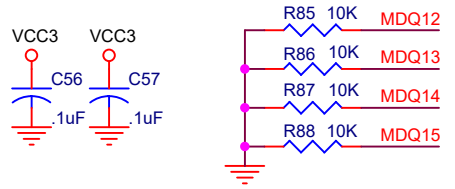
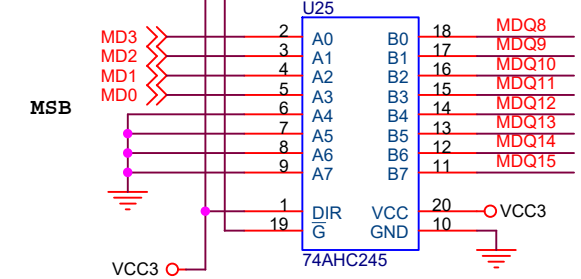
ECO - PARALLEL  
DRIVER TO OUTPUT  
GATES  
(pins 34 and 35 // to 7439 gates)

delete spare  
switch  
  
local reg to  
allow 95144  
or 95144xl

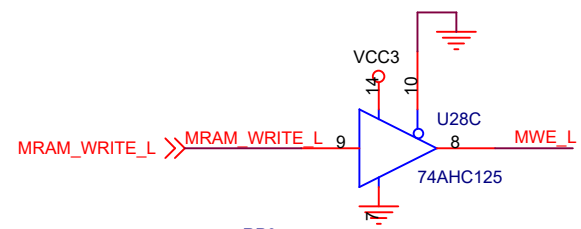
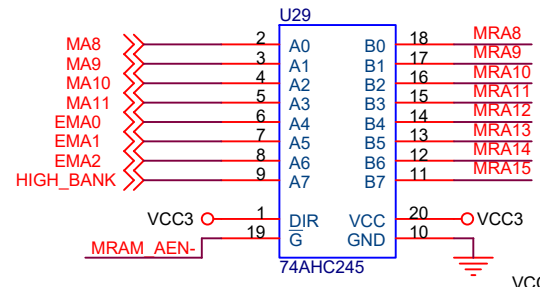
Title			PDP-8e Memory Expansion		
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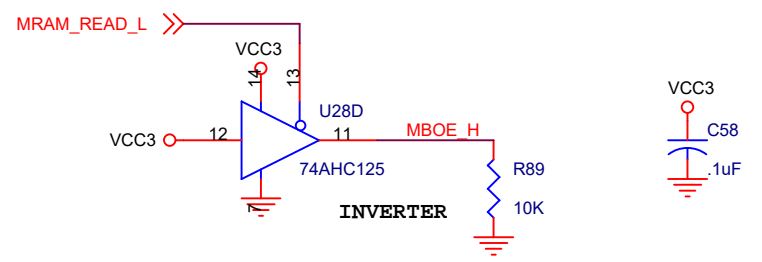
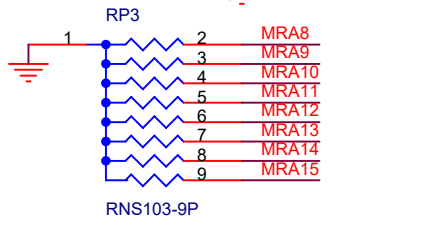
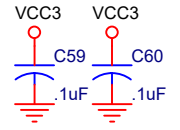
**5V TO 3.3V LEVEL SHIFTERS**



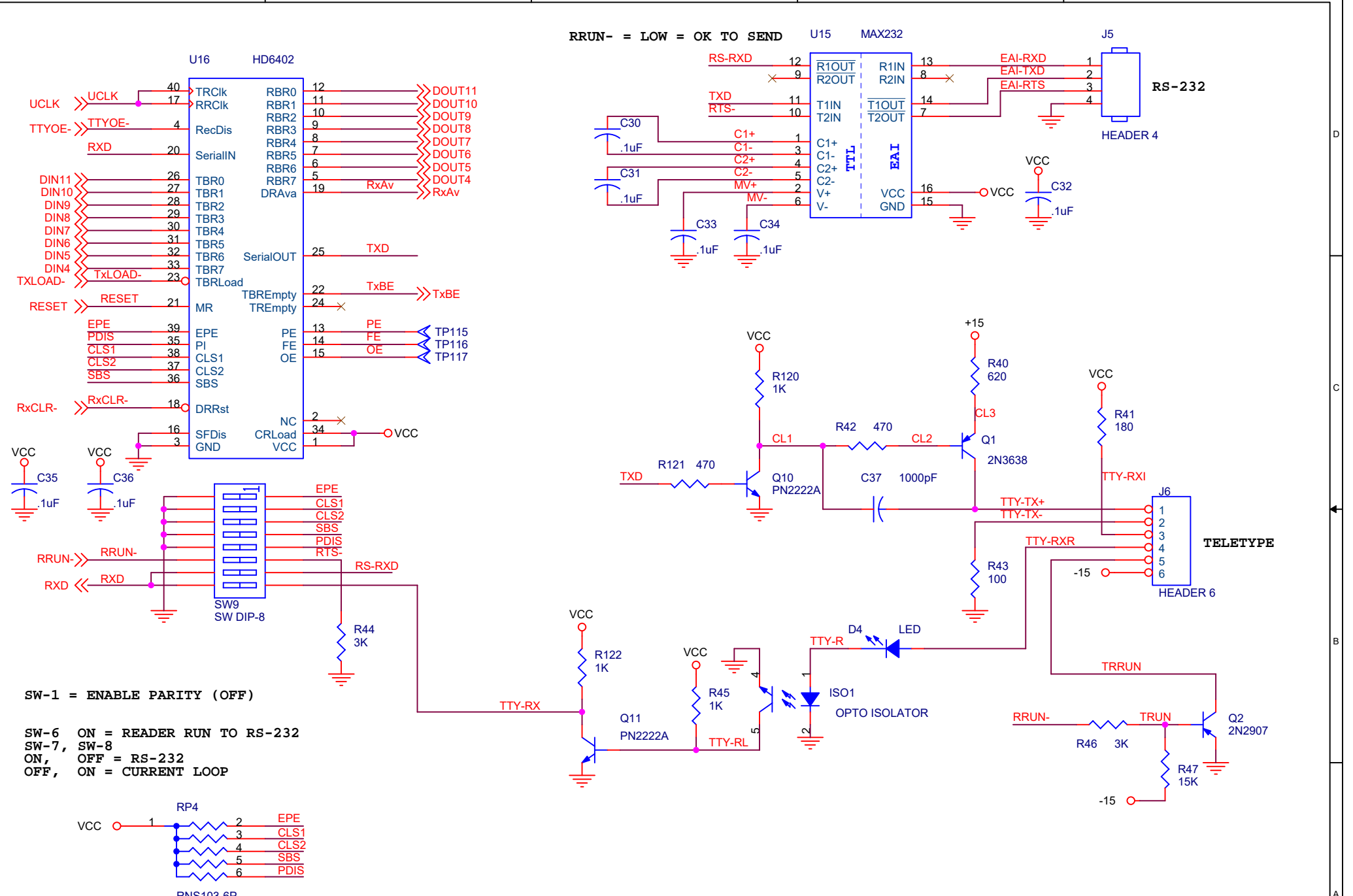
**5V TO 3.3V LEVEL SHIFTERS**



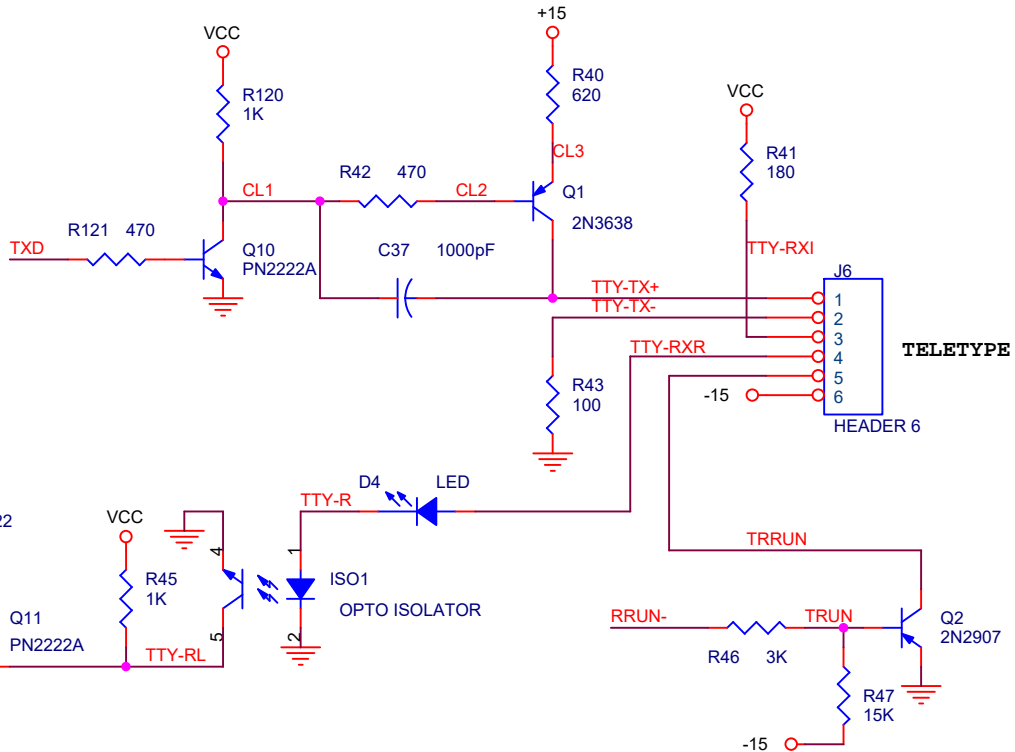
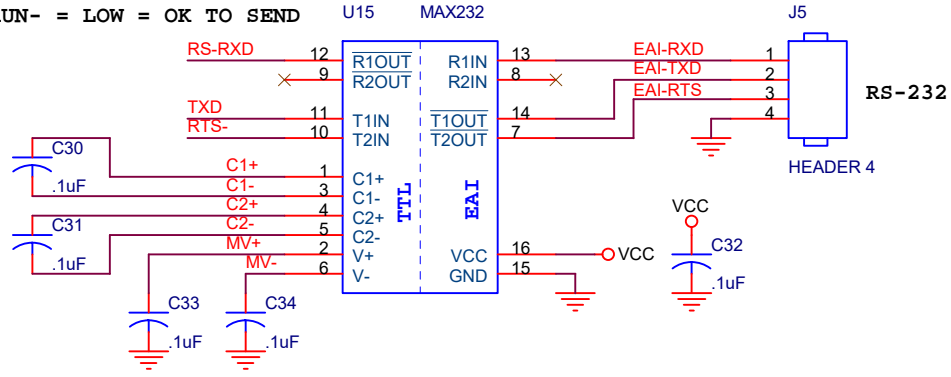
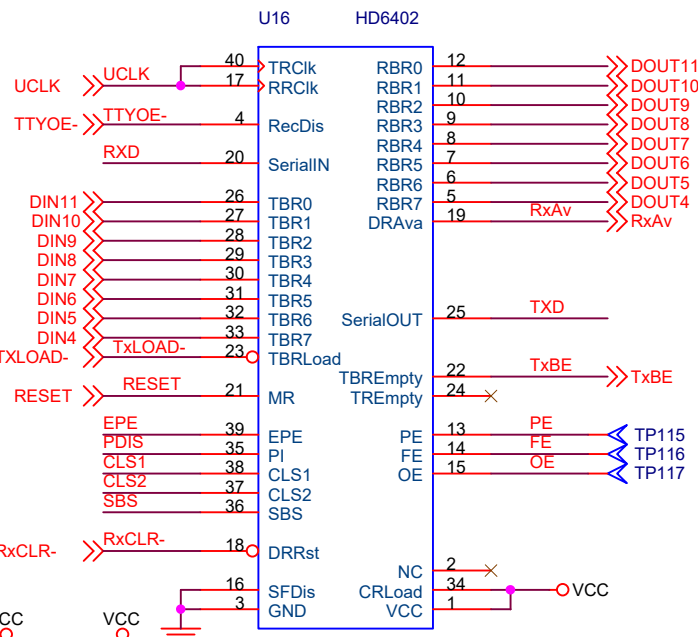
**5V TO 3.3V LEVEL SHIFTERS**



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<b>PDP-8e I/O Expansion</b>		
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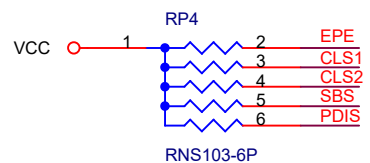


RRUN- = LOW = OK TO SEND

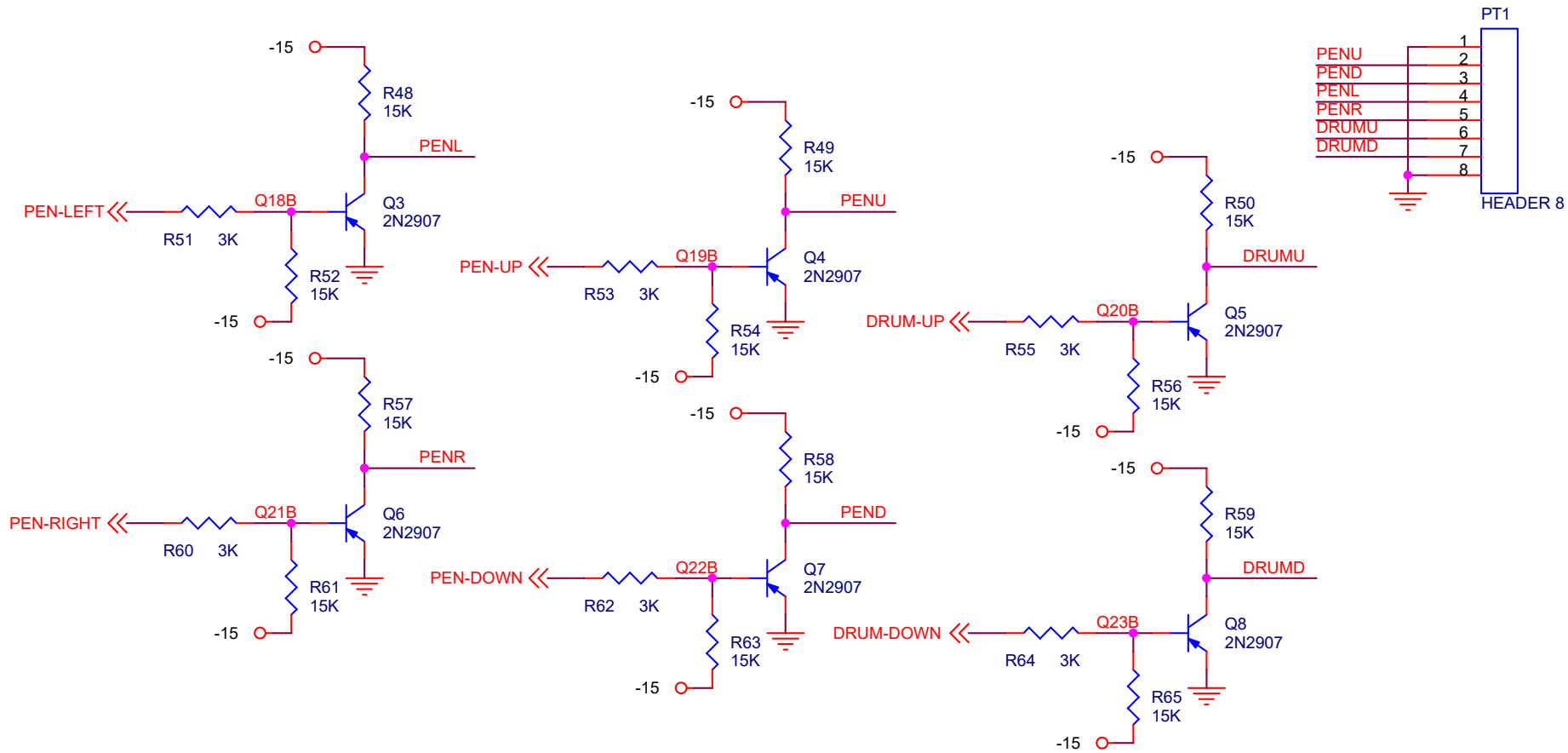


SW-1 = ENABLE PARITY (OFF)

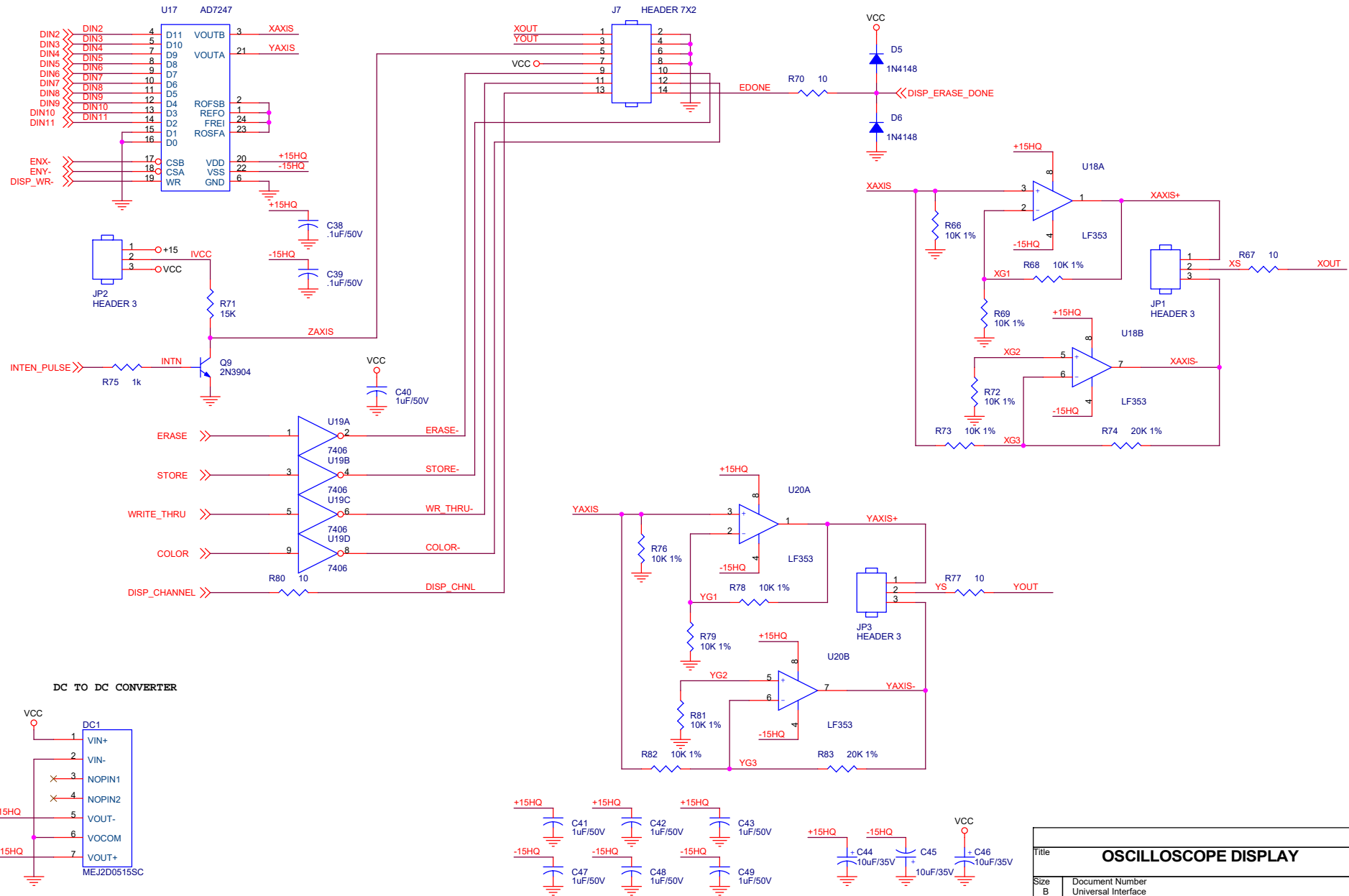
SW-6 ON = READER RUN TO RS-232  
 SW-7, SW-8 ON, OFF = RS-232  
 OFF, ON = CURRENT LOOP



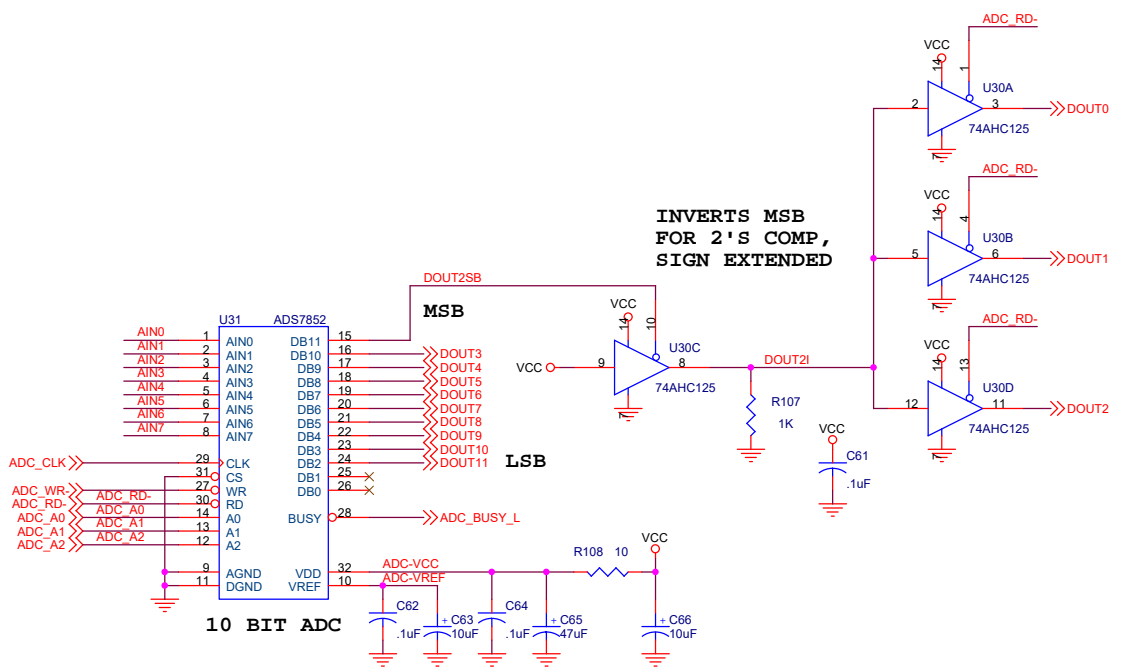
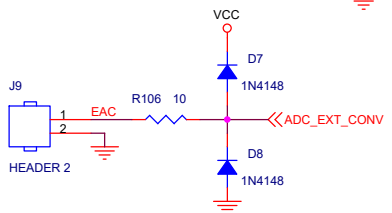
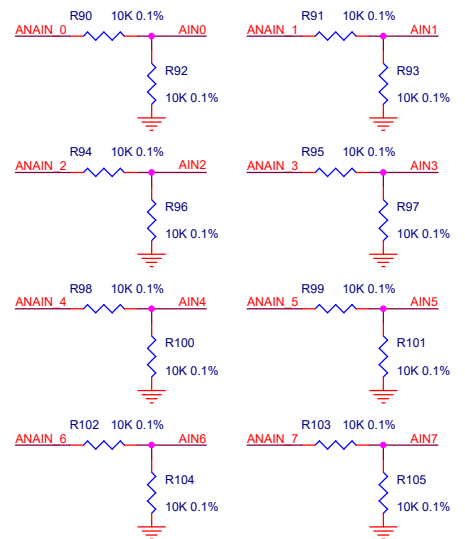
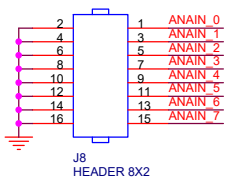
Title			<b>PDP-8s UART</b>		
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Custom					
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Title		
<b>Calcomp Plotter Interface</b>		
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<b>LAB8/E AD8-EA</b>		
<b>PDP-8e ADC</b>		
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