8001 DATA RECORDER

8010 INTERPRETING DATA RECORDER

Machine Manual

7545

9583-00

MANUAL 7545	MACH. MANUAL CONTENTS	TYPE 8001 DR 8010 1DR	S/N 10825

PRODUCT TYPE or FEATURE	PCB LOC	PCB ASM INDEX or Supplement	DASH NO. TEXT	ERRATA†	MANUAL SECTION or Document
DR, 1DR					Design Level
		7547			Controlled Asm List
		8889			Change History
					SERVICE REFERENCE
		7548			Service Reference Contents
	C Box	7396	-07		Schem-Sollnoid Driver
	C Box	9592	-05		Schem. Input Driver
	O PNL	2306	-02		Schem. Operator Panel
	Mech	5586	-04		Schem Motor Amplifier
	C Box	6954	-02		Schem. Print Hammer Driver
					BASIC LOGIC
		7549			Basic Logic Contents
	1	9928	-00		Keyboard Translate
	2	5096	-06		Keyboard Data
	3	5098	-07		Keyboard Functions
	4	8878	-13	12898-01	Motion Control Pt.1
	5	8880	-03		Punch Control
	6	3282	-12		Mode Control
	7	7492	-06		Program Memory (4) (Basic)
	7	7490	-05		" " (8) (F-1042)
	8	12816	-01		Motion Control Pt.2
	9	5108	-06		Input Memory
	10	5110	-06		Read Control
	11	7440	-02		Program Revert
	11	7404	-04		" " & 51Ca
	12	10842	-00		Punch Data
	20	2012	-05		Maintenance
1 DR	16	5122	-06		Printer Memory
	17	5124	-01		Printer Drivers
F-1027	13	5116	-06		Punch/Read Check
F-1023, 1024	18	5126	-10		Self Check N:-Modulus 10,11
F-1021,1022,					
F-1025,1047	19	4110	-05		Interspersed
					INTERFACE LOGIC
F-1026, 1055		7550			Contents-DD 12 Bit Intf.
	14	5118	-04		DD 12 Bit Pt-1
	14	8818	-02		DD 12 Bit 50Ft. Pt-1
	15	5120	-06		DD 12 Bit Pt-2
	15	8820	-04		DD 12 Bit 50Ft Pt-2
F-1061		9126			Contents-DD 8 Bit Intf.
		15470			Contents-DD 8 Bit-Muli.Hole DetectIn
	14	8874	-02		DD 8 Bit Pt-1
	14	15878	-00		DD 8 Bit Mult.Hole Detect Pt.1
	15	8876	-05		DD 8 Bit Pt.2
RPQ 179-01					SUPPLEMENTS
F-1063		9868			10.Key Reverse Keyboard
F-1069		10918			Katakana
F-1070		12031			Data Reporter
F-1080		12285			CS-200 Mod 4
F-1090		12596			96/80 Col Card Converter
F-1122		14329			Local Communication Intf.
F-1089		13366			Accounting
					WIRING REFERENCE
		7552			Wiring Reference Contents

† When indicated, errata follow this page

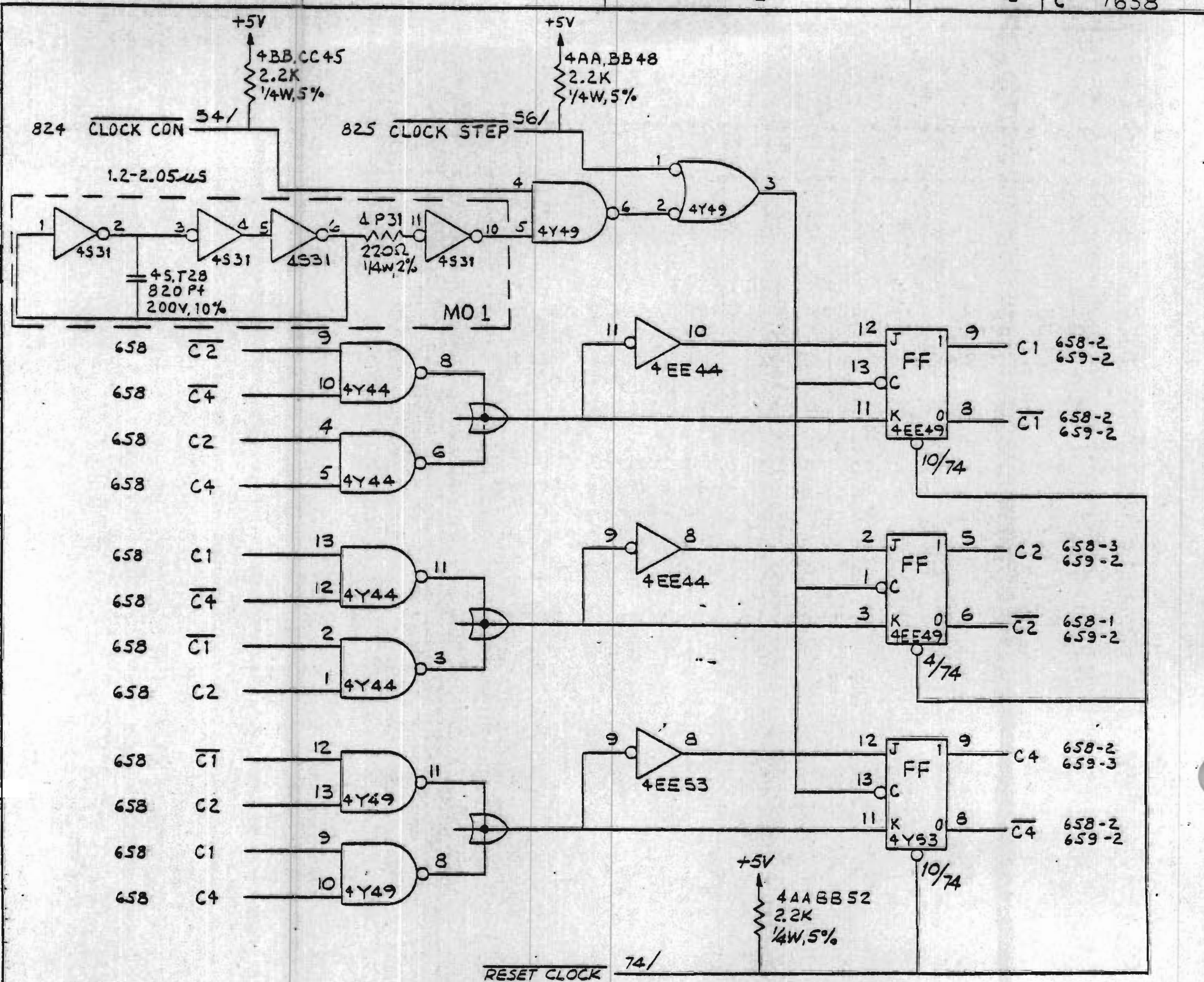
7545-A-4

LOGIC/SCHEMATIC INDEX

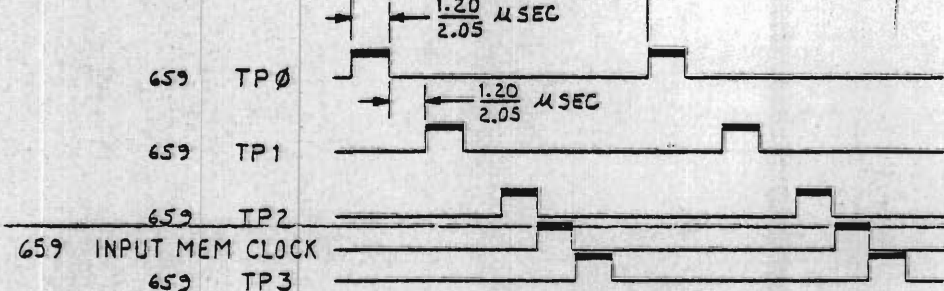
MANUAL	TYPE/FEATURE	
7545	8001/8010 DR 80 Col	PWB 12897-01
902193	8010-11 DR 360	ASM 12898-01

DOCUMENT	SH	RV	DESCRIPTION
			LOCATION: 4
			MOTION CONTROL PT-1
7658		-F	Clock Oscillator/Counter
7659		-F	Clock Output Gating
16660		--	Punch Sprocket
7661		-C	Card Tracking Flip Flops
7662		-D	Stacker Jam Detection
7663		-E	Motor Jam Check
7664		-E	General Clear
7665		-F	Position Control Flip Flops
7666		-G	Position Pulse Generation
16667		--	Position Difference
7668		-F	Position Sensor
7669		-G	Position Motor Control
12898	02	--	Schematic - Motion Control Pt-1
12898	03	--	"

C 7658



	C1	C2	C4
TPØ	0	0	0
	1	0	0
TP1	1	1	0
	0	1	0
TP2	0	1	1
	1	1	1
TP3	1	0	1
	0	0	1



CLOCK OUTPUT TIMING

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
D	8878-09	8877-06	2342-4P	
C	8878-08	8877-06	2342-2P	
B	8878-[illegible]	8877-06	1914-4C	
A	8878-0[illegible]	8877-00	1626-2C	
-	5100-01	5099-01	1421-2C	

REVISIONS

E	12898-01	12897-01	5767-1C	
C	8878-09	8877-09	2342-5C	

DECISION DATA COMPUTER CORP

DRN J. AILESI 4/18/72

CHKD 5-8-72

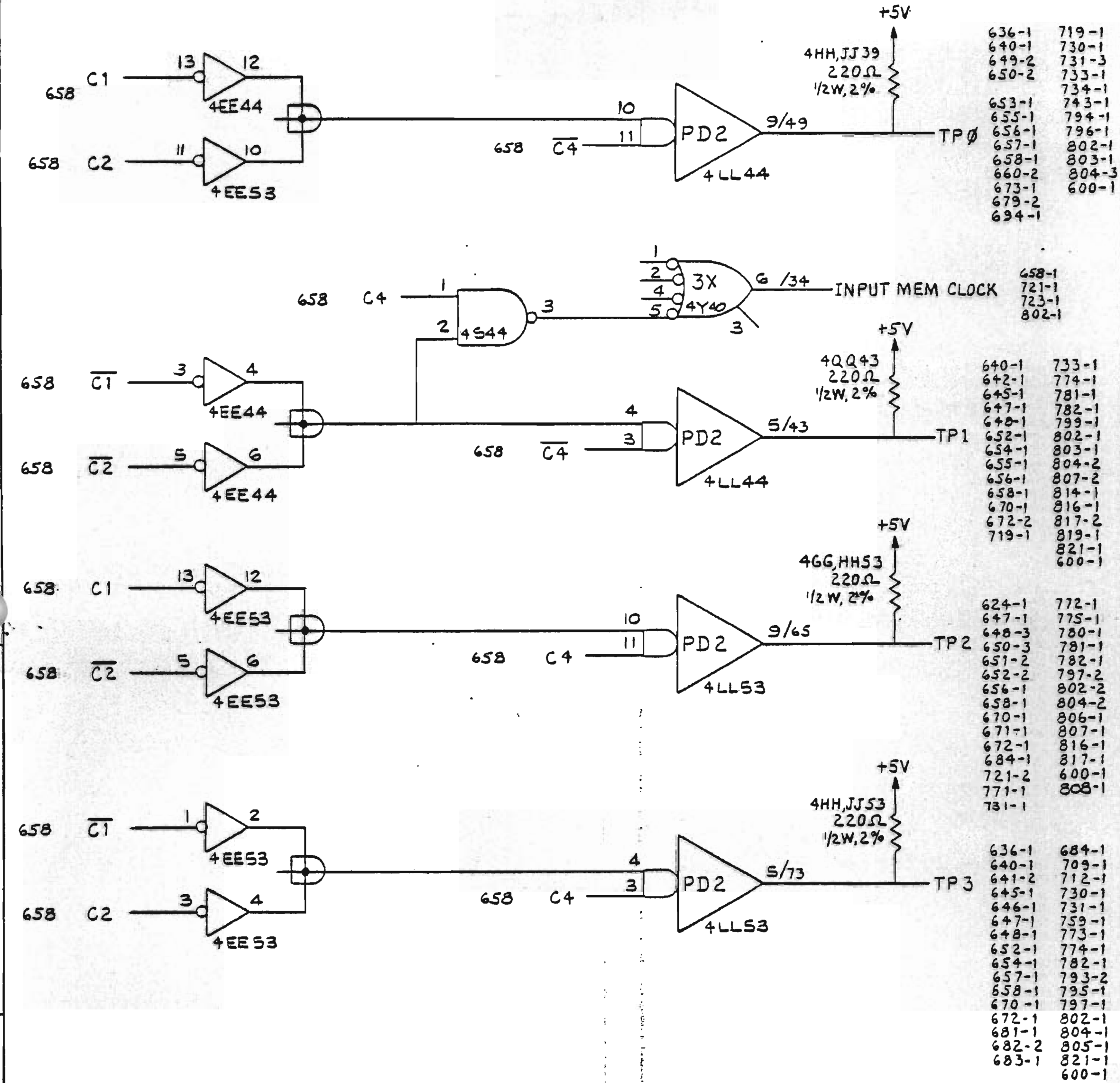
TITLE CLOCK OSCILLATOR & COUNTER

DWG NO. C 7658

DASH NO. -00

SH 1 OF 1

C 7659



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	8878-06	8877-06	1914-6C	[illegible]
B	8878-00	8877-00	1900-2P	[illegible]
A	8878-00	8877-00	1684-2C	[illegible]
-	5100-01	5099-01	1421-2C	[illegible]

REVISIONS

F	12898-01	12897-01	5767-1C	[illegible]
E	5109-06	5107-05	4536-1P	[illegible]
D	8878-06	8877-06	2681-2P	[illegible]

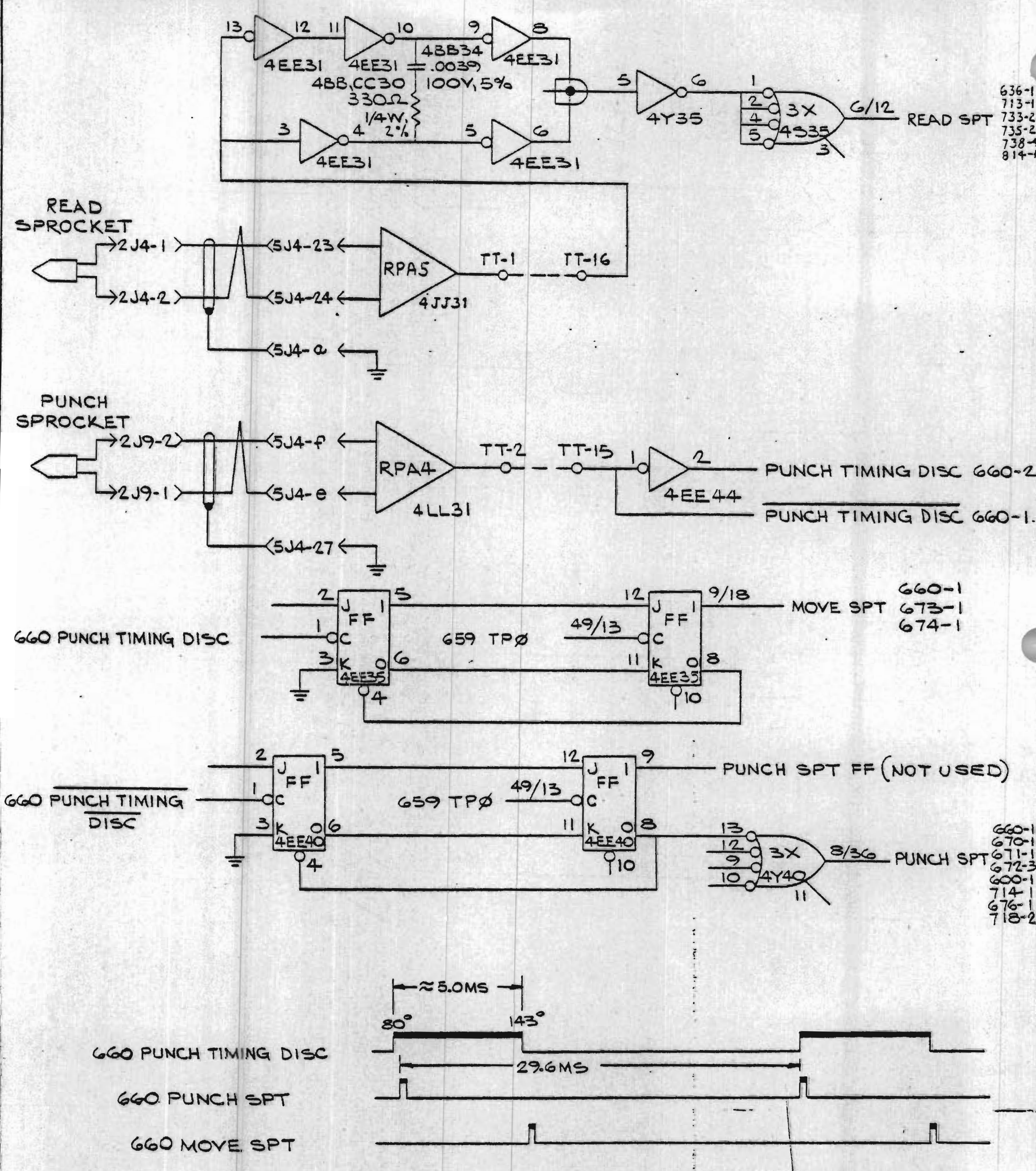
DECISION DATA COMPUTER CORP.

DRN J. ALESI 4/18/72 ENG [illegible] 10/19/72
CHK [illegible] 5/3/72 MFG

TITLE CLOCK OUTPUT GATING

DWG NO. C 7659 DASH NO. -00 SH 1 OF 1

C 16660



DECISION DATA COMPUTER CORP

REV	COMP ASM	ARTWORK	ECN	APP'VD-DATE
–	12898-01	12897-01	5767-1C	11 DEC

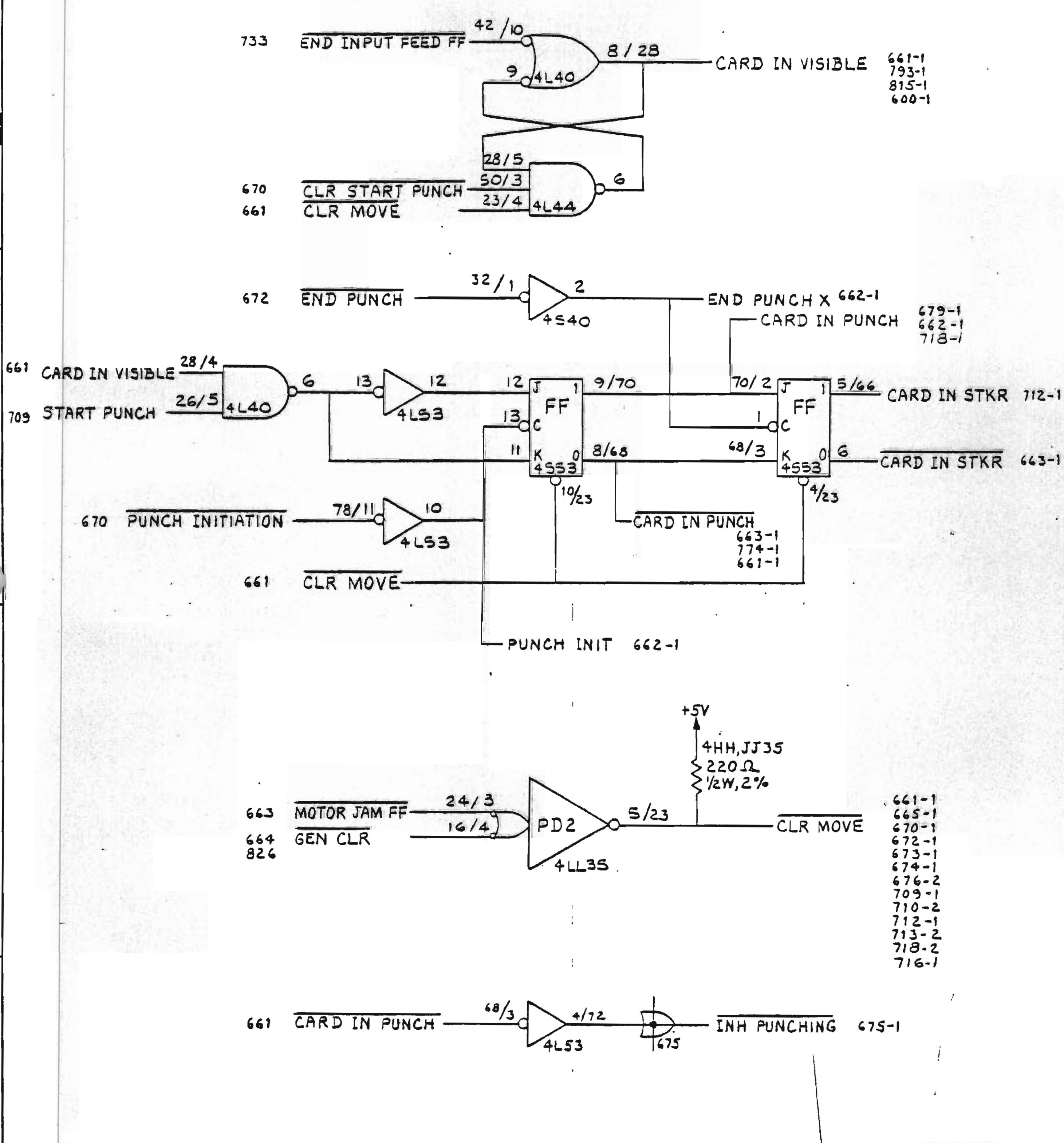
REVISIONS

DRN J. ALESI 3 NOV 79

TITLE PUNCH SPROCKET

DWG NO. C 16660 DASH NO. -00 SH 1 OF 1

C 7661



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	12898-01	12897-01	5767-1C	[illegible]
B	8878-09	8877-09	2342-5C	[illegible]
A	8878-CC	8877-CC	684-3C	[illegible]
-	5100-01	5099-01	1421-2C	[illegible]

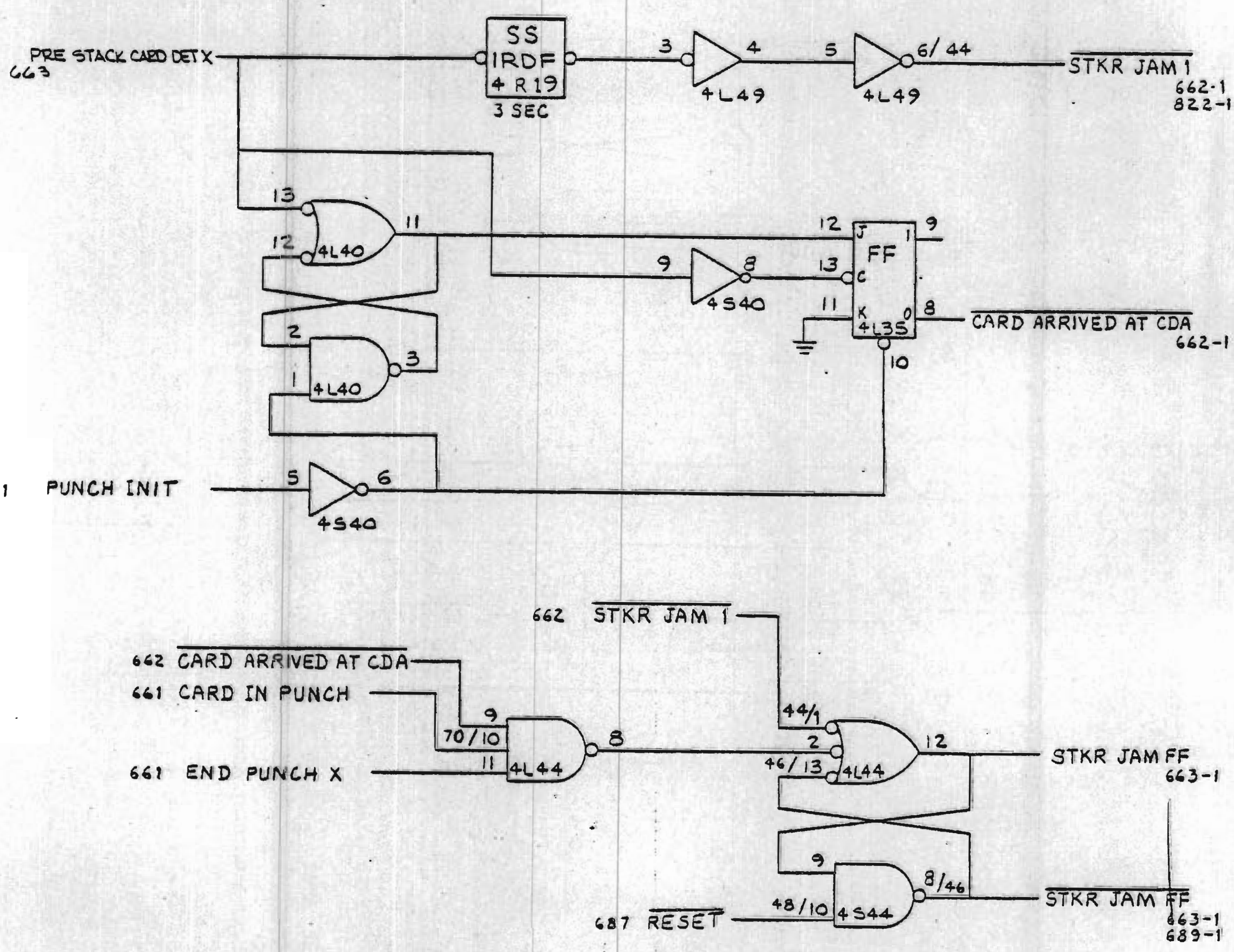
REVISIONS

DECISION DATA COMPUTER CORP.

DRN J. ALESI 4/17/72 ENG [illegible] 10/14/72
CHKD [illegible] 5-13-72 MFG [illegible]

TITLE CARD TRACKING FLIP-FLOPS

DWG NO. C 7661 DASH NO. -00
SH 1 OF 1



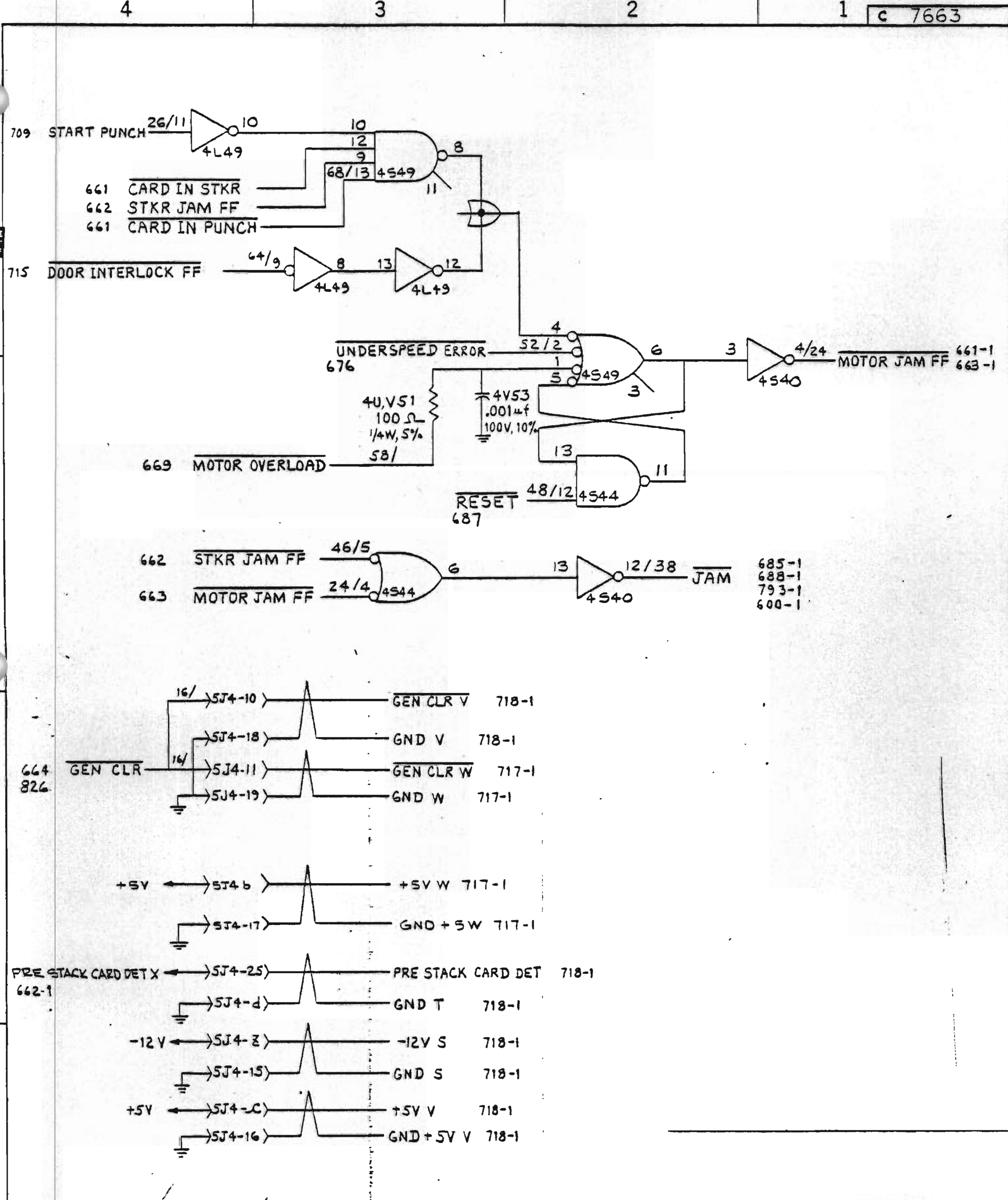
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
D	12898-01	12897-01	5767-1C	
C	8878-06	8877-06	1914-6C	
B	8878-02	8877-00	1914-2P	
A	8878-00	8877-00	1684-3C	
-	5100-01	5099-01	1421-2C	

REVISIONS

DECISION DATA COMPUTER CORP

DRN
CHK
TITLE
STACKER JAM DETECTION

DWG NO. C 7662
DASH NO. -00
SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
E	12898-01	12897-01	5767-1C	
D	8878-06	8877-06	1914-6C	
C	8878-04	8877-00	1914-4P	
B	8878-02	8877-00	1914-2P	
A	8878-00	8877-00	1684-3C	
-	5100-01	5099-01	1421-2C	

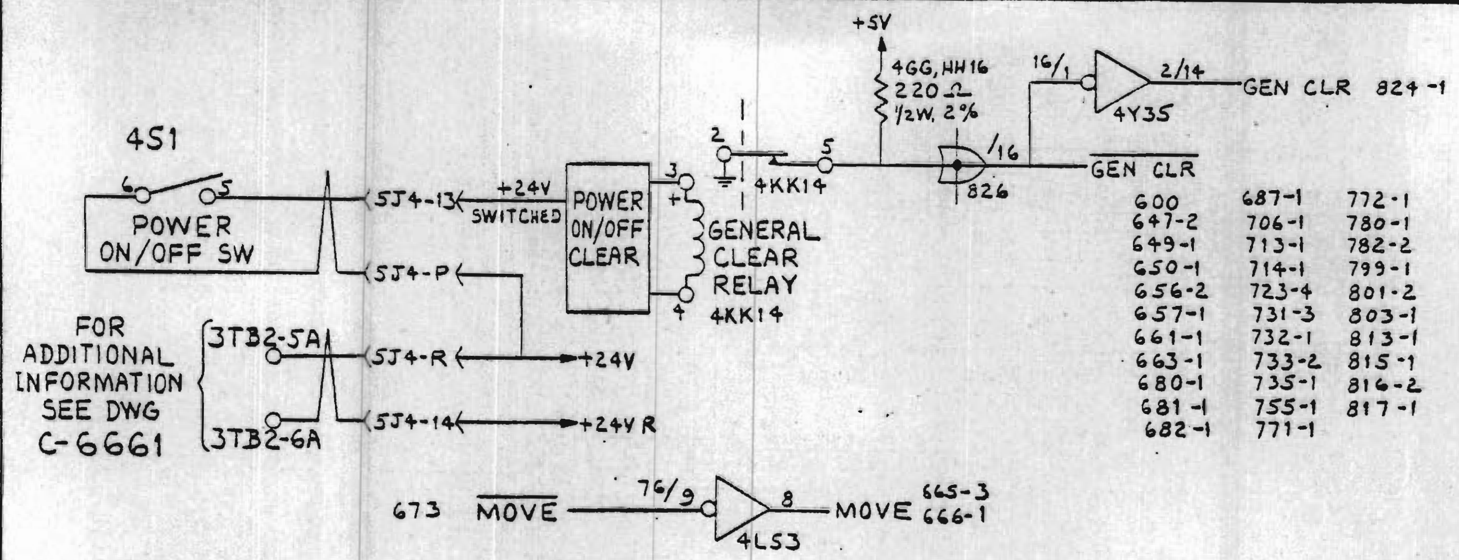
REVISIONS

DECISION DATA COMPUTER CORP.

DRN J. ALESI 4/15/72 ENG 10/19/72
CHK 5/13/72 MFG

TITLE MOTOR JAM CHECK

DWG NO. C 7663 DASH NO. -00 SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
E	12898-01	12897-01	5767-1C	
D	8878-09	8877-06	2342-4P	
C	8878-06	8877-06	1914-6C	
B	8878-02	8877-00	1914-2P	
A	8878-00	8877-00	1484-3C	
-	5100-01	5099-01	1421-2C	

REVISIONS

DECISION DATA COMPUTER COR

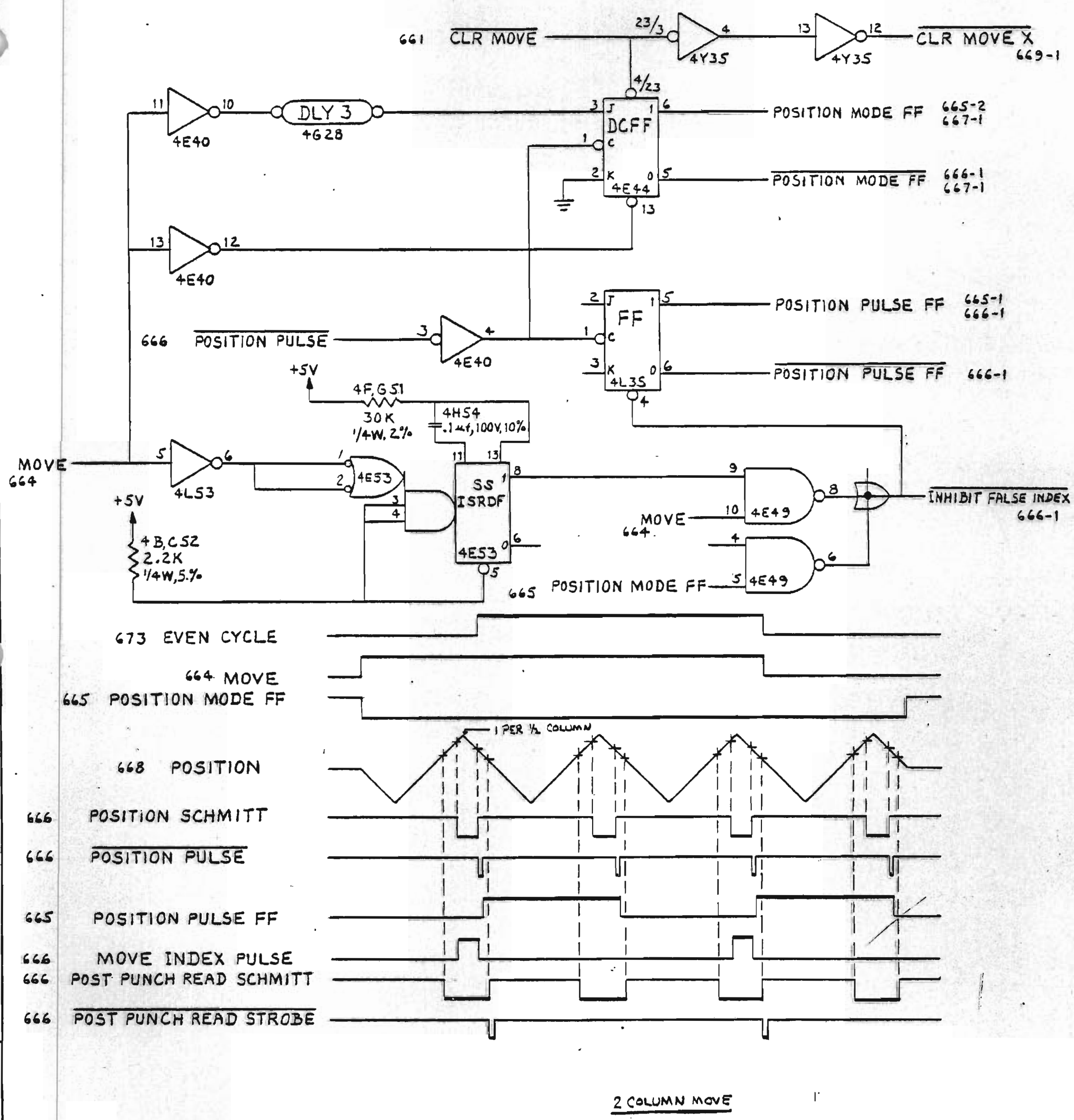
DRN J. AL-ESI 4/13/72 ENG [signature] 10/19/72

CHK C. [signature] 9-13-72 MFG

TITLE GENERAL CLEAR

DWG NO. C 7664 DASH NO. -00 SH 1 OF 1

C 7665



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
F	12898-01	12897-01	5767-1C	[illegible]
E	8878-06	8877-06	1914-6C	[illegible]
D	8878-05	8877-00	1914-5P	[illegible]
C	8878-03	8877-00	1914-3P REV A	[illegible]
B	8878-02	8877-00	1914-2P	[illegible] 3-26-73
A	8878-00	8877-00	1684-3C	4-3-73
-	5100-01	5099-01	1421-2C	[illegible]

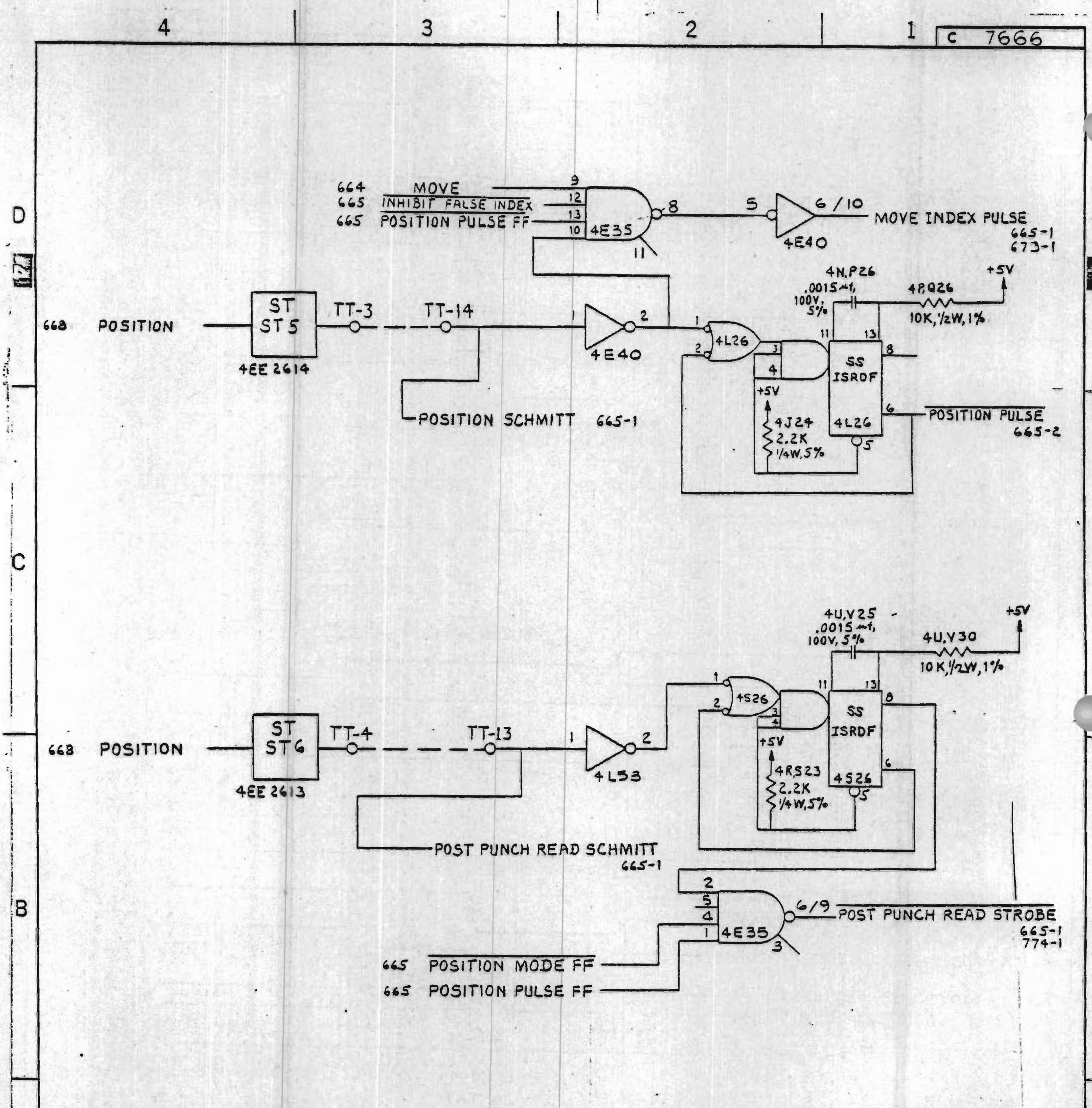
REVISIONS

DECISION DATA COMPUTER CORP.

DRN J. ALESI 10/2/72	ENG [illegible]
CHK C. SCAF[illegible] 5/13/[illegible]	MFG [illegible]

TITLE POSITION CONTROL FLIP-FLOPS

DWG NO. C 7665 — DASH NO. -00 — SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	8878-05	8877-00	1914-5P	9-4-73
B	8878-03	8877-00	1914-3P REV A	4-5-73
A	8878-00	8877-00	1684-2C	[illegible]
-	5100-01	5099-01	1421-2C	[illegible]

REVISIONS

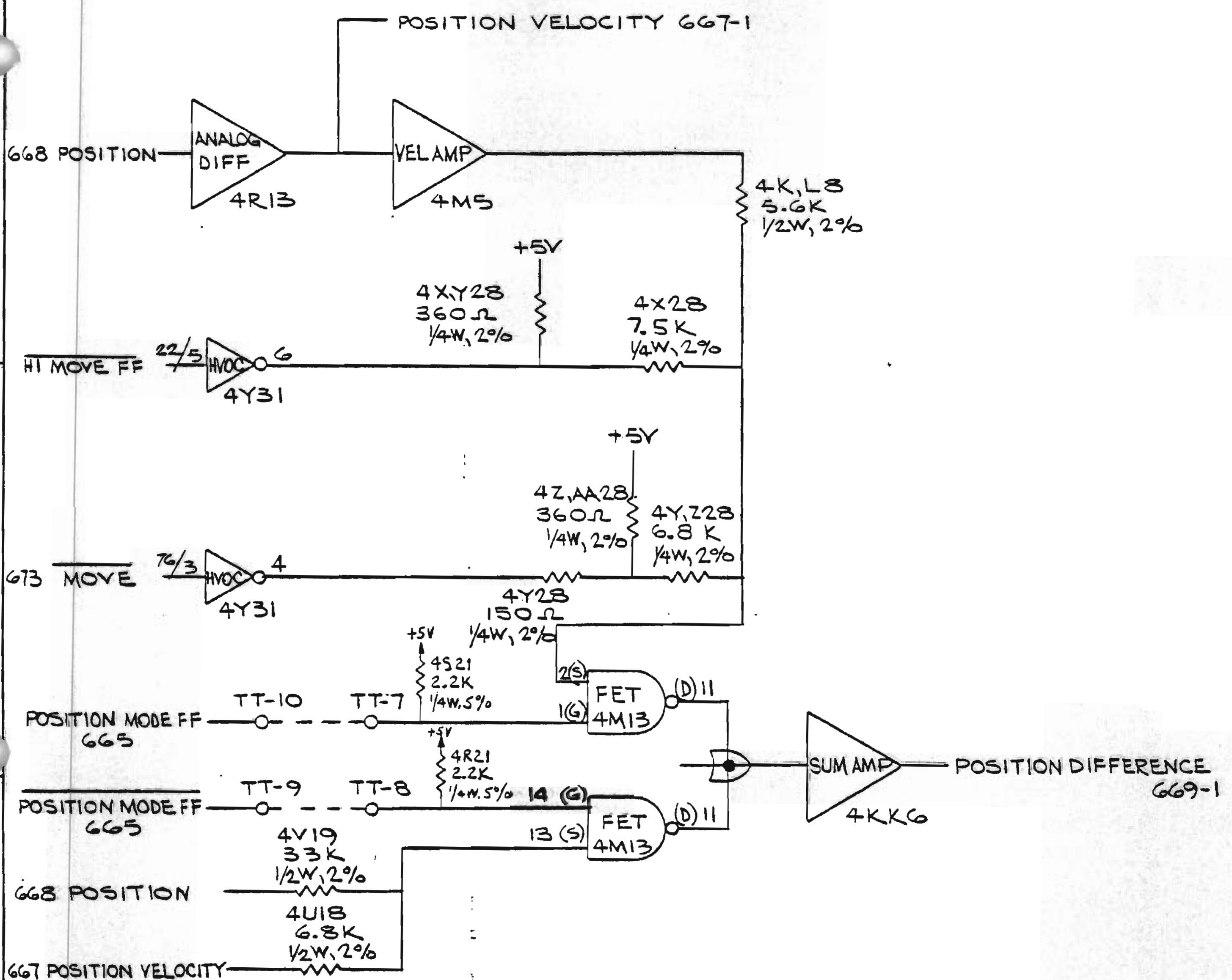
G	12898-01	12897-01	5767-1C	[illegible]
F	8878-13	8877-10	4455-1C	[illegible]
E	8878-09	8877-09	2342-5C	
D	8878-06	8877-06	1914-6C	

DECISION DATA COMPUTER CORP

DRN J. ALESI 4/17/72 ENG [illegible]
CHKD [illegible] 5-13-72 MFG [illegible]

TITLE POSITION PULSE GENERATION

DWG NO. C 7666 DASH NO. -00 SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
–	12898-01	12897-01	5767-1C	[illegible] 11 DEC

REVISIONS

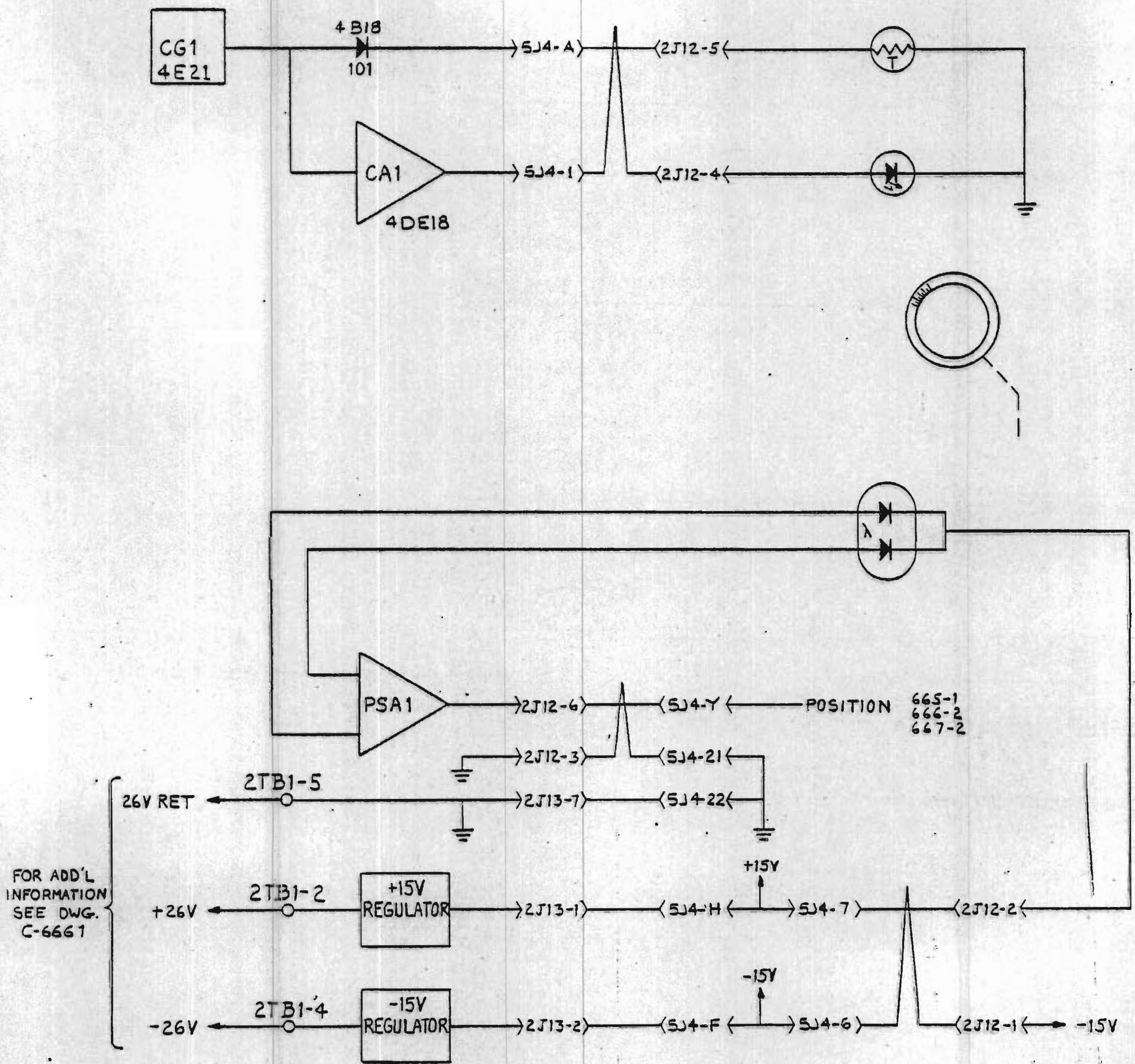
DECISION DATA COMPUTER CORP.

DRN J. ALESI 8 Nov 78 | ENG [illegible] 11 DEC 78
CHK [illegible] | MFG [illegible]

TITLE: POSITION DIFFERENCE

DWG NO. C 16667 | DASH NO. -00 | SH 1 OF 1

C 7668



REV	COMP ASM	ARTWORK	ECN	APPD-DATE
F	12898-01	12897-01	5767-1C	[illegible]
E	8878-13	8877-10	4455-1C	[illegible]
D	8878-06	8877-06	1914-6C	[illegible]
C	8878-05	8877-00	1914-5P	[illegible]
B	8878-03	8877-00	914-3P	[illegible]
A	8878-00	8877-00	1684-2C	[illegible]
-	5100-01	5099-01	1421-2C	[illegible]

REVISIONS

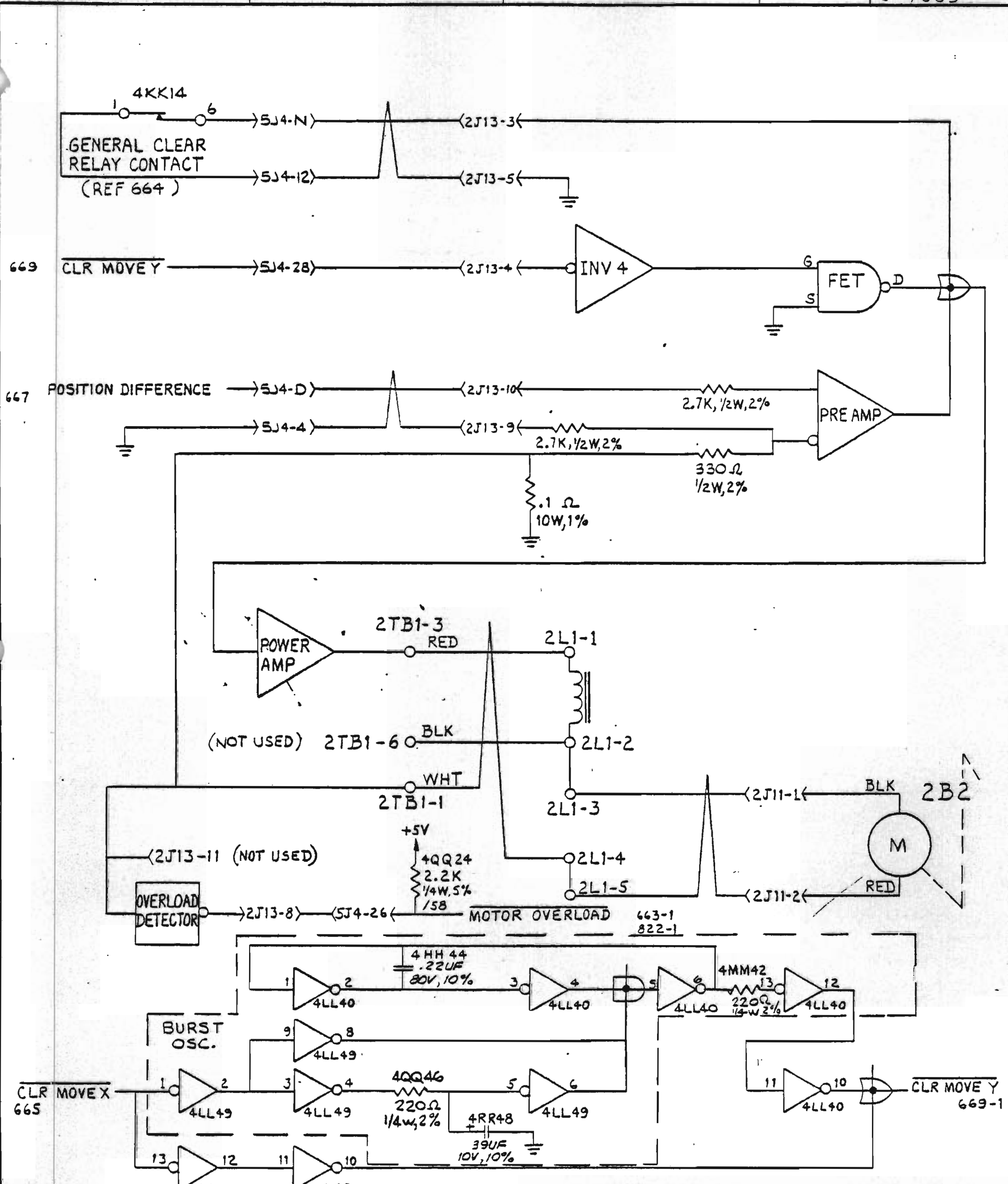
DECISION DATA COMPUTER CORP

DRN J. ALESI 4/17/72 ENG [illegible]
CHK C. [illegible] 5-13-72 MFG [illegible]

TITLE
POSITION SENSOR

DWG NO. C 7668 DASH NO. -00 SH 1 OF 1

C 7669



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
G	12898-01	12897-01	[illegible]	[illegible]
F	8878-09	8877-09	2342-5C	[illegible]
E	8878-09	8877-06	2342-4P	[illegible]
D	8878-07	8877-06	2342-1P	[illegible]
C	8878-06	8877-06	1914-6C	[illegible]
B	8878-04	8877-00	1914-4D	[illegible]
A	8878-00	8877-00	1684-2C	[illegible]
–	5100-01	5099-01	1421-2C	[illegible]

REVISIONS

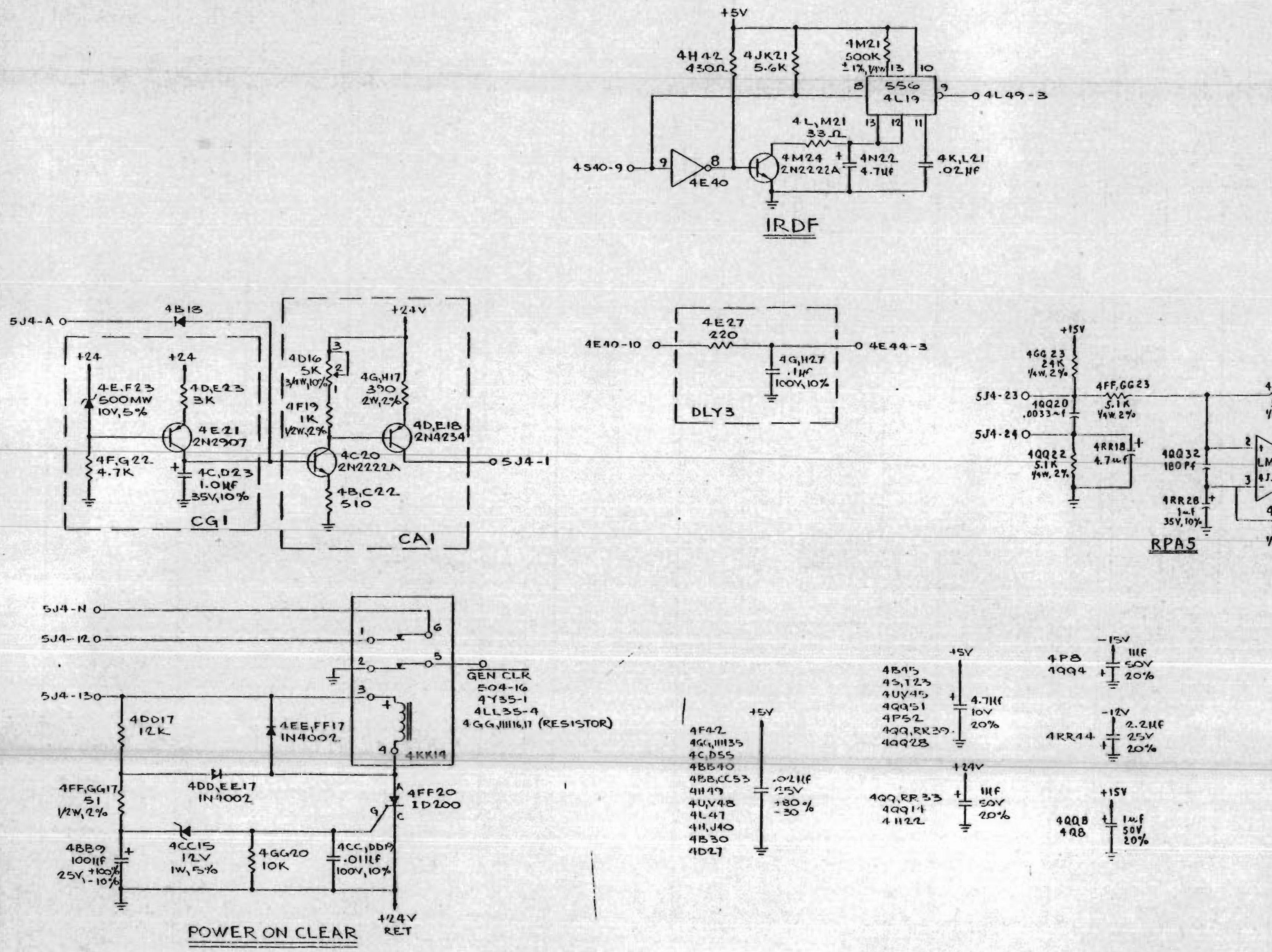
DECISION DATA COMPUTER CORP.

DRN J. ALESI 4/17/72 | ENG [illegible]
CHK [illegible] 5-13-72 | MFG [illegible]

TITLE POSITION MOTOR CONTROL

DWG NO. C 7669 | DASH NO. -00 | SH 1 OF 1

REV	DESCRIPTION	APPROVED
-	RELEASE / ECN # 5767-1C	[illegible]



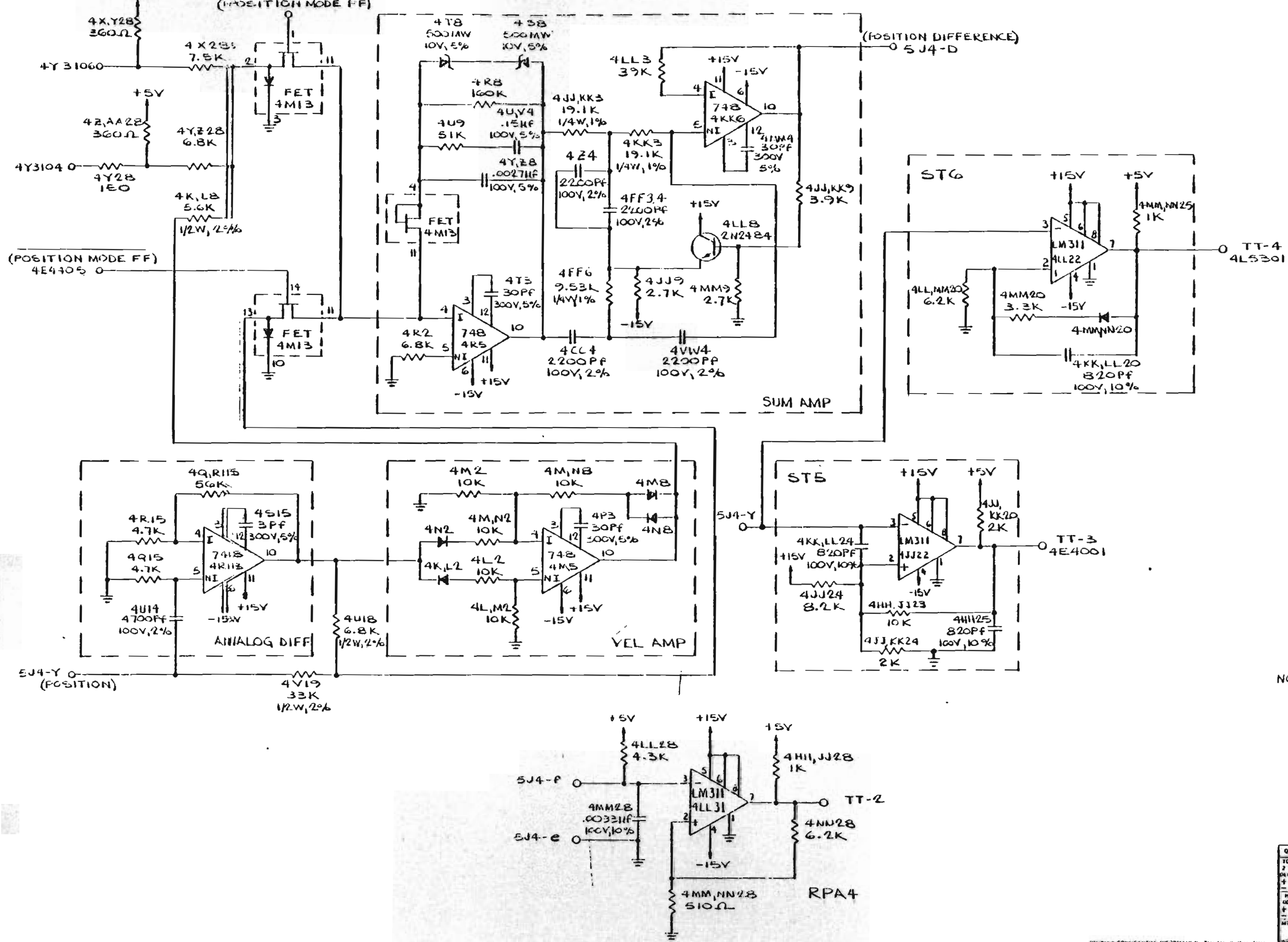
NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.
B. ALL DIODES ARE TYPE 101.

DECISION DATA COMPUTER CORP.

COMPONENT ASSEMBLY
MOTION CONTROL, PART 1

DWG NO D 12898

DASH NO 01



NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. ALL DIODES ARE TYPE 101.
B. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.

DECISION DATA COMPUTER CORP.

COMPONENT ASSEMBLY
MOTION CONTROL PART I

D 12898

SH 3 OF

CONTROLLED ASSEMBLIES LIST

TYPE NUMBER/NAME: 8001, 8010 DATA RECORDER, DD

SERIAL NUMBER

REFERENCE DRAWING NUMBER	TITLE	ASSEMBLY DASH NUMBER						
	MACHINE DESIGN LEVEL	A	A	B	B	B	B	B
9604	Mechanism Asm. 8001, 8035	X	X	00	00	00	00	00
8678	I.D.R. Mechanism	00	00	X	X	X	X	X
9561	Mechanism Asm. 8010, 8045	X	X	00	00	00	00	00
8806	Punch Mechanism Asm, 80 Column	00	00	02	02	02	02	02
8806	Punch Mechanism Asm, 80/51 Column	01	01	03	03	03	03	03
8807	Read Mech. Asm.	00	00	00	00	00	00	00
6306	Printer Asm.	00	00	00	00	00	00	00
8679	Sensor Head Asm.	00	00	00	00	00	00	00
2470	Sensor Asm.	00	00	00	00	00	00	00
2601	Keyboard Asm.	22	22	22	22	22	22	22
6999	D.R. Top Cover Asm.	00	00	X	X	X	X	X
6999	I.D.R. Top Cover Asm.	01	01	X	X	X	X	X
9735	D.R. Top Cover Asm.	X	X	00	00	00	00	00
9735	I.D.R. Top Cover	X	X	01	01	01	01	01
3189	Motor Control Asm, Wired	00	00	00	00	00	00	00
2613	Power Supply 60 HZ	00	00	00	00	00	00	00
2696	Power Supply 50/60 HZ	00	00	00	00	00	00	00
8846	Back Plane Asm. Wired, Basic	01	01	X	X	X	X	X
9647	Back Plane Asm. Wired, Basic	X	X	02	02	02	02	02
8847	Back Plane Asm. Wired, DD	01	01	X	X	X	X	X
9648	Back Plane Asm. Wired, DD	X	X	02	02	02	02	02
5094	Keyboard Translate	03	03	04	04	X	X	X
9928	Keyboard Translate	X	X	X	X	00	00	00
5096	Keyboard Data	02	02	03	04	04	05	06
5098	Keyboard Functions	06	06	07	07	07	07	07
8878	Motion Control, Part 1	02	05	07	08	09	10	12
8880	Punch Control	01	01	02	03	03	03	03
3282	Mode Control	10	11	11	11	12	12	12
7492	Program Memory (4)	02	04	05	05	06	06	06
7490	Program Memory (8)	03	03	04	04	05	05	05
8882	Motion Control, Part 2	01	05	X	X	X	X	X
9594	Motion Control, Part 2	X	X	00	00	02	04	X
5108	Input Memory	04	04	05	05	05	05	06
5110	Read Control	03	03	04	04	04	06	06
7440	Program Revert	01	01	01	02	02	02	02
7404	Program Revert And 51 Column	X	X	04	04	04	04	04
5114	Punch Data	01	01	01	02	03	03	X
5116	Punch Check	03	05	05	06	06	06	07
	REVISION OF CONTROLLED ASM. LIST	-	A	B	C	D	E	F
	DATE OF E.C.N.	-	24Jul73	28Sep73	5Oct73	21Oct73	11Dec74	7APR78
	APPROVAL	-	RVK	RVK	RVK	RVK	RVK	RPP
	ENGINEERING CHANGE NOTICE	-	2125-3P	2125-5P	2125-7P	2125-8C	3853-1C	5467-1C

DWG. NO. A 7547 REV. F SH 1 OF 2

CONTROLLED ASSEMBLIES LIST

TYPE NUMBER/NAME: 8001, 8010 DATA RECORDER, DD

SERIAL NUMBER

REFERENCE DRAWING NUMBER	TITLE	ASSEMBLY DASH NUMBER						
	MACHINE DESIGN LEVEL	A	A	B	B	B	B	B
5118	DD Interface, Part 1	03	04	04	04	04	04	04
8818	DD Interface, 50 Ft., Part 1	00	01	01	01	01	01	02
5120	DD Interface, Part 2	03	04	04	05	05	07	07
8820	DD Interface, 50 Ft., Part 2	01	02	02	03	03	05	05
5122	Printer Memory	02	03	04	05	05	06	06
5124	Printer Drivers	01	01	01	01	01	01	01
5126	Self Check Mode 10 Or 11	02	03	04	06	08	10	10
4110	Interspersed	04	05	05	05	05	05	05
2012	Maintenance Board	05	05	05	05	05	05	05
5586	Motor Amplifier	03	03	03	03	04	04	04
6954	Print Hammer Driver 2	01	01	01	01	02	02	02
2306	Control Panel	02	02	02	02	02	02	02
7396	Solenoid Driver	04	04	04	05	06	06	07
8810	Input Driver	03	03					
9592	Input Driver			00	00	02	02	05
10842	Punch Data							00
12816	Motion Control Part 2							01
8876	DD Interface 8 Bit Pt. 2							05
15878	DD Interface 8 Bit Pt. 1							00

REVISION OF CONTROLLED ASM. LIST	–	A	B	C	D	E	F
DATE OF E.C.N.	-	24Jul73	28Sep73	5 Oct73	21Oct73	17Dec74	1Apr78
APPROVAL	'	RVK	RVK	RVK	RVK	RVK	R[illegible]
ENGINEERING CHANGE NOTICE	-	2125-3P	2125-5P	2125-7P	2125-8C	3853-1C	5467-1C

DWG. NO. **A** 7547 REV. F SH 2 OF 2

E90-112

CHANGE HISTORY

TYPE NUMBER/NAME:	DESIGN LEVEL	SERIAL NUMBER

E.C.N., F.C.I., I.C.O., OPTIONAL FEATURE, ETC.	INSTALLED BY	DATE	E.C.N., F.C.I., I.C.O., OPTIONAL FEATURE, ETC.	INSTALLED BY	DATE	E.C.N., F.C.I., I.C.O., OPTIONAL FEATURE, ETC.	INSTALLED BY	DATE

CHANGE HISTORY

TYPE NUMBER/NAME:	DESIGN LEVEL	SERIAL NUMBER

E.C.N., F.C.I., I.C.O., OPTIONAL FEATURE, ETC.	INSTALLED BY	DATE	E.C.N., F.C.I., I.C.O., OPTIONAL FEATURE, ETC.	INSTALLED BY	DATE	E.C.N., F.C.I., I.C.O., OPTIONAL FEATURE, ETC.	INSTALLED BY	DATE

SERVICE REFERENCE CONTENTS

TYPE NUMBER/NAME: 8001, 8010 DATA RECORDER, DD

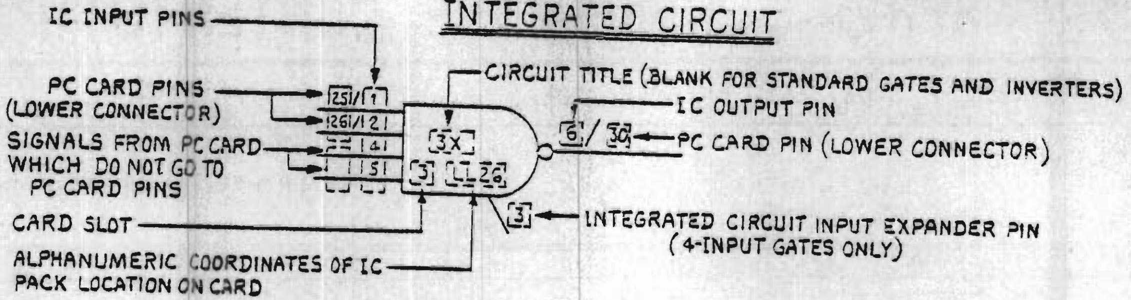
REFERENCE DRAWING NUMBER	TITLE	REFERENCE DRAWING REVISIONS			
2683	Logic Symbol Reference Key	A	A	A	A
2684	I/C Inverters, Gates & Delays	C	C	C	C
8655	I/C Memory	-	-	-	-
3829	I/C FF, Complex Functions	D	D	D	D
3830	Discrete Delay Circuits	A	A	A	A
9612	Discrete Low-Level Amplifiers	-	-	A	A
9613	Discrete Driver Circuits	-	-	-	-
9614	Misc. Discrete Circuits, Pt. 1	-	-	-	-
9615	Misc. Discrete Circuits, Pt. 2	-	-	-	-
9616	Special Circuits	-	-	-	-
3835	Position Sensor Circuits	B	B	B	B
9617	Stepping Motor Control	-	-	-	-
5274	Printer Circuits	A	A	A	A
6304	I/C-Keyboard Mos	-	-	A	A
6682	Integrated Circuits, Part 1	A	A	A	A
6683	Integrated Circuits, Part 2	A	A	A	A
8656	Integrated Circuits, Part 3	-	-	-	-
7281	Integrated Circuits, Part 4	-	-	-	-
9618	Integrated Circuits, Part 5	-	-	-	-
9596	Cd Placement, DD, 80 Col DR, IDR	-	A	B	B
7621	80 Column DR/IDR Blk Diagram	-	-	A	B
7498	Maintenance Diagnostic Panel	01	01	01	01
REVISION OF CONTENTS		-	A	B	C
DATE OF E.C.N.		25JUL73	31Jul73	12Oct73	17Dec74
APPROVAL		RVK	RVK	RVK	RVK
ENGINEERING CHANGE NOTICE		2125-2P	2125-3P	2125-8C	3853-1C

7545-A-7

DWG. NO. A 7548 REV C SH 1 OF 1

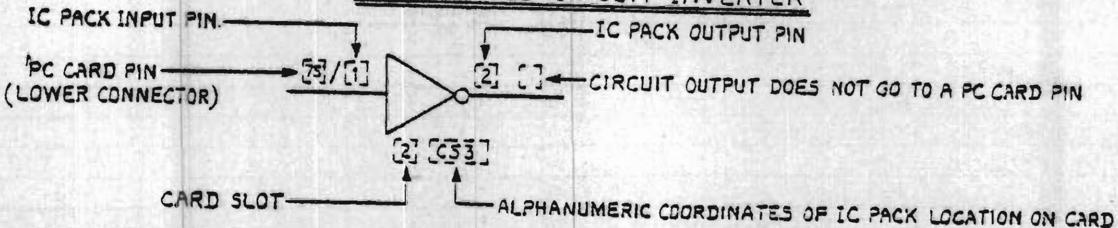
F92-112

INTEGRATED CIRCUIT

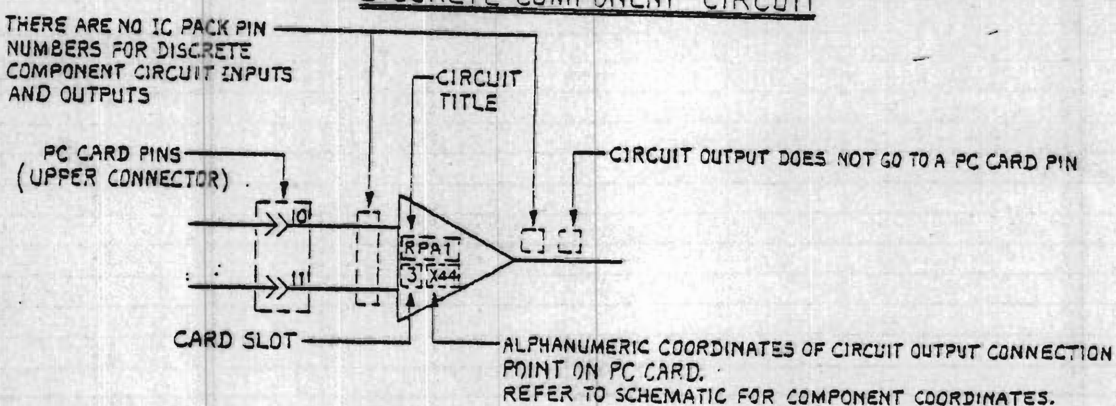


SHAPE OF SYMBOL INDICATES FUNCTION—SEE CIRCUIT REFERENCES

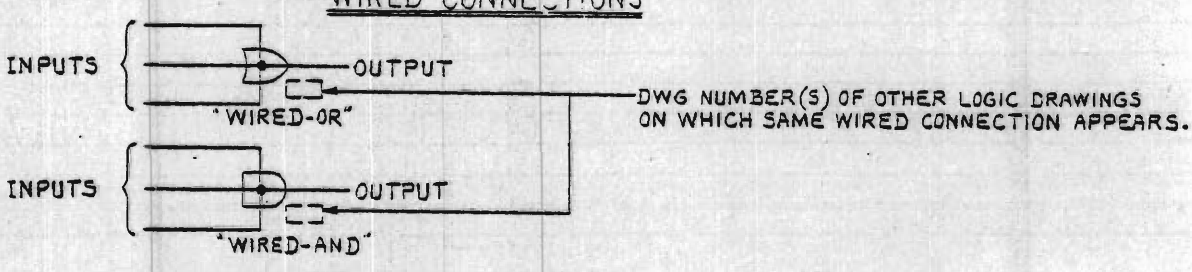
INTEGRATED CIRCUIT INVERTER



DISCRETE COMPONENT CIRCUIT



WIRED CONNECTIONS



NO COMPONENTS EXIST ON PC CARD.
INPUTS AND OUTPUT ARE DIRECTLY CONNECTED BY EITHER PC CARD PATHS OR BACKBOARD WIRING.

7545-A-3

REV	DESCRIPTION	APPVD-DATE
A	REVISED PER ECN 1619-1C	[illegible]
-	RELEASED ECN 591-1C	[illegible]

REVISIONS

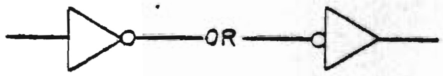
DRN J. ALESI — CHK — ENG — MFG

TITLE: LOGIC SYMBOL REFERENCE KEY

DWG NO. C 2683 — DASH NO. -00 — SH 1 OF 1

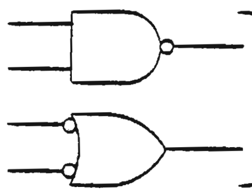
LOGIC SYMBOL

DESCRIPTION



INPUT SIGNAL IS INVERTED. CIRCLE INDICATES LOW LEVEL.

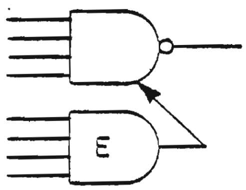
D



DESIRED FUNCTION DETERMINES SYMBOL USED. AVAILABLE IN 2,3, AND 4-INPUT CONFIGURATIONS

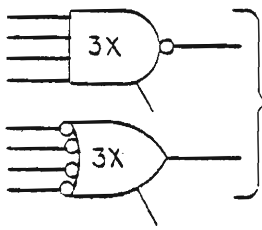
"AND" CIRCUIT: BOTH INPUT SIGNALS MUST BE HIGH TO MAKE OUTPUT LOW.

"OR" CIRCUIT: EITHER OR BOTH INPUT SIGNALS LOW MAKES OUTPUT HIGH.



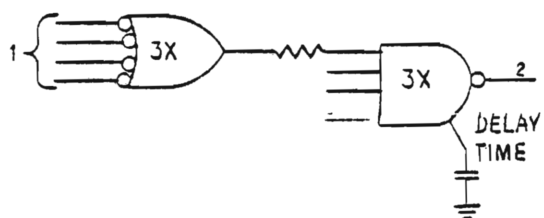
EQUIVALENT TO AN 8-INPUT "AND" OR "OR" CIRCUIT AS DESCRIBED ABOVE. THE LOWER SYMBOL REPRESENTS A SIMPLE I.C. DIODE GATE, WHICH CAN GO ONLY TO A 4-INPUT GATE EXPANDER PIN.

C

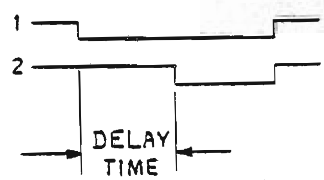


DESIRED FUNCTION DETERMINES SYMBOL USED

INTEGRATED CIRCUIT POWER GATE, WHICH HAS 3 TIMES THE DRIVE CAPABILITY OF A STANDARD GATE. "3XOC" IS SIMILAR, BUT HAS OPEN COLLECTOR OUTPUT TRANSISTOR, AND REQUIRES EXTERNAL PULLUP RESISTOR.
3X CANNOT BE WIRE-OR'D: 3XOC CAN.

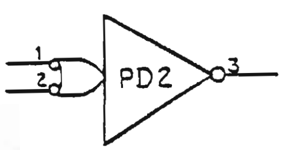


SINGLE EDGE DELAY (INTEGRATED CIRCUITS WITH EXTERNAL RESISTOR AND CAPACITOR). ANY OF INPUTS 1 GOING LOW PROVIDES A DELAYED LOW OUTPUT AT 2. HIGH LEVEL AT 1 MUST BE LONGER THAN 1/3 OF DELAY TIME. LOW LEVEL AT 1 MUST BE LONGER THAN DELAY TIME.



B E

POWER DRIVER



GENERATES HIGH-POWER LOGIC SIGNAL, LIGHTS LAMP, OR OPERATES RELAY. OUTPUT 3 IS LOW FOR A LOW SIGNAL AT EITHER INPUT 1 OR 2.

A A

7545- A- 9

REV	DESCRIPTION	APPVD-DATE
C	REVISED PER ECN 1619-IC	[illegible] 12-13-76
B	RELEASED ECN 391-IC	[illegible] 9-13-71
A	ADDED PD2	[illegible]

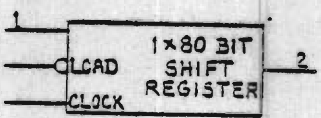
REVISIONS

DRN J. ALESI 10/30/70 ENG [illegible]
CHK [illegible] 11-12-70 MFG

TITLE INTEGRATED CIRCUIT INVERTERS, GATES & DELAYS

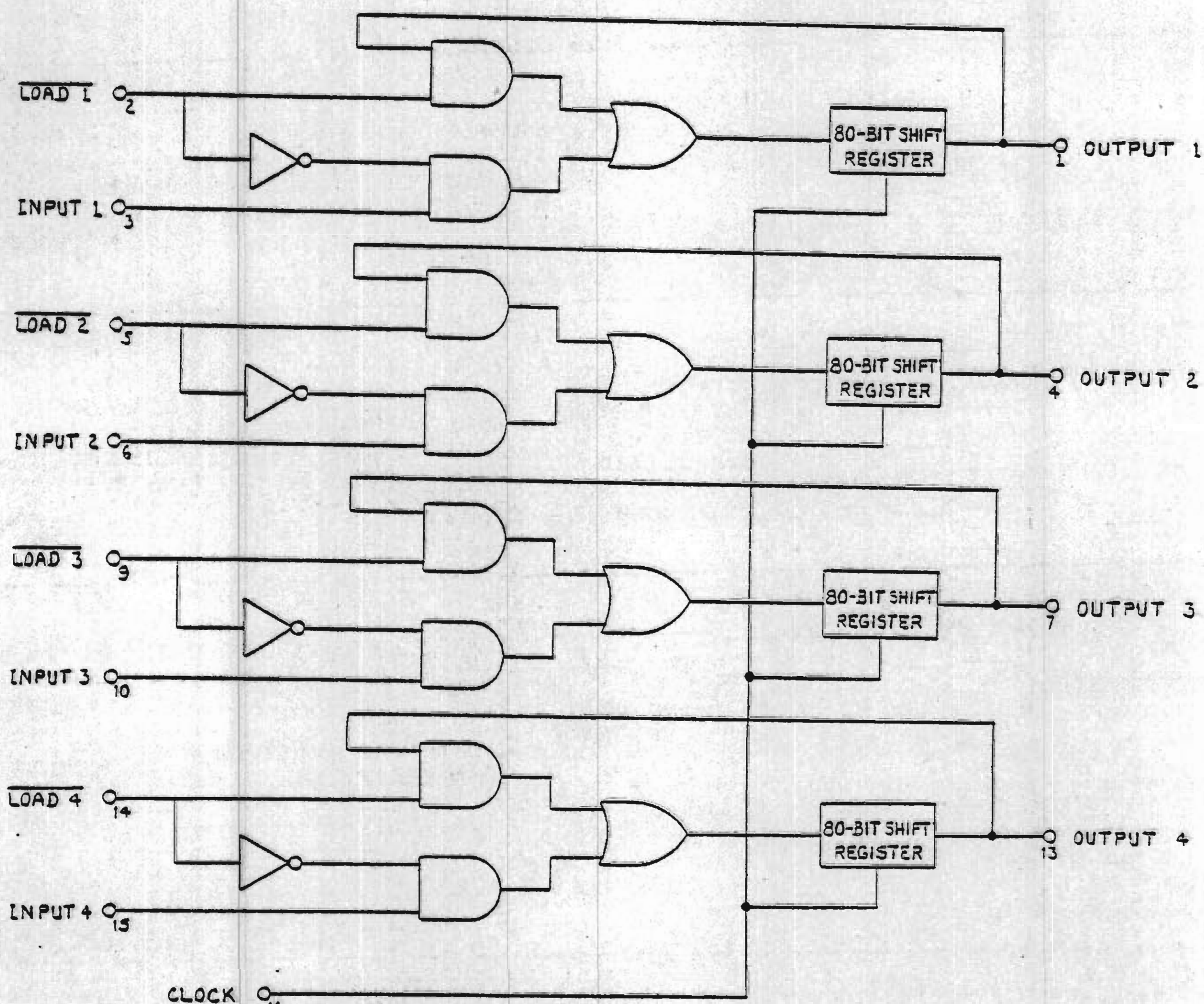
DWG NO. C 2684 DASH NO. -00 SH 1 OF 1

C 8655



TYPICAL
LOGIC SYMBOL

FOUR INDEPENDENT 80-BIT STATIC SHIFT REGISTERS IN A SINGLE PACKAGE WITH A COMMON CLOCK. WHEN LOAD INPUT IS LOW, DATA BITS ON INPUT ① ENTER FIRST REGISTER STAGE WHEN CLOCK IS HIGH. DATA SIGNALS APPEAR 80 CLOCK PULSES LATER AT OUTPUT ② WHEN CLOCK GOES LOW. WHEN LOAD INPUT GOES HIGH, INPUT GATE OF REGISTER IS BLOCKED AND RECIRCULATE GATE IS ENABLED, PERMITTING DATA TO RECIRCULATE IN REGISTER ONE POSITION FOR EACH HIGH-TO-LOW CLOCK PULSE TRANSITION. DATA SIGNALS OUT HAVE SAME POLARITY AS DATA SIGNALS IN.



REV	DESCRIPTION	APPVD-DATE
-	RELEASE PER ECN 2125-1 [illegible]	6-15-73

REVISIONS

7545 - A - 10

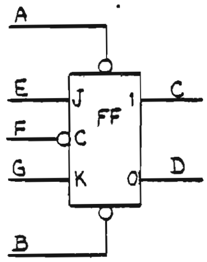
DRN J. ALESI 12/14/72 ENG [illegible] 6-15-73

INTEGRATED CIRCUIT MEMORY

DWG NO. C 8655 DASH NO. -00 SH 1 OF 1

4 | 3 | 2 | 1 | C 3829

LOGIC SYMBOL



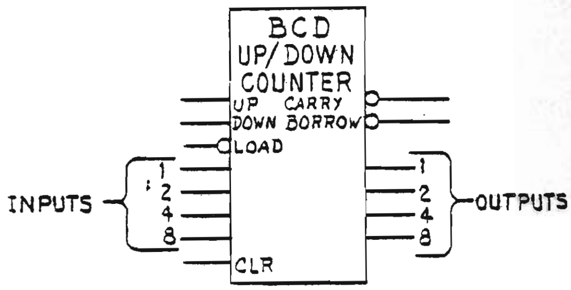
DESCRIPTION

-FLIP-FLOP-

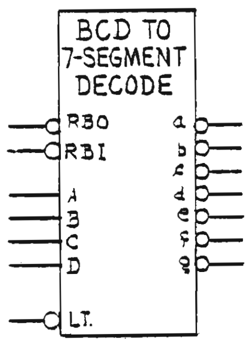
Ⓐ LOW SETS FF (Ⓒ HIGH, Ⓓ LOW), AND Ⓑ LOW RESETS FF, REGARDLESS OF STATES OF Ⓔ, Ⓕ, Ⓖ.

Ⓕ GOING LOW EITHER SET OR RESETS FF IF EITHER Ⓔ OR Ⓖ RESPECTIVELY, IS HIGH OR WAS HIGH DURING TIME Ⓕ WAS HIGH.

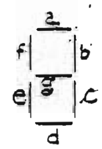
IF Ⓔ AND Ⓖ ARE BOTH HIGH (OR OPEN) AT ANY TIME WHILE Ⓕ IS HIGH, Ⓕ GOING LOW CAUSES Ⓒ AND Ⓓ TO REVERSE STATES. Ⓕ HAS NO EFFECT IF BOTH Ⓔ AND Ⓖ ARE HELD LOW OR GROUNDED.



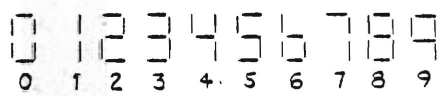
4-STAGE SYNCHRONOUS, REVERSIBLE BCD COUNTER, HIGH CLR INPUT DRIVES OUTPUTS 1,2,4,8 TO LOW STATE. LOW LOAD INPUT PRESETS OUTPUTS 1,2,4,8 TO MATCH INPUTS 1,2,4,8. HIGH GOING-EDGES ON UP OR DOWN INPUTS STEP COUNTER IN CORRESPONDING BCD SEQUENCE. COUNT CONTINUES THROUGH ZERO STATE, WITH A LOW PULSE GENERATED ON CARRY OR BORROW OUTPUTS WHEN ZERO STATE IS PASSED FROM UP OR DOWN DIRECTION, RESPECTIVELY.



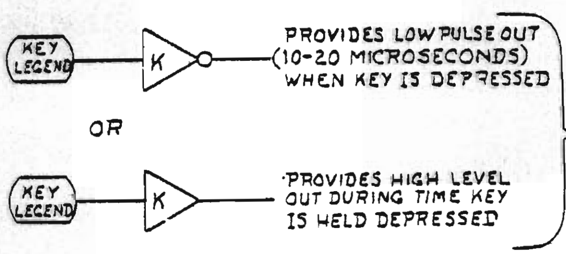
CONVERTS BCD INPUT ON A,B,C,D TO 7-SEGMENT OUTPUT (FOR LAMP DRIVING) ON a,b,c,d,e,f,g ACCORDING TO CHART BELOW. RBO INPUT, IF LOW, PREVENTS ALL SEGMENTS FROM BEING LIT. RBI INPUT, IF LOW, PREVENTS ONLY ZERO CHARACTER FROM BEING LIT.



SEGMENT IDENTIFICATION



KEYBOARD SWITCH AMPLIFIERS



BOTH AMPLIFIER TYPES ARE IN KEYSWITCH MODULE ITSELF, AND EMPLOY HALL-EFFECT GENERATORS, TRIGGER CIRCUTS, AND OUTPUT AMPLIFIERS.

REV	DESCRIPTION	APPVD-DATE
D	REVISED PER ECN 1619-1C	TRO 12-12-72
C	REVISED PER ECN 996-1C	JTD 2-1-72
B	REVISED PER ECN 906-1C	TRO 11-29-71
A	REVISED PER ECN 816-1C	TRO 10-29-71
—	RELEASED ECN 691-1C	[illegible]

REVISIONS

7545-A-11

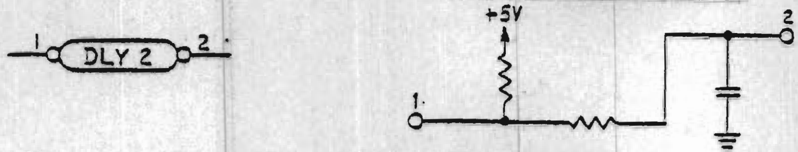
DRN J. ALESI 4/14/71 ENG [illegible]
CHK [illegible] 5/20/71 MFG

TITLE INTEGRATED CIRCUIT FLIP-FLOP, COMPLEX FUNCTIONS

DWG NO. C 3829 DASH NO. -00 SH 1 OF 1

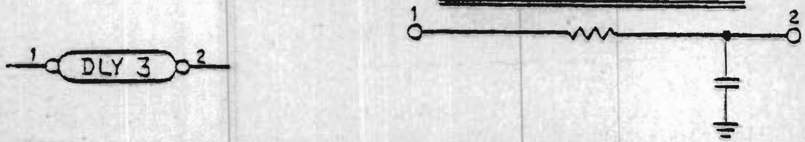
C 3830

PASSIVE DELAY #2



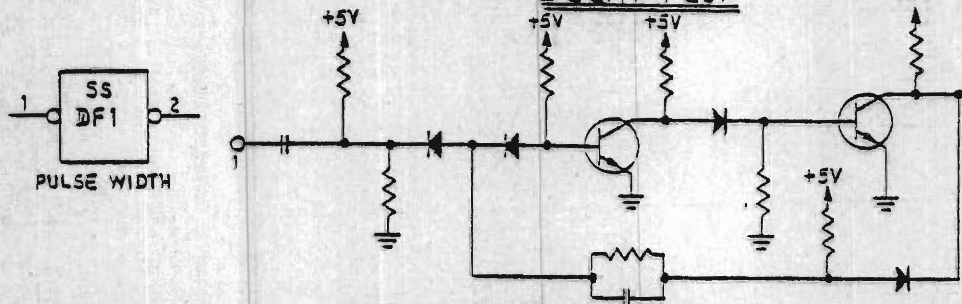
PROVIDES SIGNAL DELAY BETWEEN LOGIC CIRCUITS CONNECTED TO ① AND ②.

PASSIVE DELAY #3



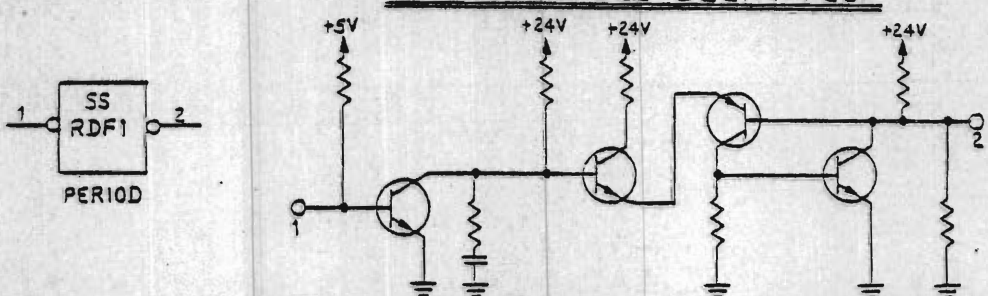
PROVIDES SIGNAL DELAY BETWEEN LOGIC CIRCUITS CONNECTED TO ① AND ②.

DELAY FLOP

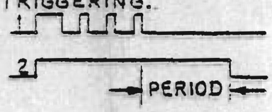


① GOING LOW GENERATES A LOW PULSE AT ②. WILL NOT RETRIGGER DURING FIRST 95% OF OUTPUT PULSE TIME. WILL RETRIGGER AFTER 105% OF OUTPUT PULSE TIME.
① MUST BE LOW FOR AT LEAST ONE MICROSECOND.

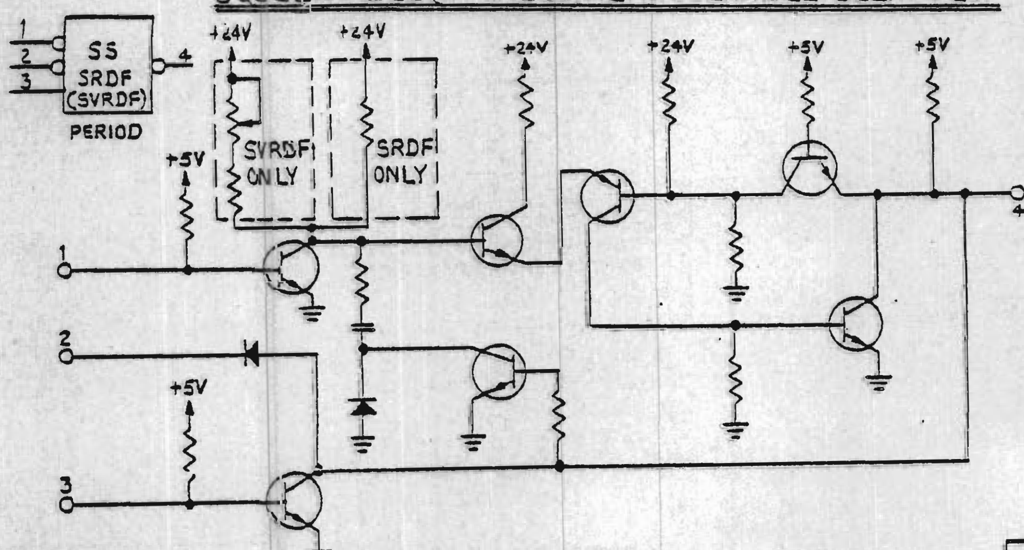
RETRIGGERABLE DELAY FLOP



① GOING HIGH CAUSES ② TO GO HIGH. ② GOES LOW AFTER ① HAS BEEN LOW FOR THE SPECIFIED PERIOD. ① MUST BE HIGH FOR AT LEAST 1/7500 OF PERIOD TO PERMIT RETRIGGERING.



SQUELCHABLE (VARIABLE) RETRIGGERABLE DELAY FLOP



① GOING HIGH CAUSES ④ TO GO HIGH. ④ GOES LOW AFTER ① HAS BEEN LOW FOR THE SPECIFIED PERIOD, UNLESS ② GOES LOW OR ③ GOES HIGH, IN WHICH CASE ④ GOES LOW AT ONCE. POTENTIOMETER IN SVRDF PERMITS DELAY TIME TO BE ADJUSTED BETWEEN 10 AND 22 SECONDS. ① MUST BE HIGH FOR AT LEAST 1/7500 OF PERIOD TO PERMIT RETRIGGERING.

REV	DESCRIPTION	APPD-DATE
A	REVISE PER ECN 1619-10	8-2-72

REVISIONS

7545-A-12

DRN J. ALESI 4/14/71 ENG

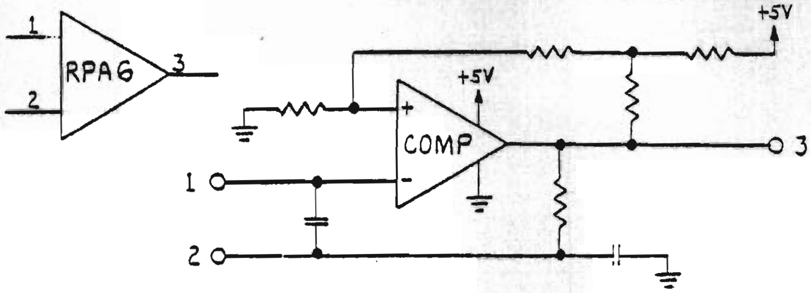
CHK 5/10/71 MFG

TITLE: DISCRETE DELAY CIRCUITS

DWG NO. C 3830 DASH NO. -00 SH 1 OF 1

4 | 3 | 2 | 1 | C 9612

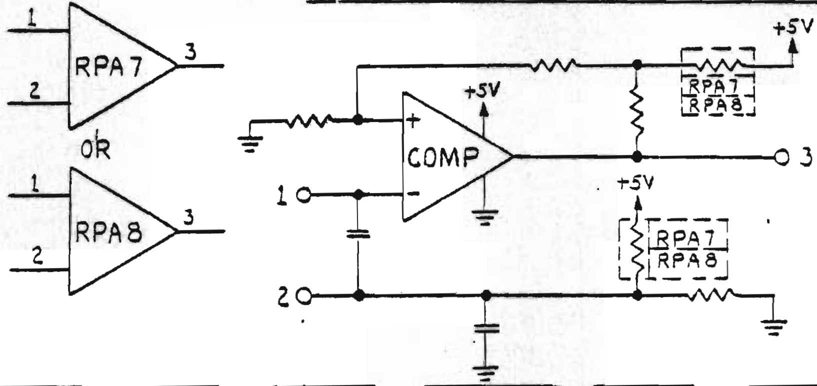
RELUCTANCE PICKUP AMPLIFIER 6



RELUCTANCE PICKUP ACROSS ① AND ② SENSES TEETH ON READ STATION SPROCKET DISC. OUTPUT AT ③ IS A SQUARE WAVE WITH PERIOD OF APPROXIMATELY 300 μSEC WHEN MOTOR IS RUNNING AND 2 MILLISEC. WHEN IT IS NOT.

D

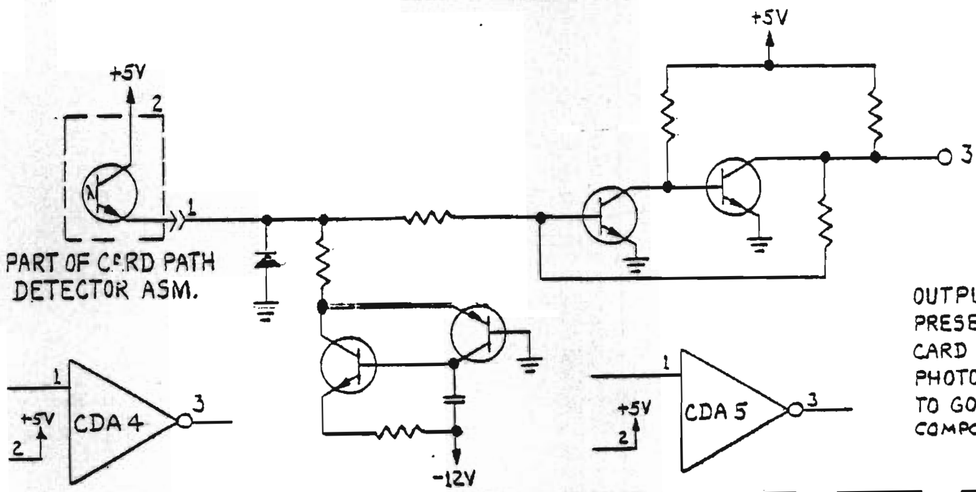
RELUCTANCE PICKUP AMPLIFIER 7 & 8



RELUCTANCE PICKUP ACROSS ① AND ② SENSES SEGMENTS ON PUNCH STATION TIMING DISC OR TIMING MARKS ON PRINT DRUM. OUTPUT AT ③ IS LOGIC-LEVEL ANALOG OF DISC/DRUM PROFILE. RPA7 & 8 DIFFER ONLY IN COMPONENT VALUES.

C

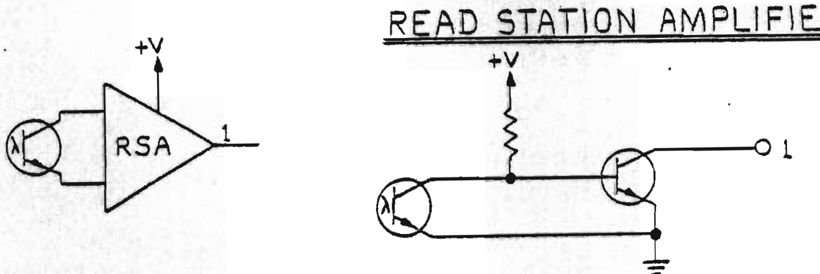
CARD DETECTOR AMPLIFIER 4 & 5



OUTPUT ③ IS HIGH WHEN CARD IS NOT PRESENT UNDER DETECTOR ASSEMBLY. CARD INTERRUPTS REFLECTED LIGHT INTO PHOTOTRANSISTOR AND CAUSES OUTPUT ③ TO GO LOW. CDA 4 & 5 DIFFER ONLY IN COMPONENT VALUES.

B E

READ STATION AMPLIFIER



OUTPUT ① IS HIGH WHEN LIGHT FROM READ STATION LAMP SHINES ON PHOTOTRANSISTOR. VOLTAGE (+V) IS SET BY POTENTIOMETER IN READ STATION HOUSING.

A A

7545-A-13

REV	DESCRIPTION	APPVD-DATE
A	REVISED PER ECN 2125-6P	TD 10 OCT 73
-	RELEASE PER ECN 2125-1P	TD 6-15-73

REVISIONS

DRN J. ALESI 5-19-73 | ENG TR Dahme 6-15-73
CHK [signature] 5/19/73 | MFG [signature]

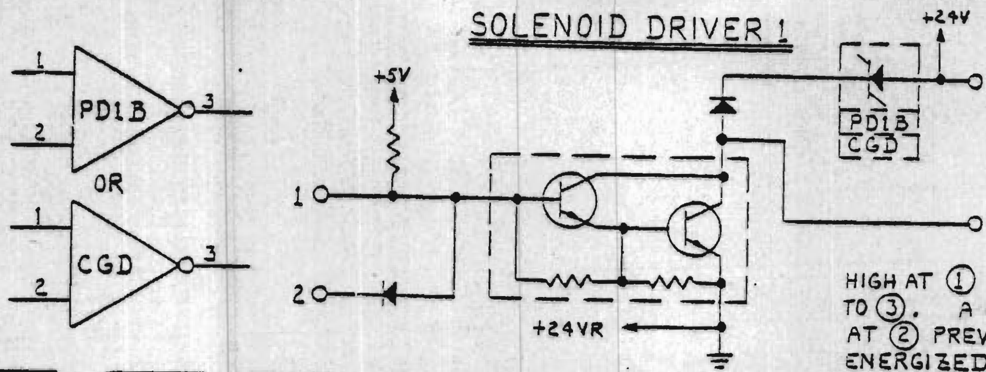
TITLE DISCRETE LOW-LEVEL AMPLIFIERS

DWG NO. C 9612 | DASH NO. -00 | SH 1 OF 1

E 79-062 4 3 2 1

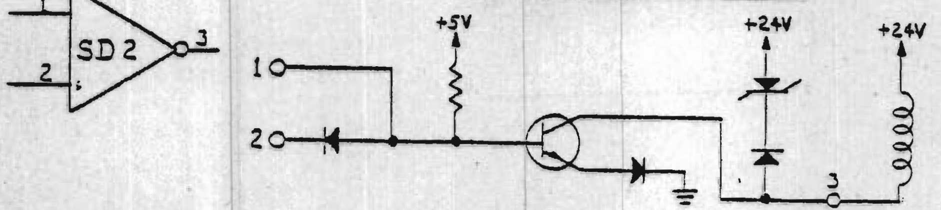
C 9613

SOLENOID DRIVER 1



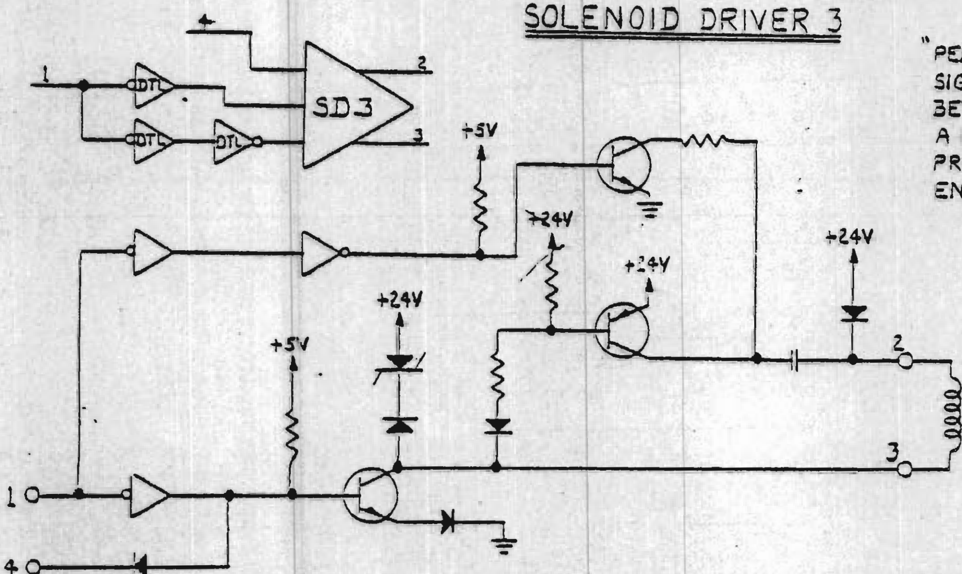
HIGH AT ① ENERGIZES COIL CONNECTED TO ③. A LOW (GENERAL CLEAR) SIGNAL AT ② PREVENTS THE COIL FROM BEING ENERGIZED.

SOLENOID DRIVER 2



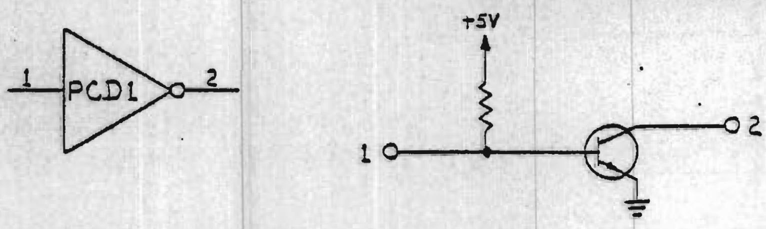
HIGH AT ① ENERGIZES COIL CONNECTED TO ③. A LOW (GENERAL CLEAR) SIGNAL AT ② PREVENTS THE COIL FROM BEING ENERGIZED.

SOLENOID DRIVER 3



"PEAK AND HOLD" DRIVER CIRCUIT-LOW SIGNAL AT ① OPERATES COIL CONNECTED BETWEEN ② AND ③.
A LOW (GENERAL CLEAR) SIGNAL AT ④ PREVENTS THE COIL FROM BEING ENERGIZED.

PUNCH COIL DRIVER



HIGH AT ① ENERGIZES PUNCH COIL CONNECTED TO ②.

REV	DESCRIPTION	APPD-DATE
-	RELEASE PER ECN 2125-1P	JCD 6-18-73

REVISIONS

7545-A-14

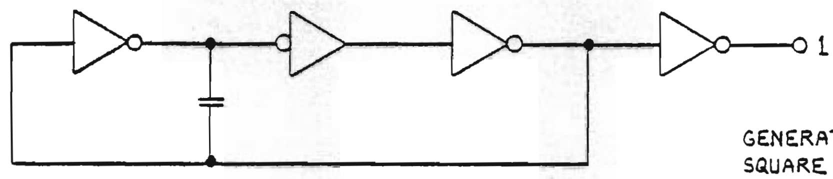
DRN J. ALESI 1/21/73 | ENG 1-19-73
CHK 2/1/73 | MFG

TITLE: DISCRETE DRIVER CIRCUITS

DWG NO. C 9613 | DASH NO. -00 | SH 1 OF 1

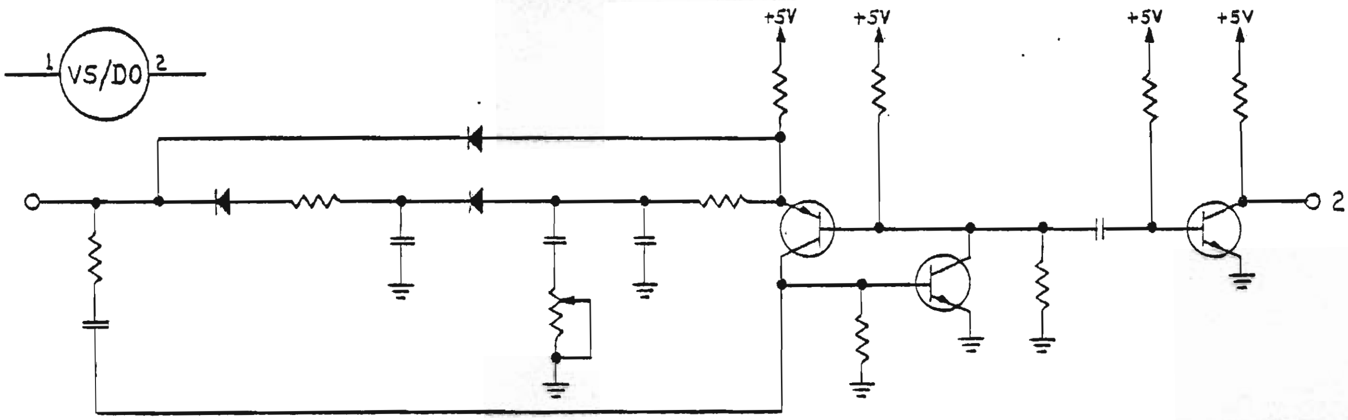
4 | 3 | 2 | 1 | C 9614

MASTER OSCILLATOR



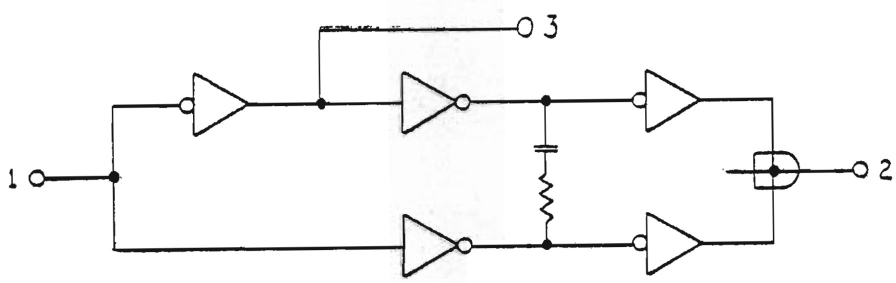
GENERATES A NOMINAL 620 KHZ SQUARE WAVE AT ①.

VARIABLE SKIP/DUP OSCILLATOR



A POSITIVE SIGNAL AT ① INITIATES AND MAINTAINS A SERIES OF POSITIVE PULSES OUT AT ②. FIRST PULSE COINCIDES WITH INPUT EDGE, SECOND PULSE OCCURS 330 MS LATER, AND SUBSEQUENT PULSE INTERVAL MAY BE SET TO OCCUR BETWEEN 40 AND 100 MS. OUTPUT PULSE IS NOMINALLY FIVE MICROSECONDS WIDE.

DOUBLE-EDGE DIFFERENTIATOR



GENERATES POSITIVE PULSE AT ② FOR EACH LOGIC-LEVEL SIGNAL TRANSITION IN AT ①. DRIVEN BY ANY LOGIC-LEVEL SIGNAL. OUTPUT PULSES ARE BETWEEN FIVE AND FIFTEEN MICROSECONDS WIDE. OUTPUT AT ③ IS AN INVERTED INPUT AT ①.

REV	DESCRIPTION	APPVD-DATE
-	RELEASE PER ECN 2125-1P	6-14-73

REVISIONS

7545-A-15

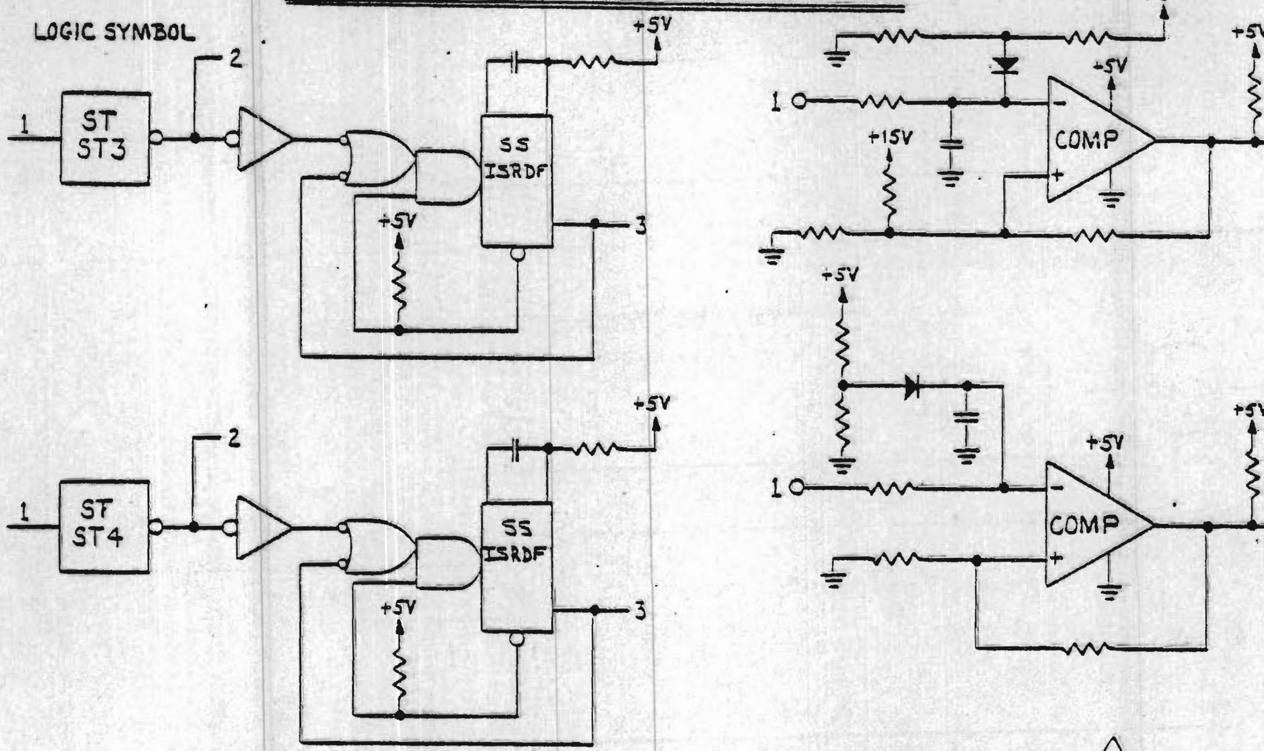
DRN J. ALESI 5/14/73 | ENG 6-18-73
CHK 5/19/73 | MFG

TITLE MISC DISCRETE CIRCUITS, PART 1

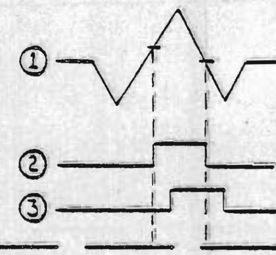
DWG NO. C 9614 | DASH NO. -00 | SH 1 OF 1

E 79-082

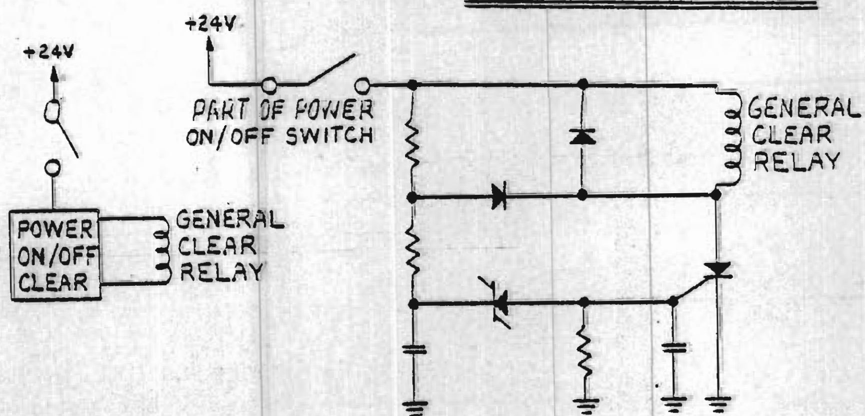
SCHMITT TRIGGER/PULSE GENERATOR



ANALOG SIGNAL AT ① GENERATES OUTPUTS ② AND ③ AS SHOWN.
ST3 AND ST4 DIFFER ONLY IN TRIGGER POINTS ON ANALOG SIGNAL.



POWER ON/OFF CLEAR



+24 VOLTS THROUGH SWITCH CHARGES CAPACITOR UNTIL SCR FIRES, PROVIDING 0.5 SECOND DELAYED OPENING OF GENERAL CLEAR RELAY DURING POWER ON SEQUENCE. POWER OFF CLOSES GENERAL CLEAR RELAY IMMEDIATELY.

REV	DESCRIPTION	APPVD-DATE
-	RELEASE PER EIN 2125-1P	5-18-73

REVISIONS

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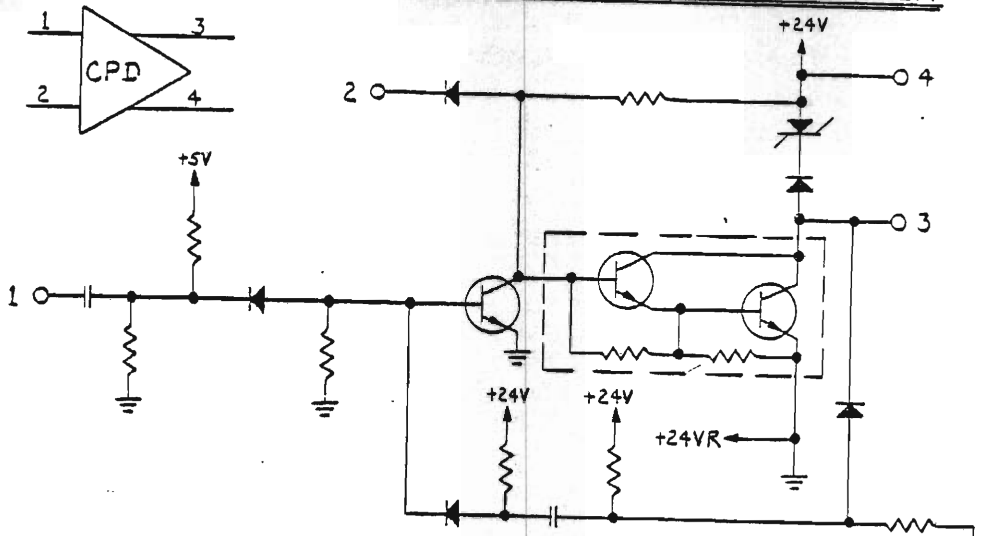
7545-A-16

DRN	J. ALESI 3-17-73	ENG	5-18-73
CHK	5/17/73	MFG	

TITLE: MISC. DISCRETE CIRCUITS PART 2

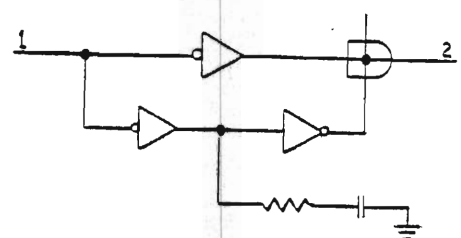
DWG NO. C 9615 — DASH NO. -00 — SH 1 OF 1

CARD PADDLE SOLENOID DRIVER



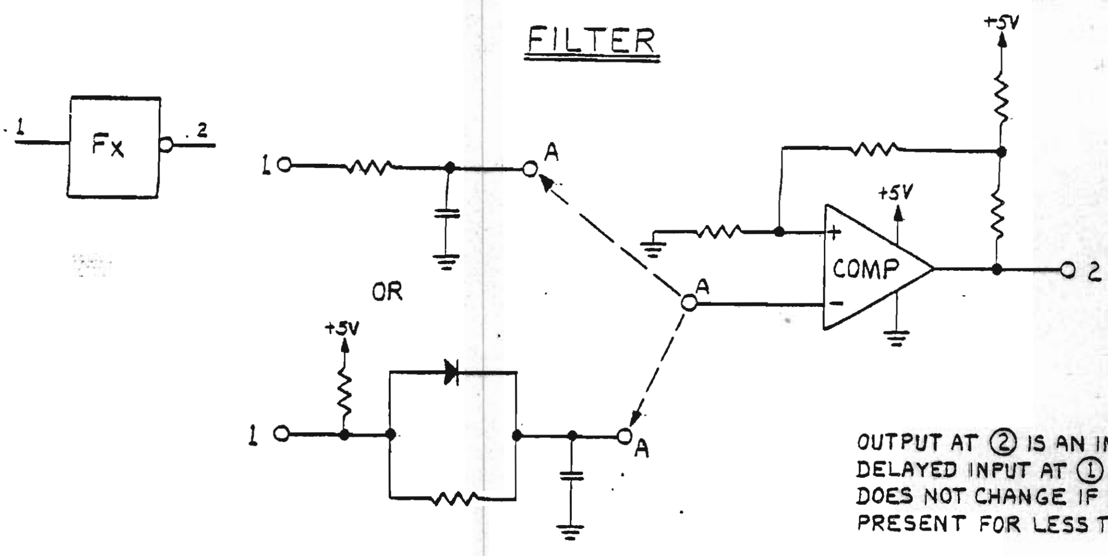
NEGATIVE-GOING LOGIC LEVEL EDGE AT ① ENERGIZES COIL CONNECTED TO ③ AND ④. INPUT AT ① MUST BE LOW AT LEAST 5 MICRO-SECONDS. THE OUTPUT AT ③ AND ④ IS DRIVEN FOR 75 MILLI-SECONDS. A LOW (GENERAL CLEAR) SIGNAL AT ② PREVENTS THE COIL FROM BEING ENERGIZED.

NEGATIVE-EDGE DIFFERENTIATOR



A NEGATIVE LOGIC-LEVEL EDGE IN AT ① PROVIDES A POSITIVE PULSE OUT AT ② DRIVEN BY ANY LOGIC-LEVEL SIGNAL. OUTPUT PULSES ARE BETWEEN .5 AND 15.6 MICROSECONDS WIDE, DEPENDING ON CAPACITOR VALUE.

FILTER



OUTPUT AT ② IS AN INVERTED AND DELAYED INPUT AT ①. OUTPUT AT ② DOES NOT CHANGE IF INPUT AT ① IS PRESENT FOR LESS THAN THE DELAY.

REV	DESCRIPTION	APPVD-DATE
-	RELEASE PER ECN 2125-1P	6-19-73

REVISIONS

7545 - A - 17

DRN J. ALESI 5/21/73 | ENG 6-18-73
CHK 5/21/73 | MFG

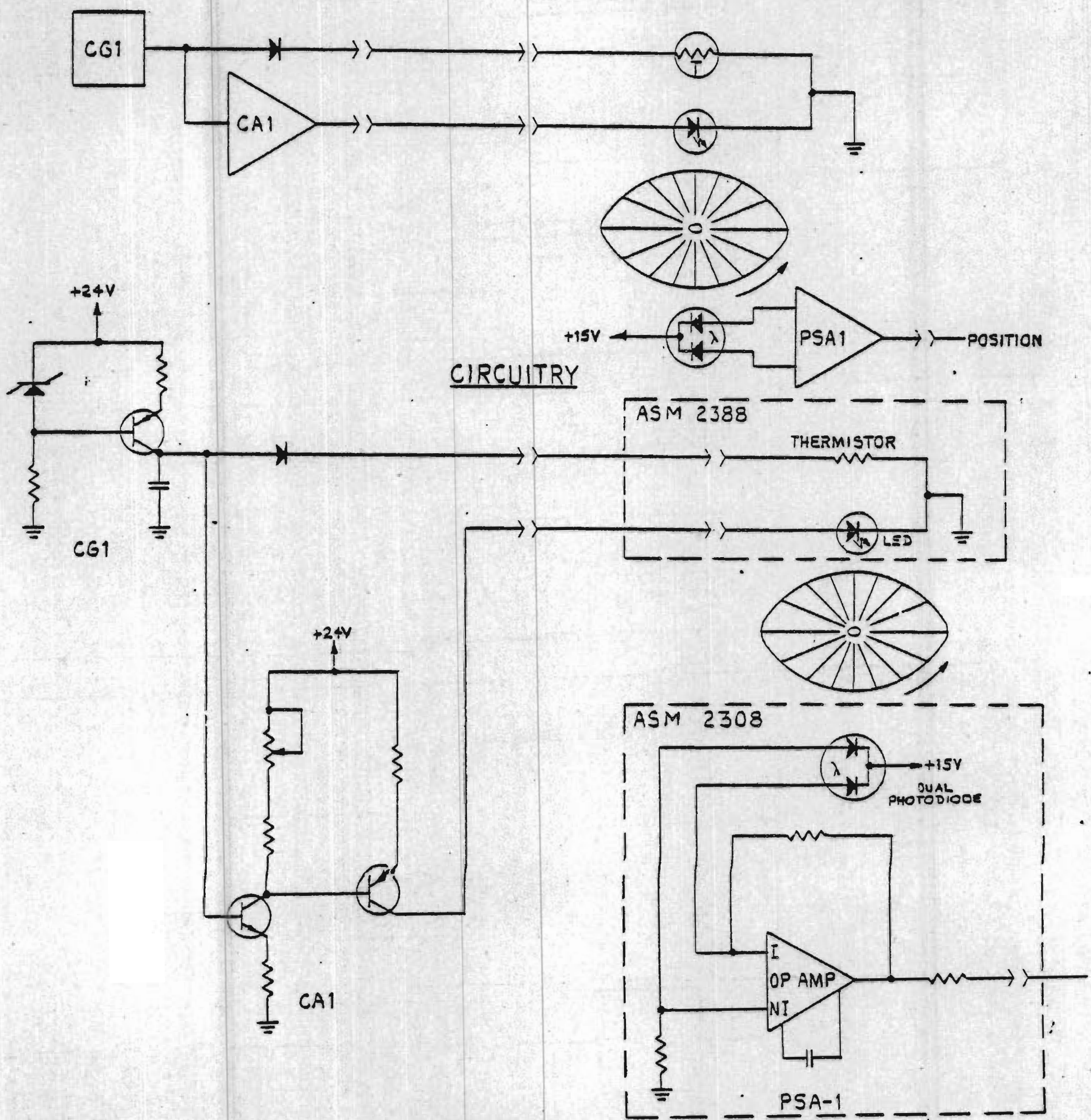
TITLE: SPECIAL CIRCUITS

DWG NO. C 9616 | DASH NO. -00 | SH 1 OF 1

C 3835

LOGIC REPRESENTATION

CIRCUITRY



DESCRIPTION

CG1 IS CONSTANT CURRENT GENERATOR, THE OUTPUT OF WHICH DIVIDES BETWEEN CA1 (CURRENT AMPLIFIER) AND TEMPERATURE-COMPENSATING THERMISTOR. ROTATING DISC MODULATES LED RADIANT OUTPUT TO DUAL PHOTODIODE. PSA1 GENERATES ANALOG SIGNAL CORRESPONDING TO DISC POSITION.

REV	DESCRIPTION	APPVD-DATE
B	REVISED PER ECN 1619-1C	12-9-72
A	REVISED PER ECN #816-1C	8-25-71

REVISIONS

7545 - A - 18

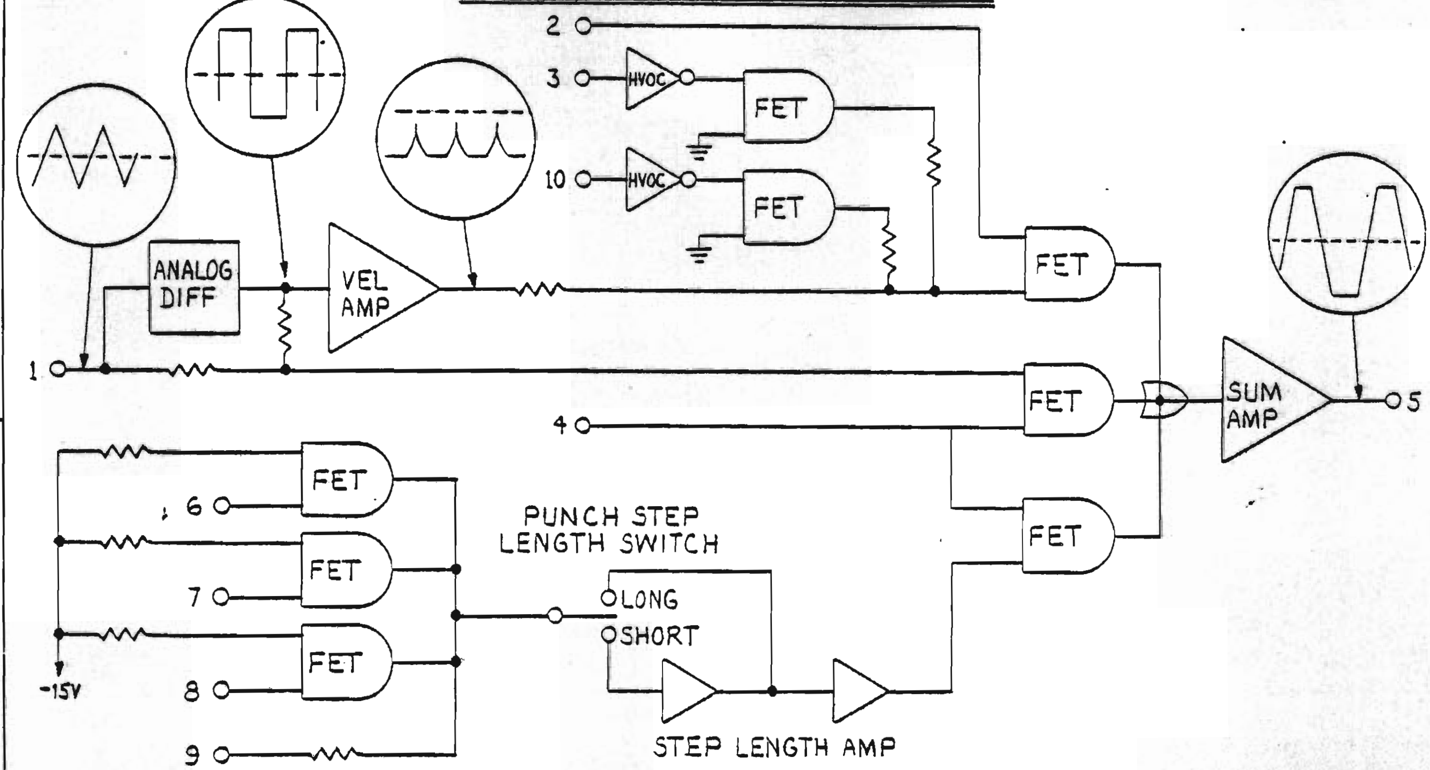
DRN J. ALESI 4/16/71

CHK 5-20-71

POSITION SENSOR CIRCUITS

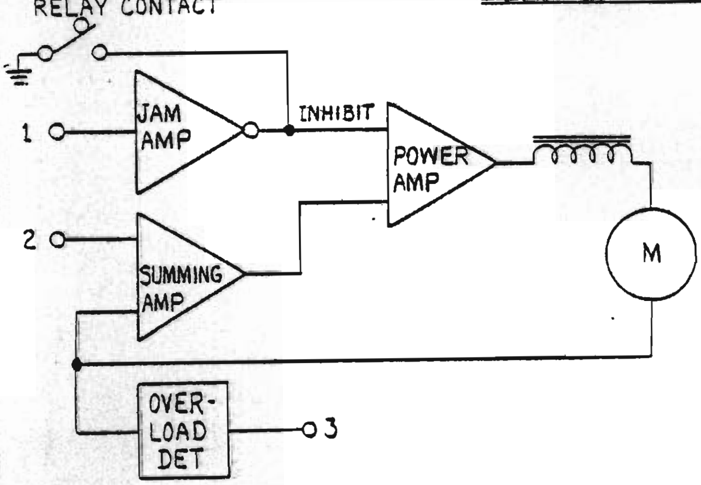
DWG NO. C 3835 — DASH NO. -00 — SH 1 OF 1

SERVO-LOGIC REPRESENTATION



ANALOG SIGNAL INPUT AT ① (POSITION SIGNAL FROM PSA1) GENERATES ANALOG OUTPUT AT ⑤ (POSITION DIFFERENCE) SIGNAL USED TO CONTROL POWER AMPLIFIER. INPUTS ② AND ④ ARE MUTUALLY EXCLUSIVE ENABLING LOGIC SIGNALS WHICH CAUSE SERVO TO OPERATE IN EITHER POSITION MODE ④ OR LOW-VELOCITY MODE ②. INPUT ③ IS A LOGIC-LEVEL SIGNAL WHICH, WHEN PRESENT WITH ⑩, FORCES A HIGH VELOCITY MODE OF OPERATION. INPUT ⑩ IS A LOGIC-LEVEL SIGNAL WHICH, WHEN PRESENT, FORCES A NORMAL VELOCITY MODE OF OPERATION. FOR CIRCUITRY SEE DWG D-8878 (WAVE FORMS INDICATED EXIST WHEN MOTOR IS RUNNING CONTINUOUSLY UNDER CONTROL OF MAINTENANCE CARD SWITCH).
INPUT ⑥ IS A LOGIC-LEVEL SIGNAL WHICH IS PRESENT FOR A COLUMN COUNT OF 20 THRU 80. INPUT ⑦ IS A LOGIC-LEVEL SIGNAL WHICH IS PRESENT FOR A COLUMN COUNT OF 40 THRU 80. INPUT ⑧ IS A LOGIC-LEVEL SIGNAL WHICH IS PRESENT FOR A COLUMN COUNT OF 60 THRU 80. INPUT ⑨ IS A LOGIC-LEVEL SIGNAL WHICH IS PRESENT WHEN THE SERVO POWER AMP IS NOT INHIBITED. WITH THE PUNCH STEP LENGTH SWITCH IN THE LONG/SHORT POSITION, THE CARD WILL BE DISPLACED FROM ITS NORMAL POSITION IN THE PUNCH TO THE RIGHT/LEFT BY 3-4 MILS FOR COLUMNS 21 THRU 40, 6-8 MILS FOR COLUMNS 41 THRU 60, AND 9-12 MILS FOR COLUMNS 61 THRU 80.

POWER AMPLIFIER



PROVIDES DRIVE FOR STEPPING MOTOR, UNDER CONTROL OF POSITION DIFFERENCE OUTPUT OF SERVO (INPUT ②). INPUT ① IS A LOGICAL INHIBIT; IF IT IS HIGH, OR THE TURN ON CLEAR RELAY IS CLOSED, THE MOTOR CANNOT BE DRIVEN. OUTPUT ③ IS A LOGIC-LEVEL OVERLOAD ERROR SIGNAL. FOR CIRCUITRY, SEE DWG D-5586.

7545-A-19

DRN J. ALESI 5-17-73	ENG [illegible] 5-17-73		
CHK [illegible] 5-17-73	MFG [illegible]		

REV	DESCRIPTION	APPVD-DATE
-	RELEASE PER ECN 2123-1P	[illegible] 6-18-73

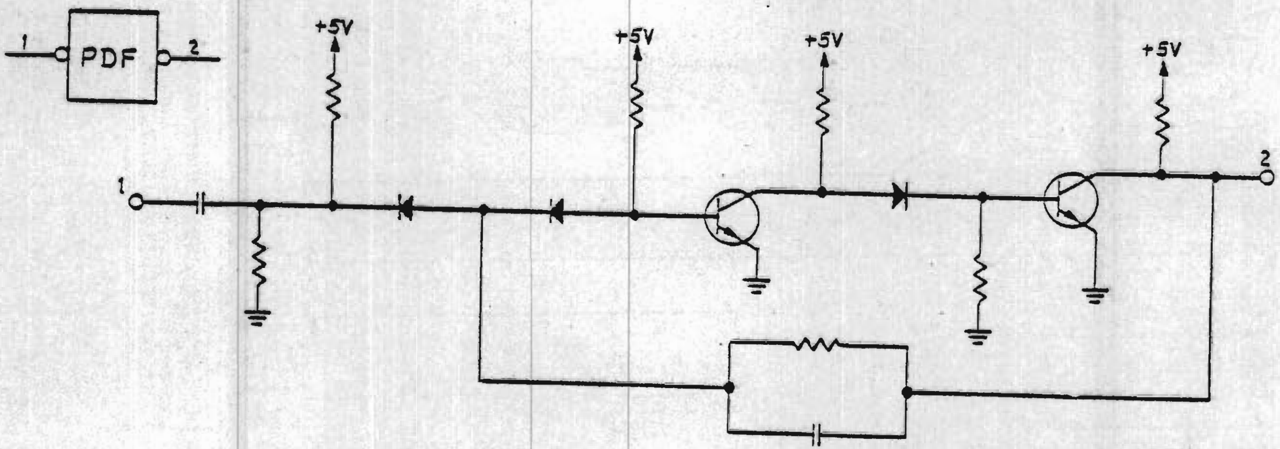
REVISIONS

TITLE STEPPING MOTOR CONTROL

DWG NO. C 9617 DASH NO. -00 SH 1 OF 1

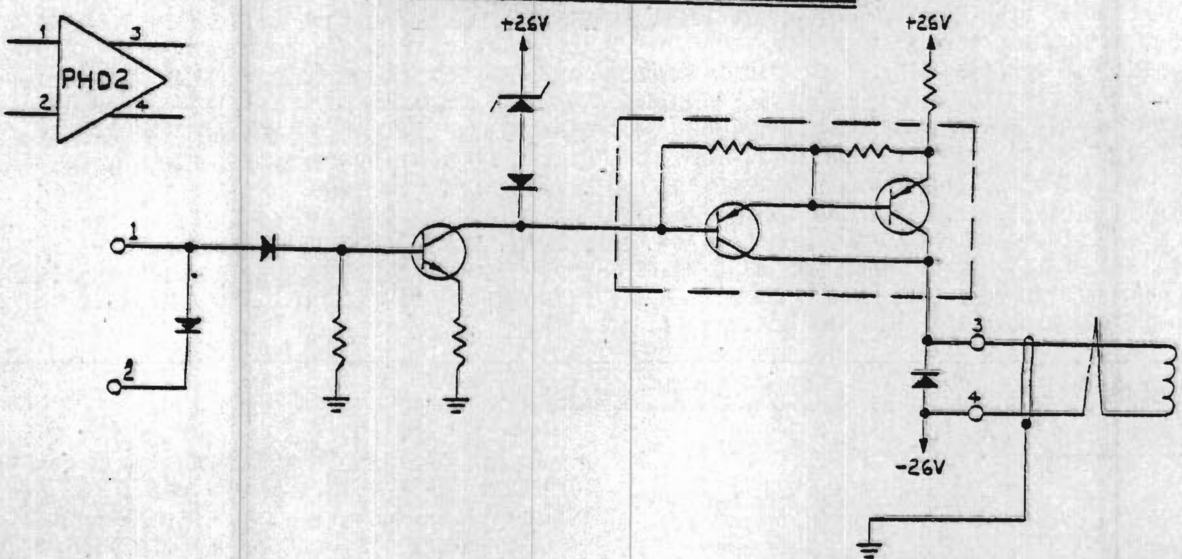
C 5274

PRINTER DELAY FLOP



SETS TIMING FOR PULSE OUTPUT TO PRINTER HAMMER DRIVER. ① GOING LOW FOR AT LEAST ONE MICROSECOND PROVIDES A NOMINAL 1.6 MILLI-SECOND LOW PULSE OUT AT ②.

PRINTER HAMMER DRIVER



HIGH INPUT AT ① (INVERTED OUTPUT OF PDF, ABOVE) ENERGIZES PRINT HAMMER COIL CONNECTED BETWEEN ③ AND ④. A LOW (GENERAL CLEAR) SIGNAL AT ② PREVENTS THE COIL FROM BEING ENERGIZED.

REV	DESCRIPTION	APPVD-DATE
A	REVISED PER ECN 1619-1C	11-3-72
-	RELEASE PER ECN 953-1C	1-15-72

REVISIONS

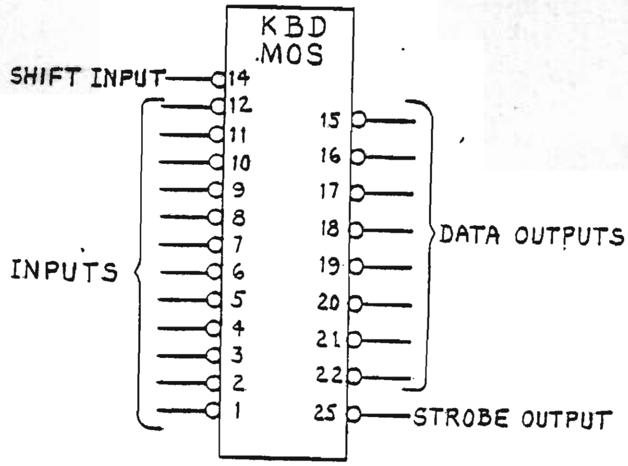
7545-A-20

DRN J. ALESI 1/25/72 ENG 1-25-72
CHK 1/25/72 MFG

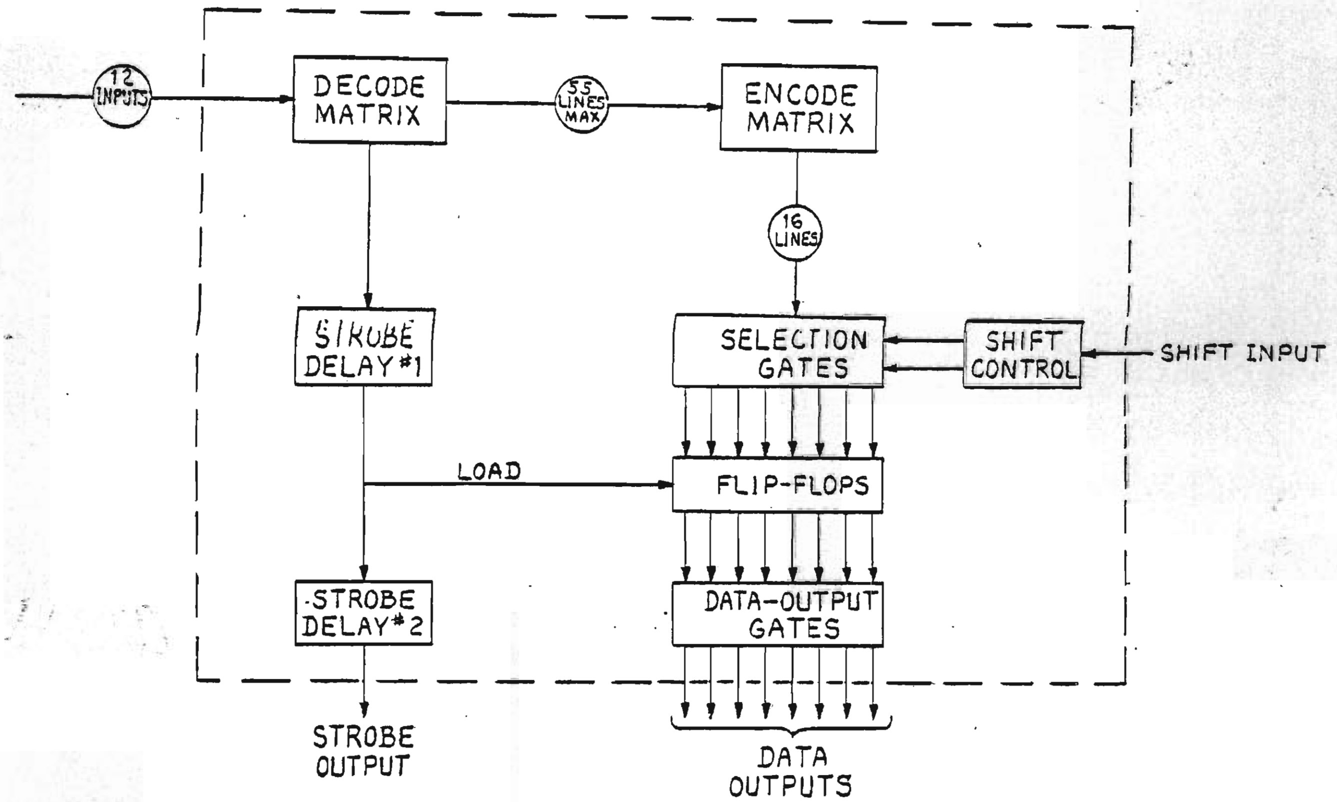
TITLE PRINTER CIRCUITS

DWG NO. C 5274 DASH NO. -00 SH 1 OF 1

C 6304



TWO-OF-TWELVE CODE ON INPUTS GENERATES EIGHT DATA OUTPUTS AND STROBE AS INDICATED ON BLOCK DIAGRAM. DATA INPUTS ARE FROM PULSE KEYS (10-20 MICROSECONDS). STROBE OUTPUT IS A PULSE EQUAL IN LENGTH TO THE PULSE FROM THE KEY. DATA OUTPUTS REMAIN STABLE UNTIL A NEW KEY IS DEPRESSED. STROBE OUTPUT IS DELAYED FROM 0.5 TO 5.0 MICROSECONDS AFTER DATA CHANGE. A 1-BIT OUTPUT FROM PIN 22 IS CONNECTED EXTERNALLY ON KEYBOARD P.C. CARD TO INHIBIT THE STROBE OUTPUT.



7545-A-21

REV	DESCRIPTION	APPVD-DATE
A	REVISED PER ECN 2407-1C	JRD 7-16-73
–	RELEASE PER ECN # 1310-1C	7-16-72

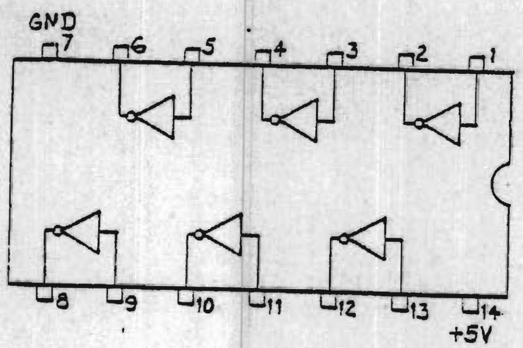
REVISIONS

DRN J. ALESI 5/16/72
CHK
ENG 6-16-72
MFG

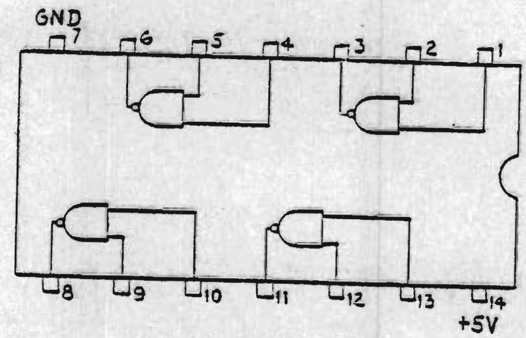
TITLE INTEGRATED CIRCUIT—KEYBOARD MOS

DWG NO. C 6304 DASH NO. -00 SH 1 OF 1

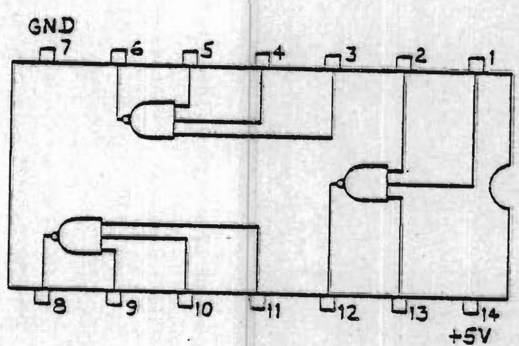
C 6682



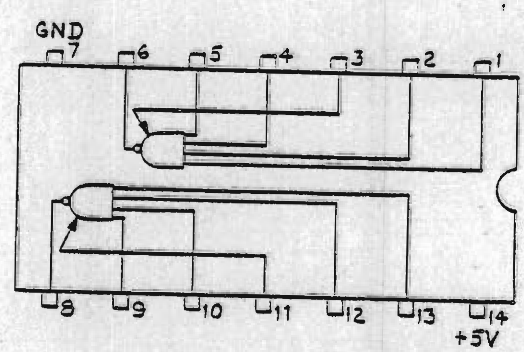
6/1 INPUT INVERTER
P/N 2773-00 (936)



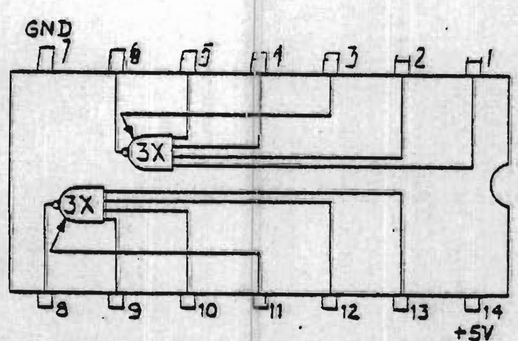
4/2 INPUT GATE
P/N 2775-00 (946)



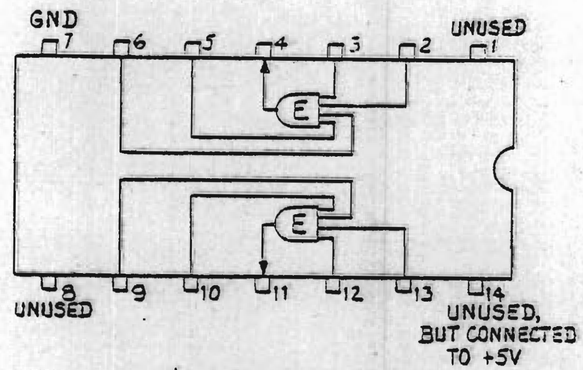
3/3 INPUT GATE
P/N 2776-00 (962)



2/4 INPUT GATE
P/N 2770-00 (930)



2/4 INPUT POWER GATE
P/N 2771-00 (932)



2/4 INPUT EXPANDER
P/N 2772-00 (933)

7545- A- 22

REV	DESCRIPTION	APPVD-DATE
A	REVISED PER ECN 1619-1C	12-4-72
-	RELEASE PER ECN # 1310-1C	10-16-72

REVISIONS

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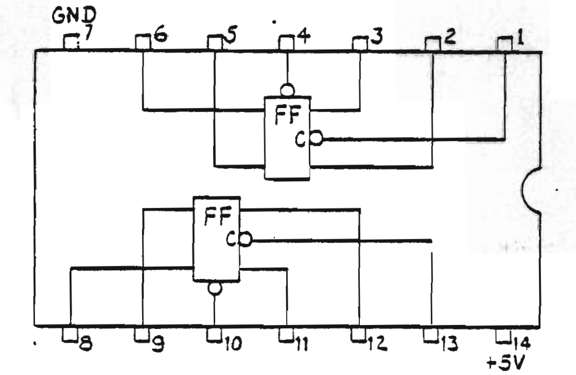
DRN T. ALESI 3/13/72

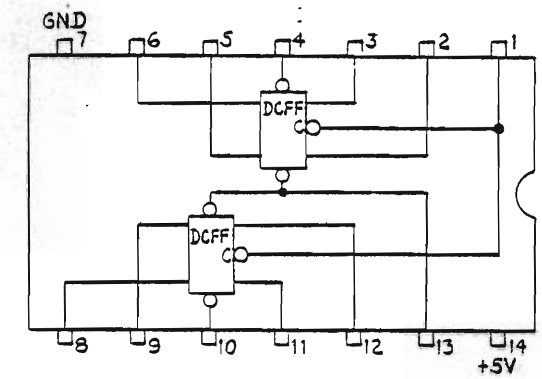
TITLE INTEGRATED CIRCUITS, PART 1

DWG NO. C 6682 DASH NO. -00 SH 1 OF 1

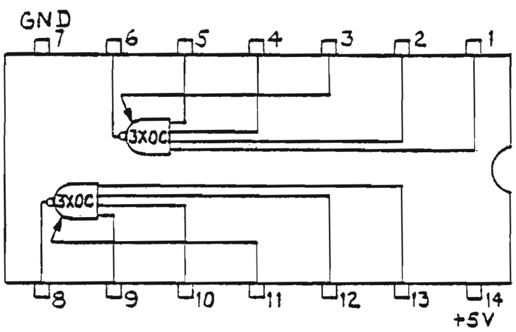
C 6683



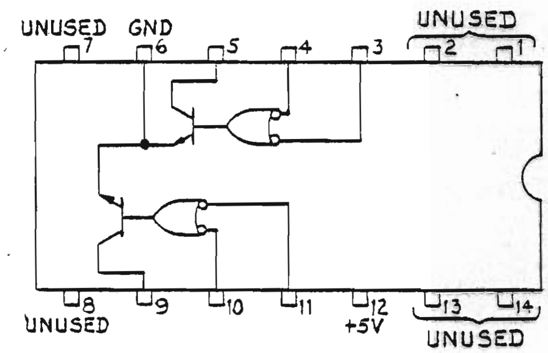
DUAL FLIP-FLOP
P/N 2777-00 (9093)



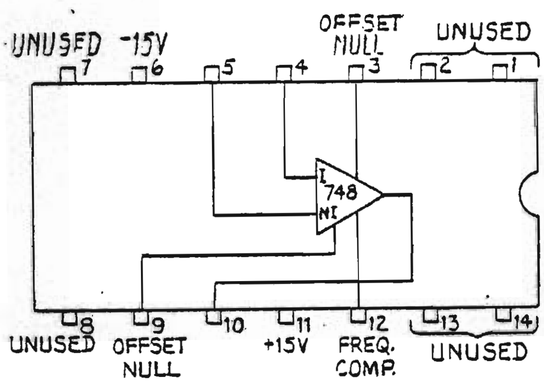
DUAL FLIP-FLOP (COMMON CLOCK)
P/N 2778-00 (9099)



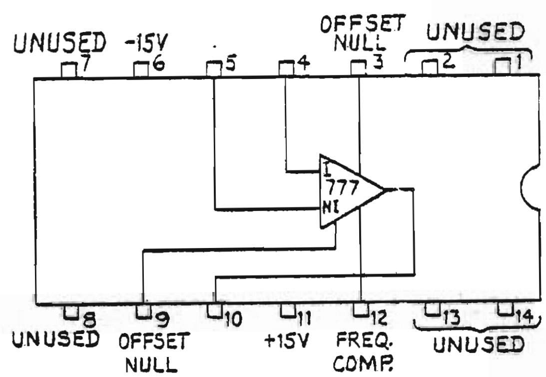
2/4 INPUT POWER GATE
P/N 4045-00 (944)



DUAL DRIVER (PD2)
P/N 4716-00



TYPE 748 OPERATIONAL AMPLIFIER
P/N 3226-00



TYPE 777 OPERATIONAL AMPLIFIER
P/N 4091-00

7545-A-23

A	REVISED PER ECN 1619-1C	12-13-72
—	RELEASE PER ECN #1310-1C	6-16-72
REV	DESCRIPTION	APPVD-DATE

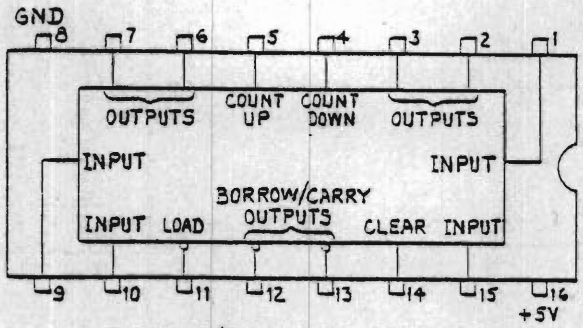
REVISIONS

DRN J. ALESI 6/14/72 ENG 6-16-72
CHK 6-15-72 MFG

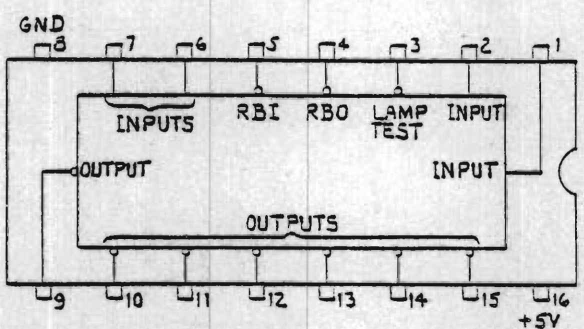
TITLE INTEGRATED CIRCUITS, PART 2

DWG NO. C 6683 DASH NO. -00
SH 1 OF 1

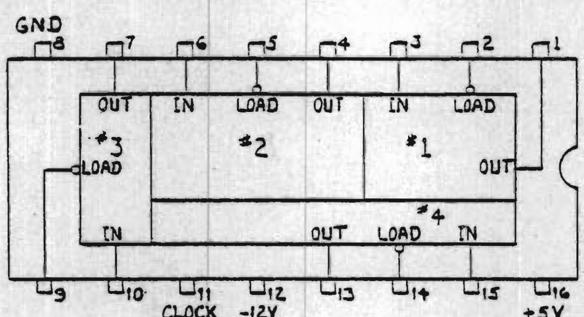
C 8656



BCD UP/DOWN COUNTER
P/N 3232-00 (74192)



SEVEN-SEGMENT DECODER/DRIVER
P/N 5943-00



4×80 BIT SHIFT REGISTER
P/N 7488-00

REV	DESCRIPTION	APPVD-DATE
-	RELEASE PER ECN 2125-IP	6-15-73

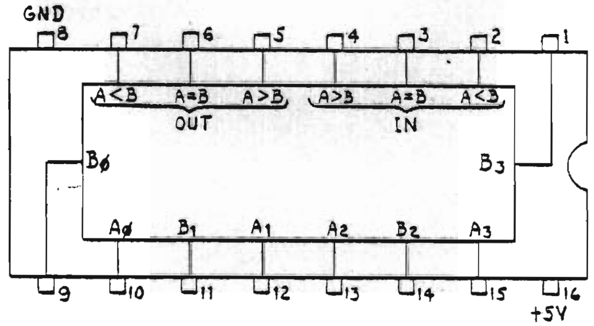
REVISIONS

7545-A-24

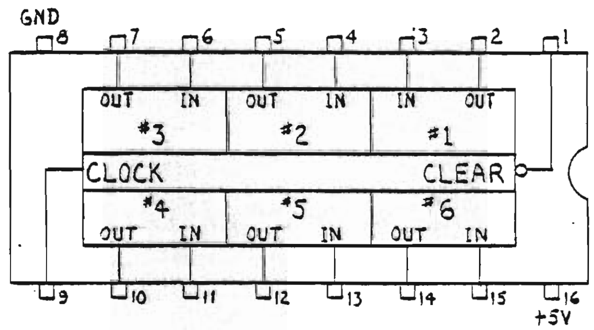
DRN J. ALESI 12/10/72 ENG 6-15-73
CHK 5/19/73 MFG

TITLE INTEGRATED CIRCUITS, PART 3

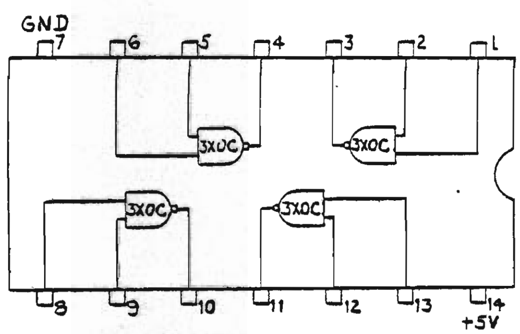
DWG NO. C 8656 DASH NO -00 SH 1 OF 1



4-BIT COMPARATOR
P/N 5023-00 (7485)



HEX FLIP-FLOP
P/N 5022-00 (74174)



4/2 INPUT POWER GATE
P/N 8651-00 (958)

REV	DESCRIPTION	APPVD-DATE
—	RELEASE PER ECN 1619-1C	8-12-4-72

REVISIONS

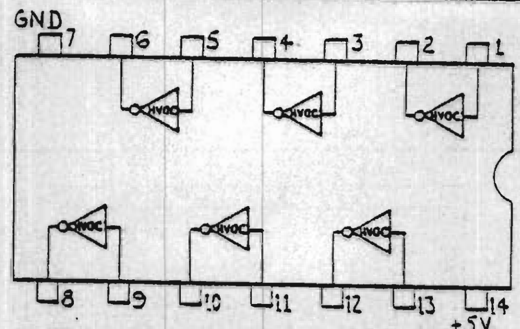
7545 - A - 25

DRN J. ALESI 8/25/72 ENG 8-25-72
CHK 8/25/72 MFG

TITLE INTEGRATED CIRCUITS, PART 4

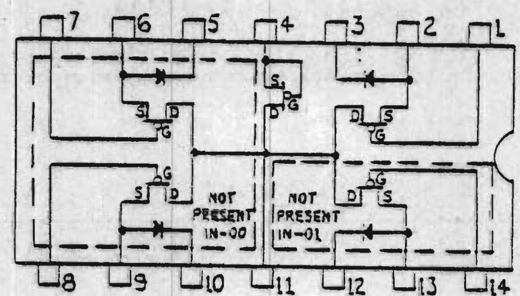
DWG NO. C 7281 DASH NO. -00 SH 1 OF 1

C 9618



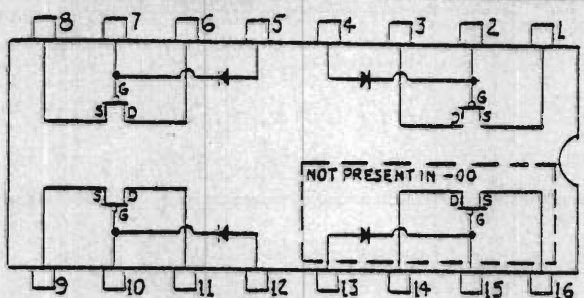
OPEN COLLECTOR HEX INVERTER

P/N 9533-00 (7406)



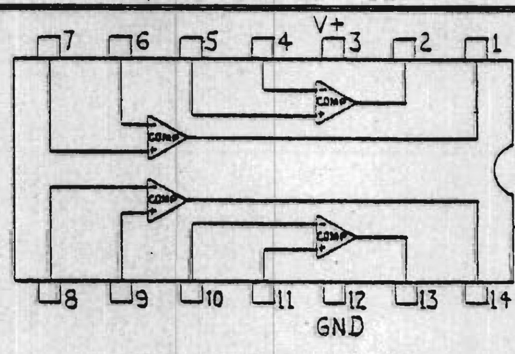
ANALOG SWITCH

P/N 8894-00, -01 & -02



ANALOG SWITCH

P/N 9553-00 & -01



QUAD COMPARATOR

P/N 9207-00

7545 - A - 26

DRN J. ALESI 5-19-73	ENG [signature] 6-1-73
CHK [signature] 5-29-73	MFG [signature]

TITLE: INTEGRATED CIRCUITS, PART 5

REV	DESCRIPTION	APPVD-DATE
-	RELEASE PER ECN 2125-1P	JD 6-15-73

REVISIONS

DWG NO. C 9618 DASH NO. -00 SH 1 OF 1

E 79-082

1.	9928, Keyboard Translate
2.	5096, Keyboard Data
3.	5098, Keyboard Functions
4.	8878, Motion Control, Part 1
5.	8880, Punch Control
6.	3282, Mode Control
7.	7492, Program Memory (4) 7490, Program Memory (8) (Opt. Feat.)
8.	9594, Motion Control, Part 2
9.	5108, Input Memory
10.	5110, Read Control
11.	7440, Program Revert 7404, Program Revert and 51 col. (Opt. Feat.)
12.	5114, Punch Data
13.	5116, Punch Check (Optional Feature)
14.	5118, DD Intf., Pt. 1 8818, DD Intf., 50 Ft., Pt. 1 (Opt. Feat.)
15.	5120, DD Intf., Pt. 2 8820, DD Intf., 50 Ft., Pt. 2 (Opt. Feat.)
16.	5122, Printer Memory
17.	5124, Printer Drivers
18.	5126, Self Check, Mod 10 or 11 (Optional Feature)
19.	4110, Interspersed (Optional Feature)
20.	2012, Maintenance

QTY	CARD PT. NO.	DESCRIPTION	LOCATION
1	7396	Solenoid Driver	Above chip box
1	9592	Input Driver	Above chip box
1	2306	Control Panel	Operator Panel
1	5586	Motor Amplifier	Mechanism
1	6954	Printer Hammer Drivers 2	Above chip box

7545-A-27

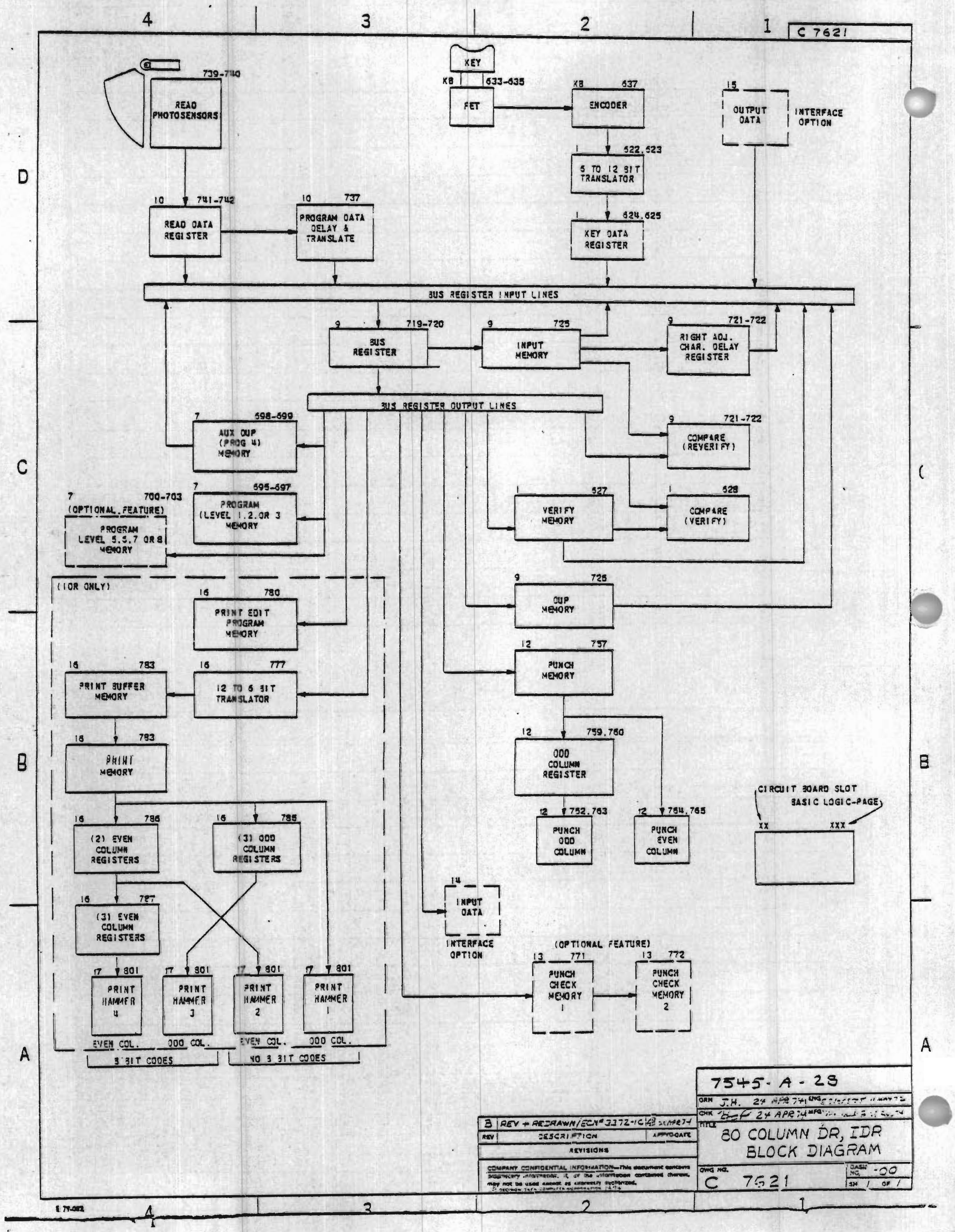
REV	DESCRIPTION	APPVD-DATE
B	Rev/ECN 2125-8C	[illegible] 28 NOV 73
A	Rev/ECN 2125-3P	RVK 31 JUL 73
–	Rel. ECN 2125-2P	RVK 30 JUL 73

REVISIONS

DRN	J. Alesi 26 JUL 73	ENG	RVK 30 JUL 73
CHK	H. Porter 26/7/73	MFG	RSB 30 JUL 73

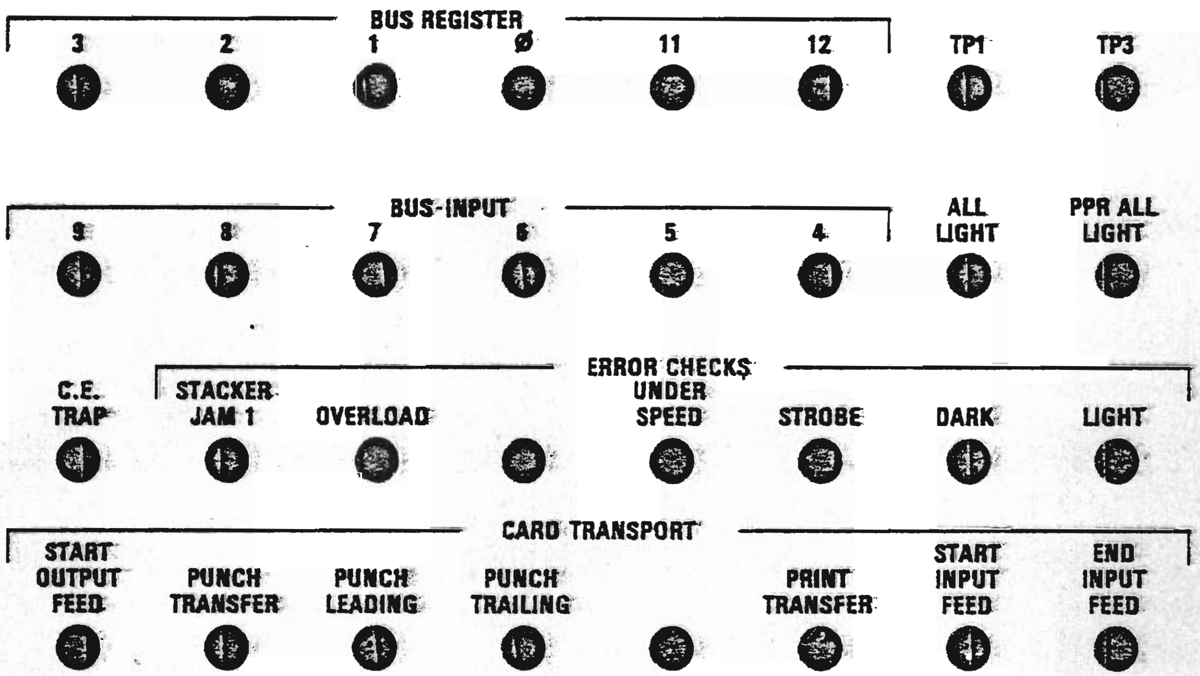
TITLE: CARD PLACEMENT, DD
80 COL DR, IDR

DWG NO. A 9596 — DASH NO. -00 — SH 1 OF 1


C 7621
KEY
KB 633-635
FET
KB 637
ENCODER
15
OUTPUT DATA
INTERFACE OPTION
739-740
READ PHOTOSENSORS
1 622,623
6 TO 12 BIT TRANSLATOR
1 624,625
KEY DATA REGISTER
10 741-742
READ DATA REGISTER
10 737
PROGRAM DATA DELAY & TRANSLATE
BUS REGISTER INPUT LINES
9 719-720
BUS REGISTER
9 725
INPUT MEMORY
9 721-722
RIGHT ADJ. CHAR. DELAY REGISTER
BUS REGISTER OUTPUT LINES
7 698-699
AUX DUP (PROG 4) MEMORY
9 721-722
COMPARE (REVERIFY)
7 695-697
PROGRAM (LEVEL 1,2,OR 3 MEMORY
7 700-703
(OPTIONAL FEATURE)
PROGRAM LEVEL 5,6,7 OR 8 MEMORY
1 627
VERIFY MEMORY
1 628
COMPARE (VERIFY)
(IDR ONLY)
16 780
PRINT EDIT PROGRAM MEMORY
9 726
DUP MEMORY
16 783
PRINT BUFFER MEMORY
16 777
12 TO 6 BIT TRANSLATOR
12 757
PUNCH MEMORY
16 783
PRINT MEMORY
12 759,760
ODD COLUMN REGISTER
CIRCUIT BOARD SLOT
BASIC LOGIC-PAGE
XX XXX
16 786
(2) EVEN COLUMN REGISTERS
16 785
(3) ODD COLUMN REGISTERS
12 762,763
PUNCH ODD COLUMN
12 764,765
PUNCH EVEN COLUMN
16 787
(3) EVEN COLUMN REGISTERS
14
INPUT DATA
INTERFACE OPTION
(OPTIONAL FEATURE)
13 771
PUNCH CHECK MEMORY 1
13 772
PUNCH CHECK MEMORY 2
17 801
PRINT HAMMER 4
PRINT HAMMER 3
PRINT HAMMER 2
PRINT HAMMER 1
EVEN COL.
ODD COL.
EVEN COL.
ODD COL.
3 BIT CODES
NO 3 BIT CODES
7545-A-28
DRN J.H. 24 APR 74
CHK 24 APR 74
B REV + REDRAWN/ECN# 3372-IC
REV DESCRIPTION APPVD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION—This document contains proprietary information. It, or the information contained therein, may not be used except as otherwise authorized.
TITLE
80 COLUMN DR, IDR BLOCK DIAGRAM
DWG NO. C 7621
DASH NO. -00
SH 1 OF 1
E 79-082

8001/10 DATA RECORDER

MAINTENANCE DIAGNOSTIC PANEL



MOTOR ON

INPUT MEM TO BUS

HI SPEED SERVO ON

SPECIAL SWITCH 1

LO SPEED SERVO ON

INHIBIT FIRING

READ CARD

SELECT STACKER 2

CLOCK CONTROL

CLOCK STEP

CARD RELEASE

PUNCH CHECK RESET

PROGRAM TO BUS

SPECIAL SWITCH 2

CLEAR INDICATORS

GEN CLEAR

I. SWITCHES—Note: Switches are inactive in up position.

A. MOTOR ON—Overrides the AC motor turn off delay.

B. INPUT MEM TO BUS—Allows a character of input memory to be gated onto the input bus lines and displayed by the twelve bus register indicators on the panel. The contents of the input memory can be viewed by placing this switch down and depressing the space bar. Original memory content is retained.

C. HI SPEED SERVO ON—Activates position servo at high speed to permit customer engineer speed check. GEN CLEAR (switch S) stops servo.

D. SPECIAL SW. #1—No defined use in basic machine, but may be used in special configurations.

E. LO SPEED SERVO ON—Activates position servo to permit the customer engineer to set the speed.

F. INHIBIT FIRING—Inhibits firing of the print hammers. The printer logic operates normally.

G. READ CARD—Used with the CARD RELEASE switch. (See L.) When the switch is down, all cards released are read and the information stored in the input memory.

H. SELECT STACKER 2—Forces all cards fed into stacker number 2.

J. CLOCK CONTROL—Inhibits generation of normal clock pulses. Used with clock step switch. (See K.) Programs must be reloaded after use of this switch.

K. CLOCK STEP—Steps clock controls on a manual, single cycle basis. Clock control switch must be active. Memory contents are not retained.

L. CARD RELEASE—A momentary action switch which feeds a card.

M. PUNCH CHECK RESET—Resets the punch data check flip flop when setting up the post punch read station. Interfaced machines can also reset the lamp via the error reset key when OFF LINE.

N. PROGRAM TO BUS—Forces program bits to BUS REGISTER (program switch on and AUTO SKIP/DUP switch off). Single depression of REL key in PUNCH MODE will punch program.

P. SPECIAL SW. #2—No defined use in basic machine, but may be used in special configurations.

R. CLEAR INDICATORS—Clears all maintenance panel indicators and C.E. TRAP FF2.

S. GEN CLEAR—Forces General Clear—equivalent to General Clear generated at power turn-on.

II. INDICATORS

A. BUS REGISTER (6)—Displays the current contents of input bus register, bits 12, 11, 0, 1, 2 and 3. If the PROGRAM TO BUS switch is active, the selected program contents can be viewed. The column counter displays the column in view. The space bar is used to index to the column to be displayed. See para. 3.2 C for the lower order bits.

B. TP1 & TP3 (2)—These indicators normally glow—they are actually turning on and off following the internal clock pulses TP1 & TP3 (a quick indication that the internal clock is running).

C. BUS INPUT (6)—These indicators display the data bits on the bus input lines, 4, 5, 6, 7, 8 and 9. See para. 3.2 A for the display of the high order bits.

D. ALL LIGHT—This indicator glows when all 12 phototransistors of the pre-punch read station see light.

E. PPR ALL LIGHT—This indicator glows when all 12 phototransistors of the post punch read station see light.

F. C. E. TRAP—A low signal on pin 71 is trapped by a flip-flop. The flip-flop is cleared by the CLEAR INDICATORS switch. If the CLEAR INDICATORS switch is held in the down position (low), then the indicator serves as a level detector for the signal on pin 71.

G. ERROR CHECKS:

(1) STACKER JAM 1—Indicates if a card remains under the post-print card detector for approximately 3 seconds.

(2) OVERLOAD—Indicates if DC incrementing motor is unable to turn when directed.

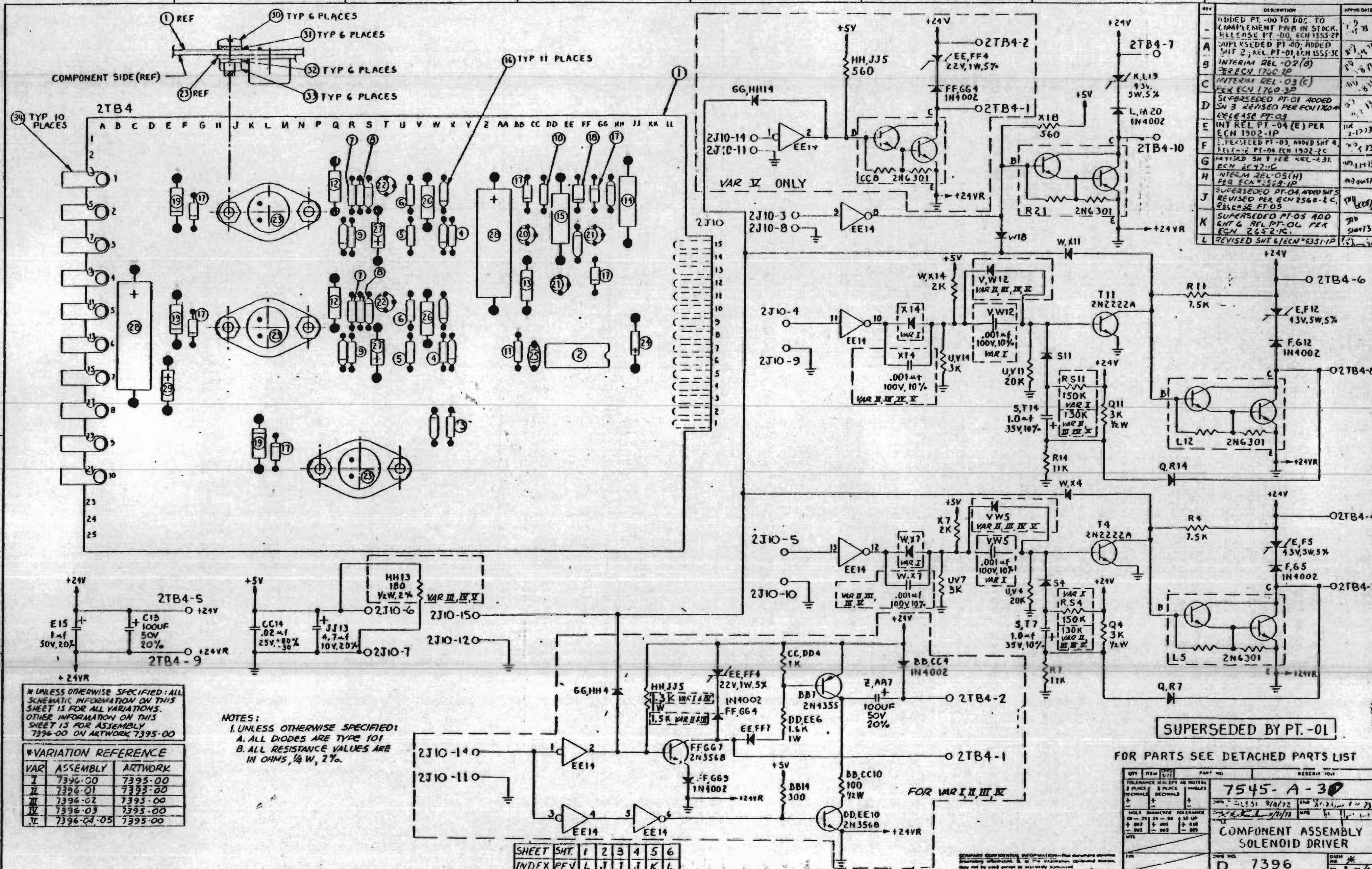
(3) UNDER SPEED—Indicates that DC motor increments too slowly. This incident causes a feed check.

(4) STROBE—Indicates if a read check was caused by a strobing error.

(5) DARK—Indicates if a read check was caused by all phototransistors not seeing dark when a card passes the read station.

(6) LIGHT—Indicates if a read check was caused by all phototransistors not seeing light at the time a feed command is given.

H. CARD TRANSPORT (7)—These indicators light during feed of a card if a particular event took place. (See Fig. 1 for event headings). They remain lit until cleared by the CLEAR INDICATORS SWITCH.



REV	DESCRIPTION	APPVD DATE
–	ADDED PT. -00 TO DOC. TO COMPLEMENT PWB IN STOCK. RELEASE PT. -00, ECN 1555-2P	
A	SUPERSEDED PT -00; ADDED SHT 2; REL. PT-01, ECN 1555-3C	
B	INTERIM REL -02 (B) PER ECN 1760-2P	
C	INTERIM REL -03 (C) PER ECN 1760-3P	
D	SUPERSEDED PT-01 ADDED SH 3 REVISED PER ECN 1760 RELEASE PT-03	
E	INT REL PT -04 (E) PER ECN 1902-1P	
F	SUPERSEDED PT-03, ADDED SHT 4, RELEASE PT-04 ECN 1902-2C	
G	REVISED SH 4 PER REC-431, ECN 2542-1C	
H	INTERIM REL-05 (H) PER ECN 2568-1P	
J	SUPERSEDED PT-04, ADDED SHT 5 REVISED PER ECN 2568-2C, RELEASE PT-05	
K	SUPERSEDED PT-05 ADD SHT 6 REL PT-06 PER ECN 2652-1C.	
L	REVISED SHT 6/ECN 2351-1P	

* UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 7396-00 ON ARTWORK 7395-00

NOTES:
1. UNLESS OTHERWISE SPECIFIED:
   A. ALL DIODES ARE TYPE 101
   B. ALL RESISTANCE VALUES ARE IN OHMS, 1/4 W, 2%.

*VARIATION REFERENCE

VAR	ASSEMBLY	ARTWORK
I	7396-00	7395-00
II	7396-01	7395-00
III	7396-02	7395-00
IV	7396-03	7395-00
V	7396-04-05	7395-00

SHEET INDEX	SHT.	1	2	3	4	5	6
	REV.	L	J	J	J	K	L

SUPERSEDED BY PT. -01

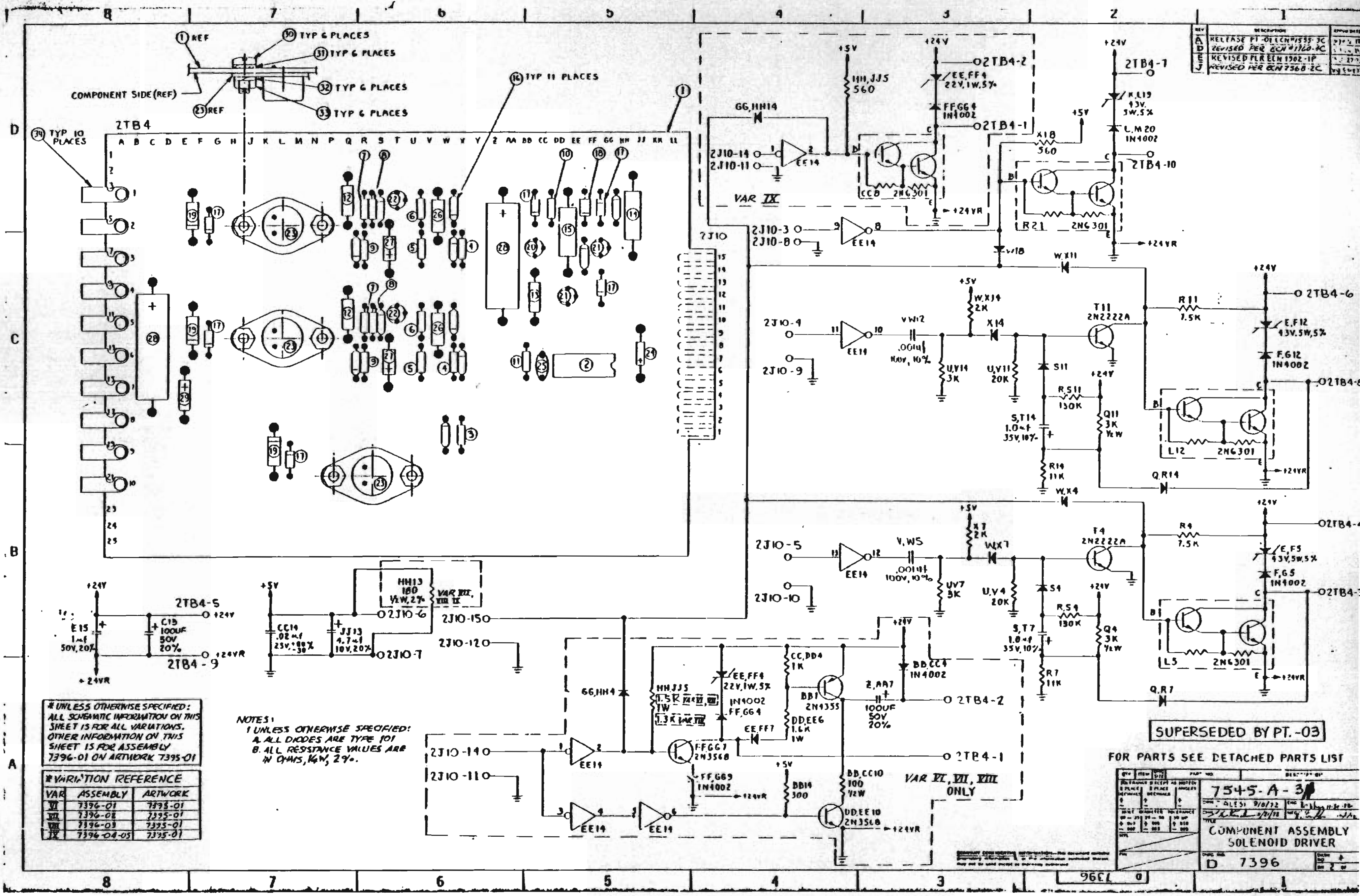
FOR PARTS SEE DETACHED PARTS LIST

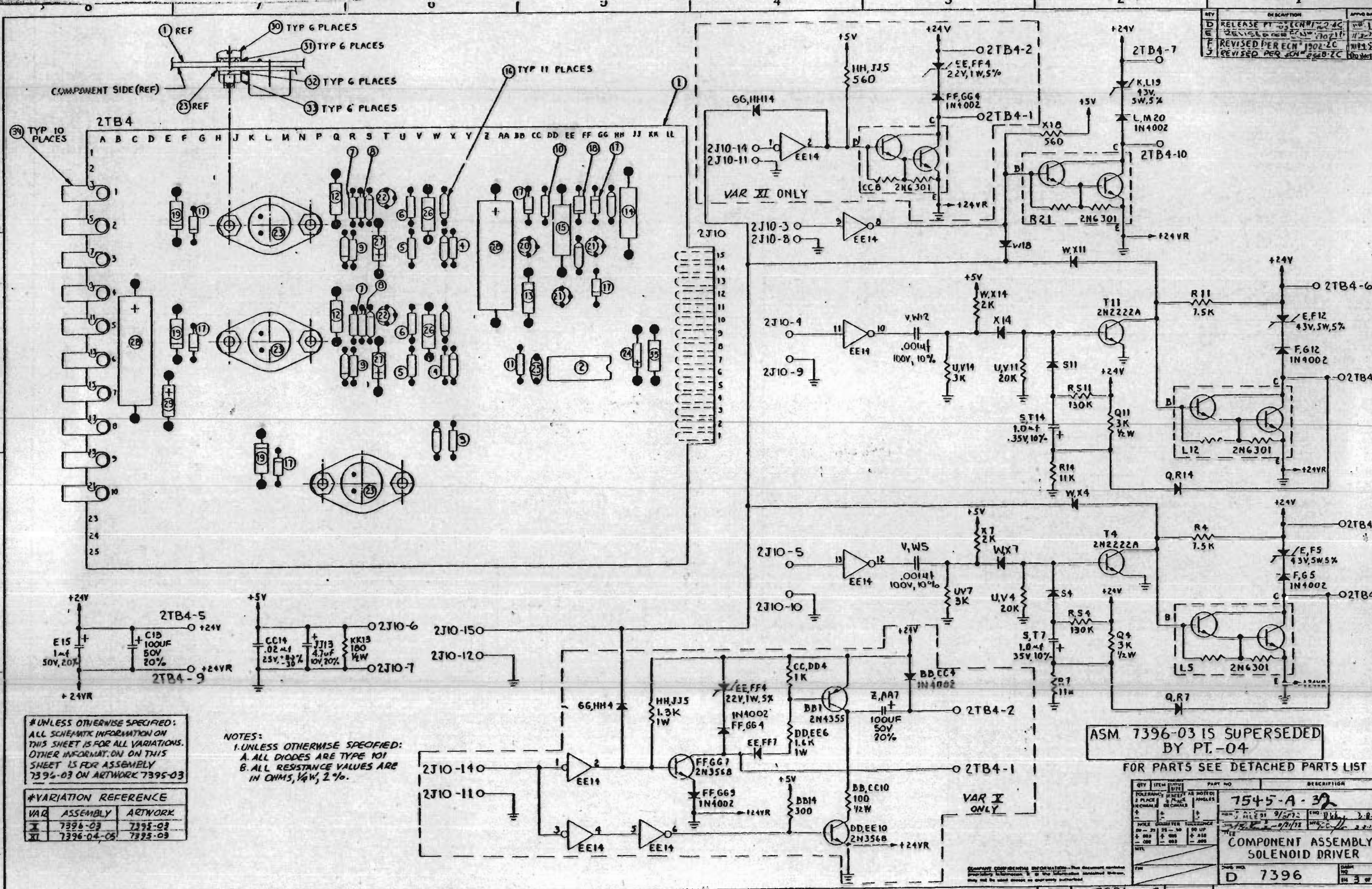
7545-A-3

COMPONENT ASSEMBLY SOLENOID DRIVER

D 7396

SH 1 OF 6


* UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 7396-01 ON ARTWORK 7395-01
* VARIATION REFERENCE
VAR | ASSEMBLY | ARTWORK
VI | 7396-01 | 7395-01
VII | 7396-02 | 7395-01
VIII | 7396-03 | 7395-01
IX | 7396-04-05 | 7395-01
NOTES:
1 UNLESS OTHERWISE SPECIFIED:
A. ALL DIODES ARE TYPE 101
B. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.
COMPONENT SIDE (REF)
TYP 6 PLACES
TYP 11 PLACES
TYP 10 PLACES
2TB4
2J10
VAR IX
VAR VI, VII, VIII ONLY
SUPERSEDED BY PT.-03
FOR PARTS SEE DETACHED PARTS LIST
A RELEASE PT-01 ECN #1535-3C
D REVISED PER ECN #1760-3C
E REVISED PER ECN 1902-1P
J REVISED PER ECN 7368-2C
7545-A-3
COMPONENT ASSEMBLY SOLENOID DRIVER
D 7396



#UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 7396-03 ON ARTWORK 7395-03

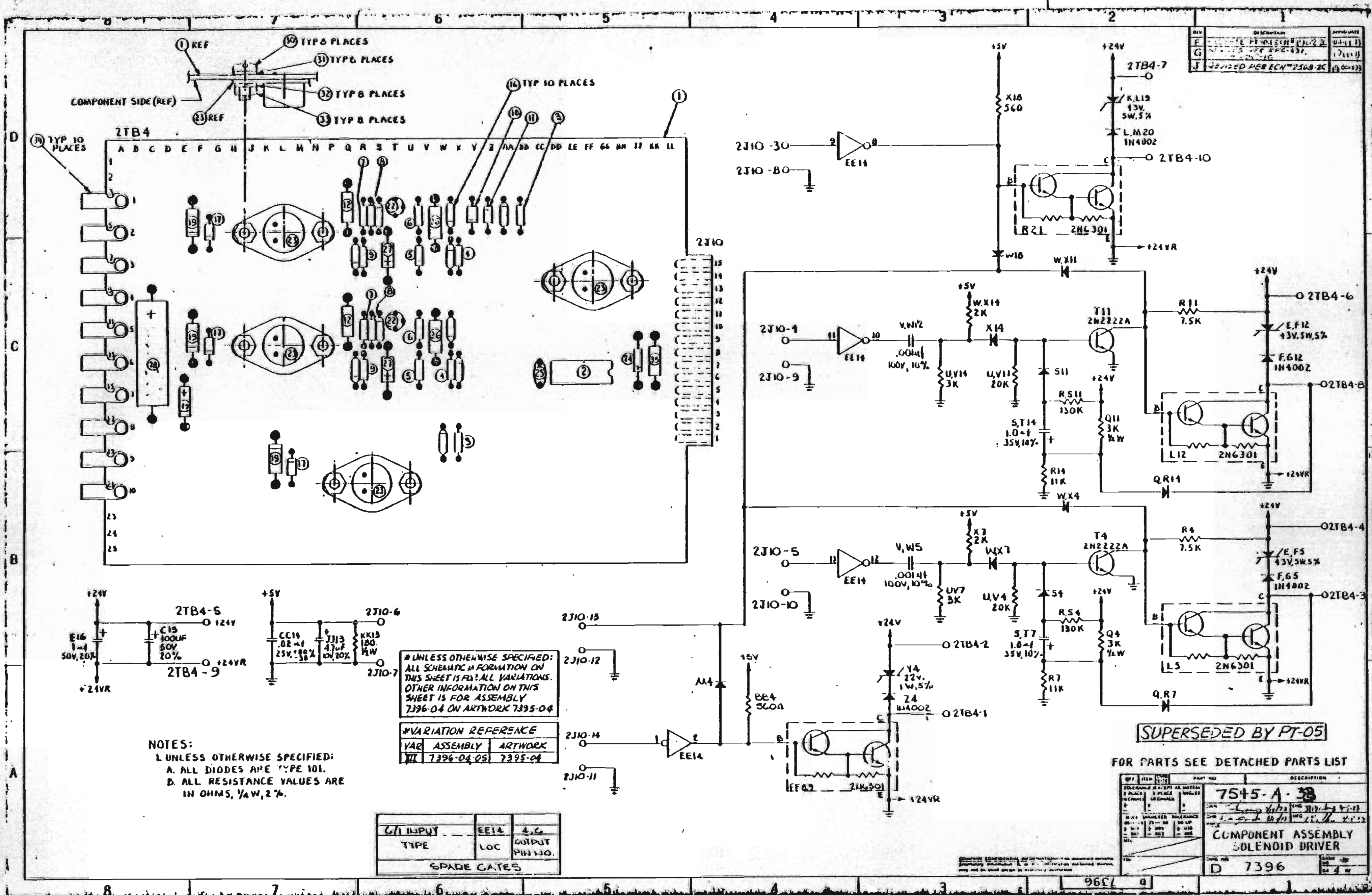
#VARIATION REFERENCE		
VAR	ASSEMBLY	ARTWORK
I	7396-03	7395-03
XI	7396-04,-05	7395-09

NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. ALL DIODES ARE TYPE 101
B. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.

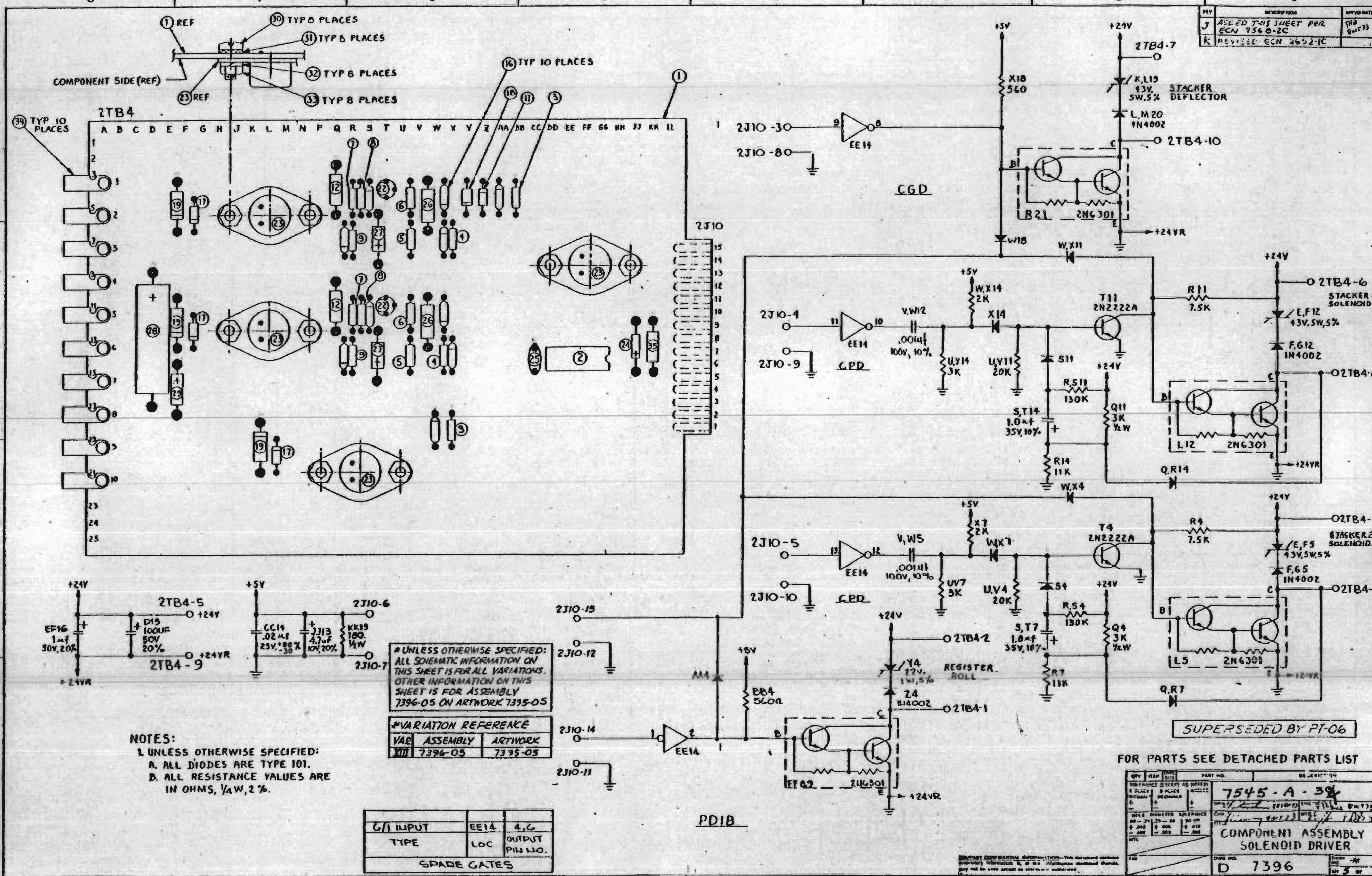
COMPONENT ASSEMBLY SOLENOID DRIVER

7545-A-32

D 7396



COMPONENT SIDE (REF)
2TB4
2J10
2J10-30
2J10-80
2J10-4
2J10-9
2J10-5
2J10-10
2J10-15
2J10-12
2J10-14
2J10-11
2TB4-7
2TB4-10
2TB4-6
2TB4-8
2TB4-4
2TB4-3
2TB4-2
2TB4-1
2TB4-5
2TB4-9
T11 2N2222A
T4 2N2222A
2N6301
EE14
* UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 7396-04 ON ARTWORK 7395-04
*VARIATION REFERENCE
VAR ASSEMBLY ARTWORK
III 7396-04-05 7395-04
NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. ALL DIODES ARE TYPE 101.
B. ALL RESISTANCE VALUES ARE IN OHMS, 1/4 W, 2%.
SPARE GATES
SUPERSEDED BY PT-05
FOR PARTS SEE DETACHED PARTS LIST
7545-A-38
COMPONENT ASSEMBLY SOLENOID DRIVER
D 7396



*UNLESS OTHERWISE SPECIFIED:
ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 7396-05 ON ARTWORK 7395-05

*VARIATION REFERENCE

VAR	ASSEMBLY	ARTWORK
XIII	7396-05	7395-05

NOTES:
1. UNLESS OTHERWISE SPECIFIED:
   A. ALL DIODES ARE TYPE 101.
   B. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.

G/I INPUT	EE14	4,6
TYPE	LOC	OUTPUT PIN NO.
SPARE GATES		

REV	DESCRIPTION	APPROVED
J	ADDED THIS SHEET PER ECN 7568-2C	
K	REVISED ECN 2652-1C	

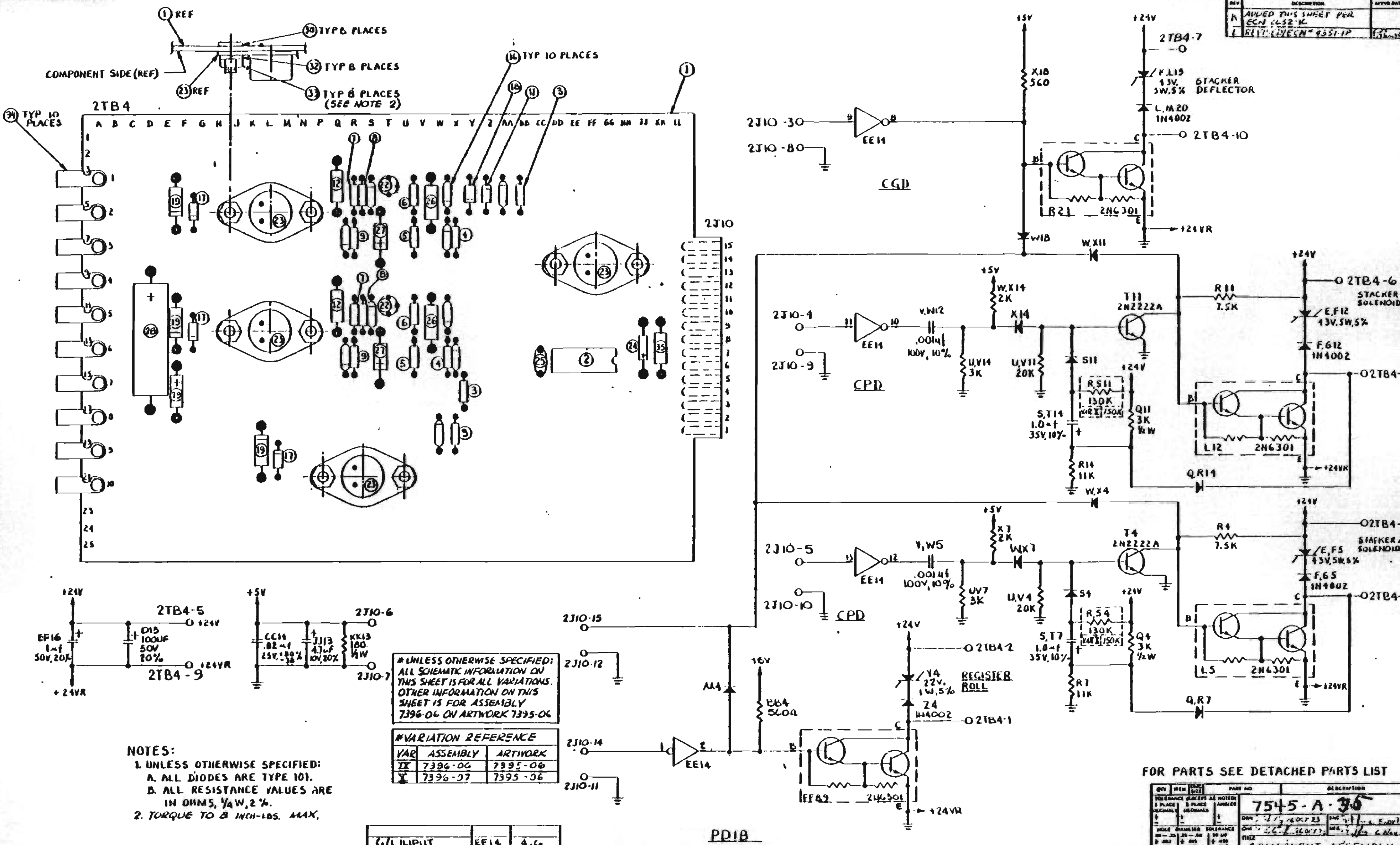
SUPERSEDED BY PT-06

FOR PARTS SEE DETACHED PARTS LIST

7545-A-3

COMPONENT ASSEMBLY
SOLENOID DRIVER

D 7396



REV	DESCRIPTION
K	ADDED THIS SHEET PER ECN 6652-K
L	REV'D PER ECN 4351-1P

*UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 7396-06 ON ARTWORK 7395-06

*VARIATION REFERENCE		
VAR	ASSEMBLY	ARTWORK
IX	7396-06	7395-06
X	7396-07	7395-36

G/1 INPUT	EE14	4,6
TYPE	LOC	OUTPUT PIN NO.
SPARE GATES		

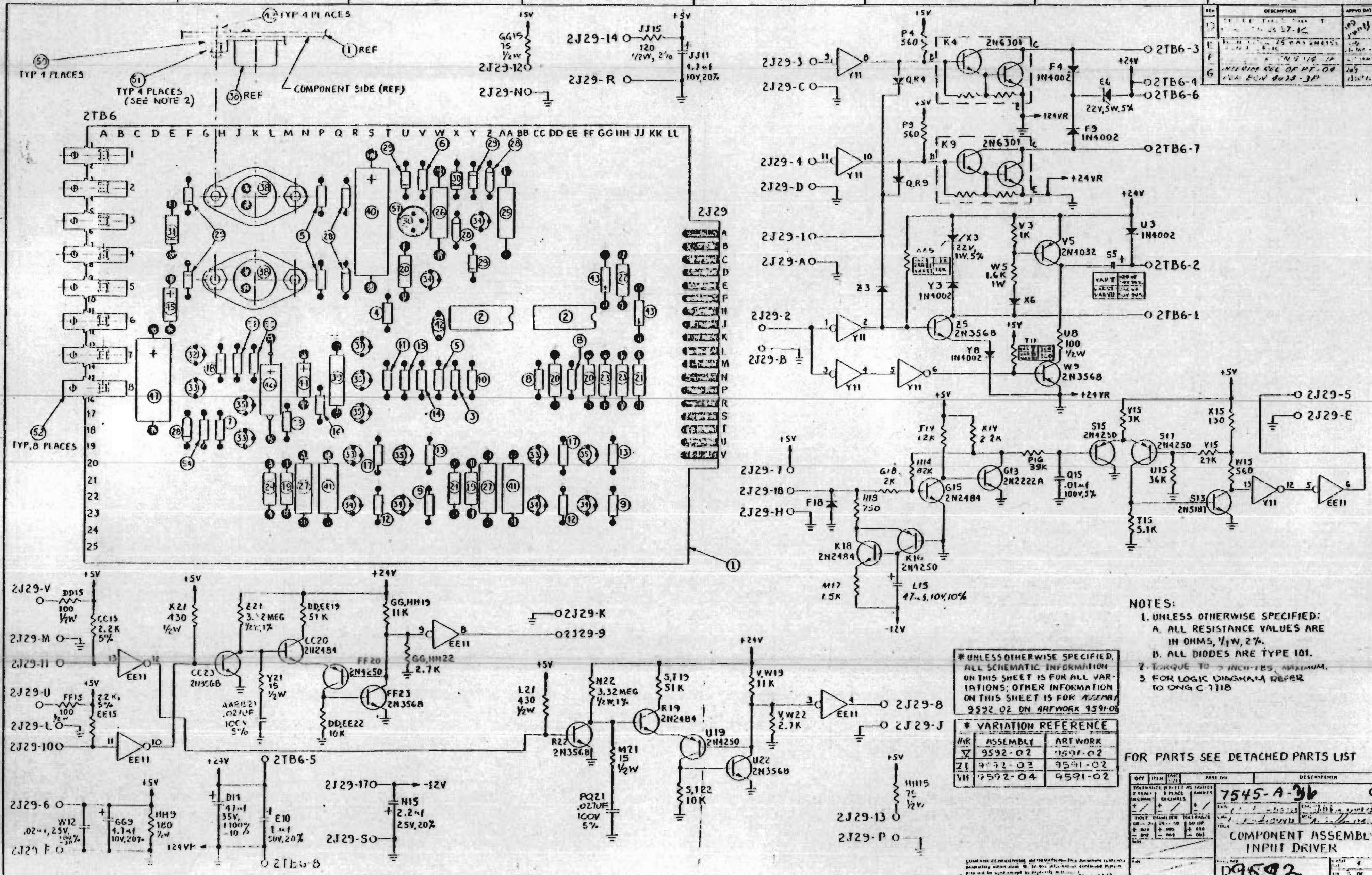
NOTES:
1. UNLESS OTHERWISE SPECIFIED:
   A. ALL DIODES ARE TYPE 101.
   B. ALL RESISTANCE VALUES ARE IN OHMS, 1/4 W, 2%.
2. TORQUE TO 8 INCH-LBS. MAX.

FOR PARTS SEE DETACHED PARTS LIST

7545-A-36

COMPONENT ASSEMBLY SOLENOID DRIVER

D 7396



NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.
B. ALL DIODES ARE TYPE 101.
2. TORQUE TO 5 INCH-LBS. MAXIMUM.
3. FOR LOGIC DIAGRAM REFER TO DWG C-7118

* UNLESS OTHERWISE SPECIFIED, ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS; OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 9592-02 ON ARTWORK 9591-02

* VARIATION REFERENCE

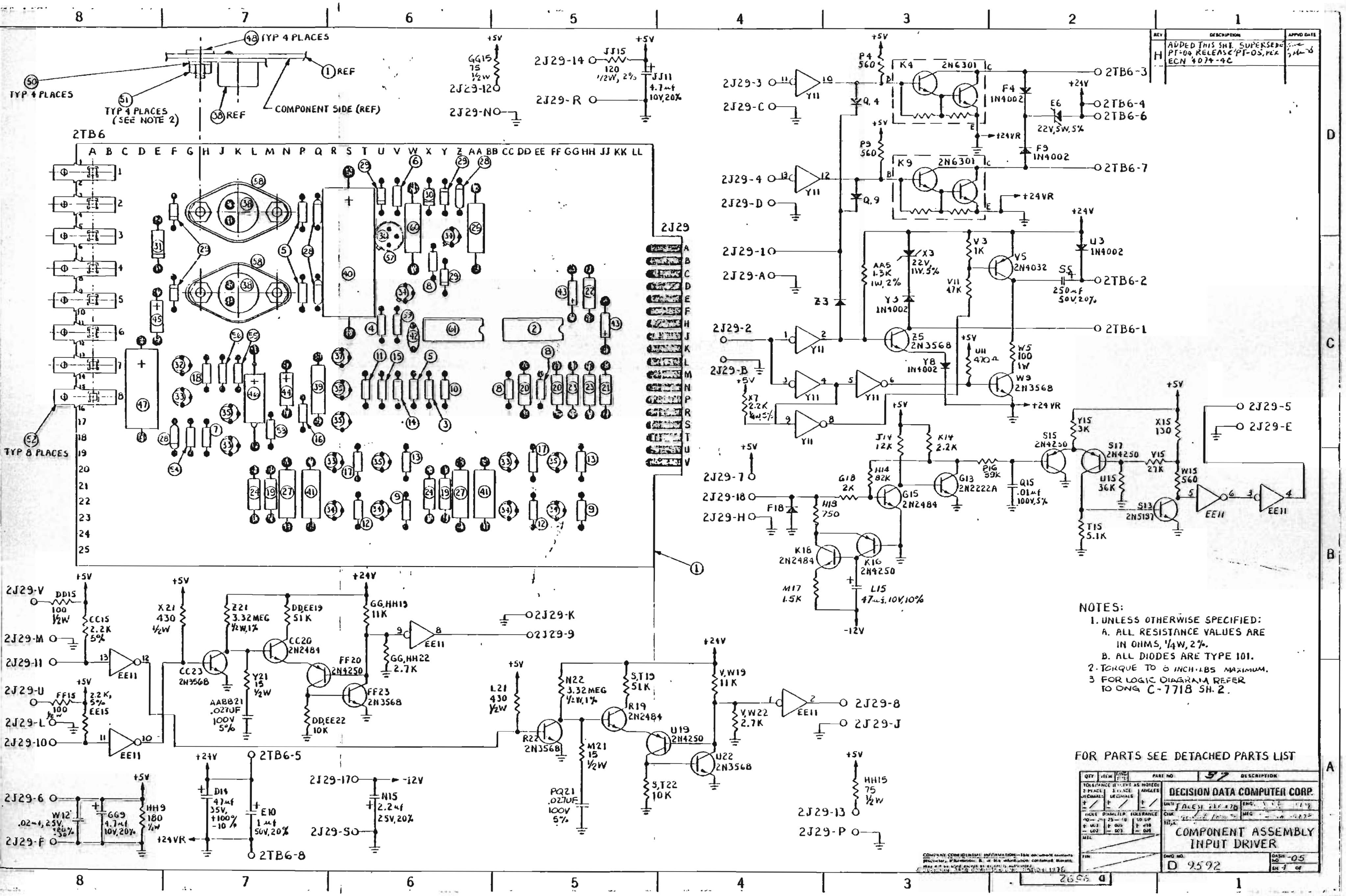
VAR	ASSEMBLY	ARTWORK
V	9592-02	9591-02
VI	9592-03	9591-02
VII	9592-04	9591-02

FOR PARTS SEE DETACHED PARTS LIST

7545-A-36 G

COMPONENT ASSEMBLY
INPUT DRIVER

D9592



NOTES:
1. UNLESS OTHERWISE SPECIFIED:
   A. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.
   B. ALL DIODES ARE TYPE 101.
2. TORQUE TO 6 INCH-LBS MAXIMUM.
3. FOR LOGIC DIAGRAM REFER TO DWG C-7718 SH. 2.

FOR PARTS SEE DETACHED PARTS LIST

REV	DESCRIPTION	APPD DATE
H	ADDED THIS SHT. SUPERSEDES PT-04 RELEASE PT-05, PER ECN 4074-4C	

DECISION DATA COMPUTER CORP.

COMPONENT ASSEMBLY INPUT DRIVER

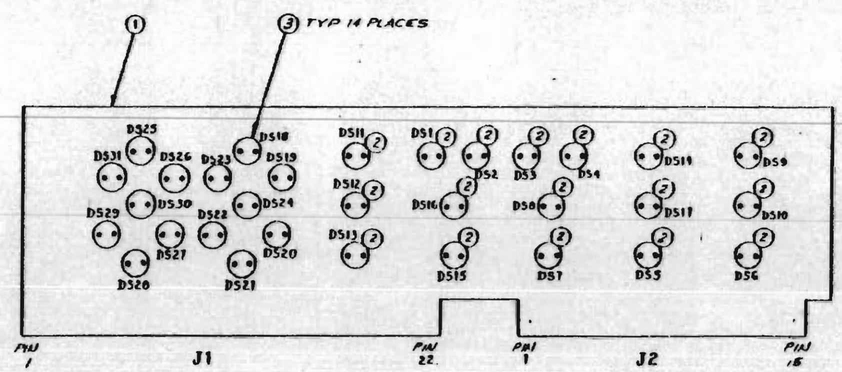
DWG NO. D 9592 -05

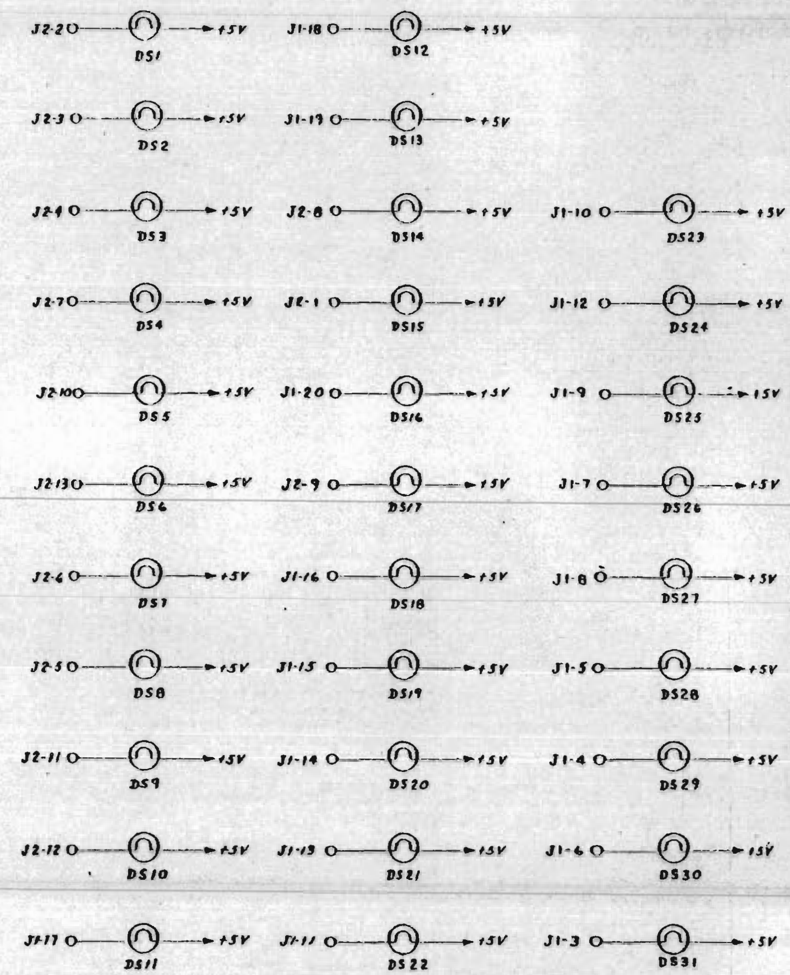
REV	DESCRIPTION	APPROVED
D	[illegible]	[illegible]
E	REVISED/ECN° 2662-1C	[illegible]

* UNLESS OTHERWISE SPECIFIED ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 2306-02 ON ARTWORK 2305-02

*VARIATION REFERENCE		
VAR	ASSEMBLY	ARTWORK
I	2306-00	2305-00
II	2306-01	2305-01
III	2306-02	2305-02

NOTES:
1. FOR LOGIC DIAGRAM REFER TO DWGS 7688, 7689, 7690, 8408, 8409 & 8410
2. NEXT TO AREA DESIGNATED BY WORD "ASSEMBLY", ON SOLDER SIDE OF BOARD, STAMP COMPONENT ASSEMBLY NO. 2306-02 (III).





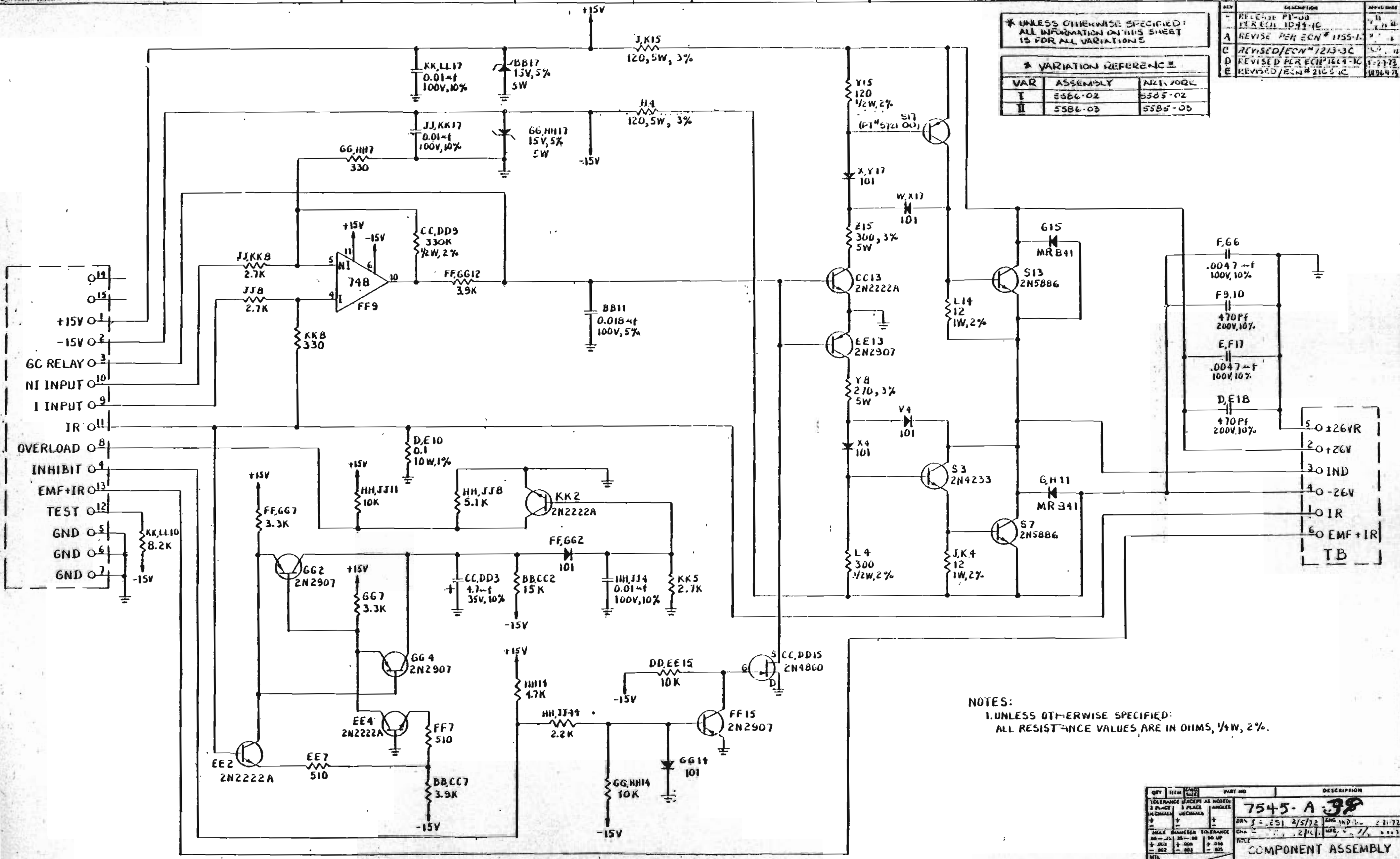
FOR PARTS SEE DETACHED PARTS LIST

7545-A-32

COMPONENT ASSEMBLY
CONTROL PANEL

D 2306

SH 3 ✱



NOTES:
1. UNLESS OTHERWISE SPECIFIED:
ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.

* UNLESS OTHERWISE SPECIFIED: ALL INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS

* VARIATION REFERENCE

VAR	ASSEMBLY	NETWORK
I	5586-02	5585-02
II	5586-03	5585-03

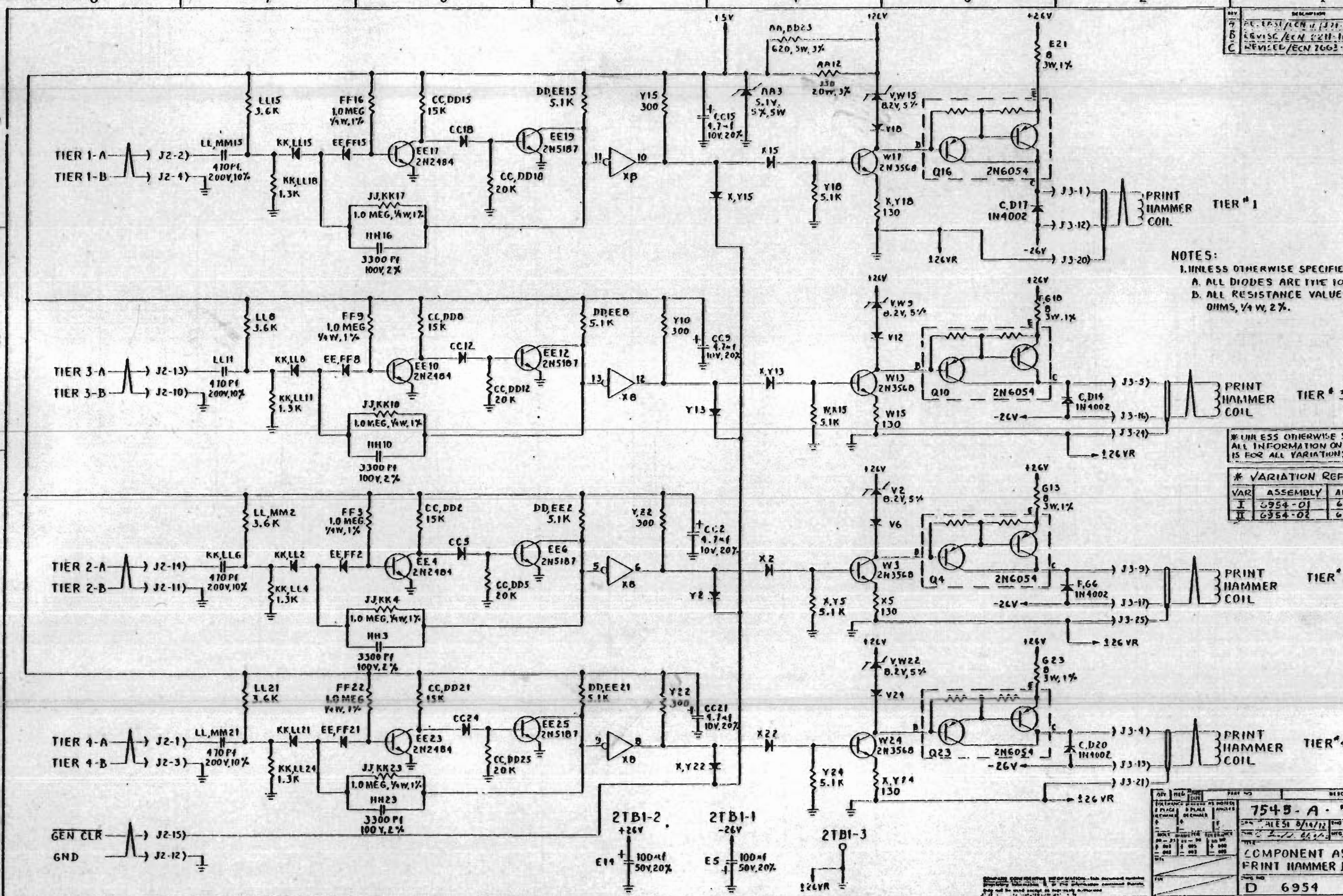
REV	DESCRIPTION	APPROVED
-	RELEASE PT-00 PER ECN 1049-1C	
A	REVISE PER ECN # 1155-1	
C	REVISED/ECN # 1213-3C	
D	REVISED PER ECN # 1664-1C	1-27-72
E	REVISED/ECN # 2105-1C	

7545-A-38

COMPONENT ASSEMBLY
MOTOR AMPLIFIER

D 5586

SH 2 OF 2



NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. ALL DIODES ARE TYPE 101.
B. ALL RESISTANCE VALUES ARE IN OHMS, 1/4 W, 2%.

* UNLESS OTHERWISE SPECIFIED: ALL INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS

* VARIATION REFERENCE		
VAR	ASSEMBLY	ARTWORK
I	6954-01	6953-01
II	6954-02	6953-02

REV	DESCRIPTION	APPROVED
A	RE-LEASE/ECN ...	...
B	REVISE/ECN 2211-IC	
C	REVISED/ECN 2663-IC	

7545 - A - 4

COMPONENT ASSEMBLY
PRINT HAMMER DRIVER 2

D 6954

BASIC LOGIC CONTENTS

TYPE NUMBER/NAME: 8001, 8010 DATA RECORDER

P.C.B. LOC. / LOGIC NO.	P.C. BOARD TITLE / LOGIC DRAWING TITLE	ART. NO. / P.C.B. NO.	ARTWORK DASH NUMBER / P.C. BOARD ASSEMBLY DASH NUMBER / LOGIC DRAWING REVISION									
		5093-xx	01	01	01	03	03	04				
	KEYBOARD TRANSLATE	5094-xx	01	02	03	03	04	04				
1		9927-xx							00	00		
	KEYBOARD TRANSLATE	9928-xx							00	00		
6314 Sh. 1	Keyboard Translate R.O.M.		–	–	–	–	–	–	–			
6314 Sh. 2	Keyboard Translate R.O.M.		–	–	–	–	–	–	–			
6314 Sh. 3	Keyboard Translate R.O.M.		–	–	–	–	–	–	–			
6314 Sh. 4	Keyboard Translate R.O.M.		–	–	–	–	–	–	–			
7620 Sh. 1	Keyboard Translate R.O.M.									–		
7620 Sh. 2	Keyboard Translate R.O.M.									–		
7620 Sh. 3	Keyboard Translate R.O.M.									–		
7620 Sh. 4	Keyboard Translate R.O.M.									–		
7620 Sh. 5	Keyboard Translate R.O.M.									–		
7620 Sh. 6	Keyboard Translate R.O.M.									–		
7620 Sh. 7	Keyboard Translate R.O.M.									–		
7620 Sh. 8	Keyboard Translate R.O.M.									–		
7622	Keyboard Translate, Part 1		–	–	–	–	–	–	A	A		
7623	Keyboard Translate, Part 2		–	–	A	A	A	A	B	B		
7624	Keyboard Data Register, Part 1		–	A	A	A	B	B	C	C		
7625	Keyboard Data Register, Part 2		–	–	–	–	–	–	A	A		
7626	Verify Memory Control		–	–	A	A	B	B	C	C		
7627	Verify Memory		–	–	A	A	A	A	B	B		
7628	Verify Comparator		–	–	–	–	–	–	A	A		
7629	Memory To Bus		–	–	–	–	–	–	A	A		
7630	Display Column Counter		–	–	–	A	A	A	B	B		
7631	Field Erase Counter		–	–	–	–	–	–	A	A		
7632	Inhibit Secondary Feed		–	–	–	–	A	B	C	C		
7633	Keyboard Data Keys, Part 1		–	–	–	–	–	A	B	B		
7634	Keyboard Data Keys, Part 2		–	–	–	–	–	A	B	B		
7635	Keyboard Data Keys, Part 3		–	–	–	–	–	A	B	B		
2		5095-xx	01	01	02	02	03	03	04	04	05	06
	KEYBOARD DATA	5096-xx	01	02	02	03	03	04	04	05	05	06
7636	Keyboard Sprocket Control		–	–	B	C	D	D	D	E	F	H
7637	Keyboard Data Encode		–	–	–	–	A	A	B	B	C	C
7638	Keyboard Function Keys, Part 1		–	–	–	A	B	C	C	C	C	C
7639	Keyboard Function Keys, Part 2		–	–	–	–	A	A	A	A	A	A
7640	Function Insert Control		–	–	–	–	A	A	A	A	A	B
7641	Output Release Control		–	–	B	C	D	E	E	E	F	F
7642	Read Alert Control		–	–	B	C	D	D	D	E	F	F
7643	Keyboard Shift Control		–	–	B	C	D	D	F	G	H	H
7644	Dup/Skip Logic		–	–	–	A	B	B	B	B	C	C
7645	Keyboard Switches		–	–	A	A	B	C	C	C	D	D
7646	Clicker Control		–	–	A	A	A	A	A	A	A	B

DR80-B-1

DWG. NO. A 7549 REV D SH 1 OF 8

E88-112

BASIC LOGIC CONTENTS

TYPE NUMBER/NAME: 8001, 8010 DATA RECORDER

P.C.B. LOC. / LOGIC NO.	P.C. BOARD TITLE / LOGIC DRAWING TITLE	ART. NO. / P.C.B. NO.	ARTWORK DASH NUMBER / P. C. BOARD ASSEMBLY DASH NUMBER / LOGIC DRAWING REVISION									
3		5097-xx	01	01	01	03	03	03	03	03	07	07
	KEYBOARD FUNCTIONS	5098-xx	01	02	03	03	04	05	06	07	07	07
7647	Backspace, Field Erase FF's		-	-	A	A	A	A	A	A	A	B
7648	Backspace Counter		-	-	-	-	-	-	-	A	A	A
7649	Punch Right Adjust, Part 1		-	-	A	B	C	C	C	D	E	E
7650	Punch Right Adjust, Part 2		-	-	-	A	A	A	A	A	A	A
7651	Error Control		-	-	A	B	B	B	B	B	D	D
7652	Verify Right Adjust, Part 1		-	-	A	B	C	C	C	D	E	E
7653	Verify Right Adjust, Part 2		-	-	-	A	A	A	A	A	B	B
7654	Verify Correct Control		-	-	-	A	A	A	B	C	C	C
7655	Reverify Control		-	-	A	B	B	B	B	B	B	B
7656	First Column Home FF		-	A	B	C	C	D	E	F	G	G
7657	Slew, Step Control		-	-	-	-	-	-	A	A	A	A
		5099-xx										
4	MOTION CONTROL, PART 1	5100-xx										
		8877-xx	For previous information	refer to Sheet 7	10							
	MOTION CONTROL, PART 1	8878-xx			10							
7658	Clock Oscillator & Counter				E							
7659	Clock Output Gating				D							
7660	Punch Sprocket				E							
7661	Card Tracking Flip-Flops				B							
7662	Stacker Jam Detection				C							
7663	Motor Jam Check				D							
7664	General Clear				D							
7665	Position Control Flip-Flops				F							
7666	Position Pulse Generation				F							
7667	Position Difference				G							
7668	Position Sensor				D							
7669	Position Motor Control				F							
		5111-xx	01	01								
5	PUNCH CONTROL	5112-xx	01	02								
		8879-xx			00	00	01	01	02	02	03	
	PUNCH CONTROL	8880-xx			00	01	01	02	02	03	03	
7670	Output Feed Control FF's		-	-	A	A	A	B	C	C	C	
7671	Punch Start Control (Sh. 1&2)		-	-	B	C	C	D	E	E	E	
7672	Punch Stepping Control		-	-	A	A	A	B	C	C	C	
7673	Servo Move Control		-	-	A	A	A	B	C	C	C	
7674	Servo Hi Speed Control		-	-	A	A	A	B	C	C	C	
7675	Blank Data Detection		-	-	A	A	A	B	C	C	C	
7676	Punch Column Counter		-	-	A	A	A	B	C	C	C	
7677	Punch Col. Ctr. Partial Decodes		-	A	B	B	B	B	B	B	B	
7678	Punch Column Counter Decodes		-	A	B	B	B	B	B	B	B	
7679	Punch Check & Print Control		-	A	B	B	B	B	B	B	B	

DR80-B-2

DWG. NO. A 7549 REV D SH 2 OF 8

BASIC LOGIC CONTENTS

TYPE NUMBER/NAME: 8001, 8010 DATA RECORDER

P.C.B. LOC. / LOGIC NO.	P.C. BOARD TITLE / LOGIC DRAWING TITLE	ART. NO. / P.C.B. NO.	ARTWORK DASH NUMBER / P.C. BOARD ASSEMBLY DASH NUMBER / LOGIC DRAWING REVISION									
6		3281-xx	09	09	09	11	11	12				
	MODE CONTROL	3282-xx	09	10	11	11	12	12				
7680	Mode Control Switch		-	-	A	A	B	C				
7681	Start Stop Switches		-	A	A	A	B	B				
7682	Run Out		-	-	-	-	-	A				
7683	Start Output Feed Gates		-	-	-	-	A	A				
7684	Start Input Feed Gates		-	-	-	-	-	B				
7685	Reproduce Sequence Control FF's		-	-	-	-	A	B				
7686	Stacker Full		-	-	A	A	A	B				
7687	Reset Control		-	-	A	A	A	B				
7688	Status Display, Part 1		-	-	-	A	A	B				
7689	Status Display, Part 2		-	-	-	A	A	B				
7690	Column Display		-	-	-	-	-	A				
7		7489-xx	00	00	02	02	02	03	03	04	04	05
	PROGRAM MEMORY (8)	7490-xx	01	02	02	03	03	03	04	04	05	05
	PROGRAM MEMORY (4)	7492-xx	01	02	02	03	04	04	05	05	06	06
7691	Program Load, Part 1		-	-	-	-	-	-	A	B	B	C
7692	Program Load, Part 2		-	-	-	-	-	-	A	B	B	C
7693	Program Load, Part 3		X	-	-	-	-	-	-	-	-	A
7694	Program Control		-	-	A	A	A	A	B	B	C	D
7695	Program Memory 1		-	-	-	-	-	-	-	-	A	B
7696	Program Memory 2		-	-	-	-	-	-	-	-	A	B
7697	Program Memory 3		-	-	-	-	-	-	-	-	A	B
7698	Program Memory 4, Part 1		-	-	-	-	-	-	-	-	-	A
7699	Program Memory 4, Part 2		-	-	-	-	-	-	-	-	-	A
7700	Program Memory 5		X	-	-	-	-	-	-	-	A	B
7701	Program Memory 6		X	-	-	-	-	-	-	-	A	B
7702	Program Memory 7		-	-	-	-	-	-	-	-	A	B
7703	Program Memory 8		-	-	-	-	-	-	-	-	A	B
7704	Program Outputs		-	-	-	-	-	-	-	-	A	B
7705	Program 4, Auxiliary Dup		-	-	-	-	-	-	-	-	-	A
7706	Program Select Holding Register		A	A	B	B	B	B	B	C	C	E
7707	Program Select Decode		-	-	A	A	A	A	B	C	C	D
7708	Program Indicator Decode		-	-	-	-	-	-	-	-	-	A
		5105-xx	For previous information refer to sheet 2.	X	X							
	MOTION CONTROL, PART 2	5106-xx		X	X							
8		8881-xx		X	X							
	MOTION CONTROL, PART 2	8882-xx		X	X							
		9593-xx		03	04							
	MOTION CONTROL, PART 2	9594-xx		03	04							
7709	Output Feed Detail FF's, Part 1			C	C							
7710	Output Feed Detail FF's, Part 2			C	C							
7711	Notch Timing Control			B	B							
7712	Stacker Control			D	E							
7713	Drive Motor Control			F	G							
7714	Actuators			F	F							
7715	Hopper/Stacker Switches			D	D							
7716	Click Coil Counter			C	C							
7717	Stacker Drive Signals			B	B							
7718	Card Registration (Sheet 1&2)			G	G							

BASIC LOGIC CONTENTS

TYPE NUMBER/NAME: 8001, 8010 DATA RECORDER

P.C.B. LOC. / LOGIC NO.	P.C. BOARD TITLE / LOGIC DRAWING TITLE	ART. NO. / P.C.B. NO.	ARTWORK DASH NUMBER / P. C. BOARD ASSEMBLY DASH NUMBER / LOGIC DRAWING REVISION								
9		5107-xx	02	02	03	03	04	04	05	05	
	INPUT MEMORY	5108-xx	02	03	03	04	04	05	05	05	
7719	Bus Register, Part 1		-	-	-	-	-	-	A	A	
7720	Bus Register, Part 2		-	-	-	-	-	-	-	-	
7721	Right Adjust Shift Reg., Part 1		-	-	-	A	A	A	A	A	
7722	Right Adjust Shift Reg., Part 2		-	-	-	-	-	-	-	-	
7723	Memory Controls		-	-	-	-	-	-	A	A	
7724	Input Mem. & Ver. Mem. Gating		-	-	A	A	B	C	D	D	
7725	Input Memory		-	-	A	B	B	B	B	B	
7726	Dup Memory		-	-	-	-	-	-	-	A	
7727	Verify Comparator		-	-	B	C	C	C	D	D	
7728	Memory To Bus, Part 1		-	-	-	A	A	A	A	A	
7729	Memory To Bus, Part 2		-	-	-	-	-	-	-	-	
7730	Punch Transfer Counter		-	-	-	-	-	-	A	A	
7731	Release, Transfer Control		-	-	B	B	C	D	D	D	
10		5109-xx	01	01	01	03	03	04	04	04	06
	READ CONTROL	5110-xx	01	02	03	03	04	04	05	06	06
7732	Input Feed Flip-Flops		-	-	A	A	A	B	B	B	D
7733	Card In Read Station Controls		-	A	B	B	C	C	C	D	E
7734	Input Error Flip-Flops		-	A	B	B	B	B	C	C	D
7735	Read Col. Ctr. & Timing Counter		-	A	A	B	C	C	E	F	G
7736	Read Col. Ctr. & Timing Counter		-	-	-	-	A	A	B	B	C
7737	Read Col. Ctr. & Timing Counter		-	-	-	-	-	A	B	B	C
7738	Read Timing Gates		-	-	-	-	A	B	B	B	C
7739	Read Station, Part 1		-	-	-	-	-	-	-	A	B
7740	Read Station, Part 2		-	-	-	-	-	-	-	A	A
7741	Read Register, Part 1		-	-	-	-	-	-	-	-	-
7742	Read Register, Part 2		-	-	-	-	-	-	-	-	-
7743	Light Dark & Strobe Check Gates		-	-	-	-	A	A	A	A	B
7744	Read Data FF's Output Gates		-	-	-	-	A	B	B	B	C
		7403-xx	00	00	03	03	03	04			
11	PROGRAM REVERT AND 51COL.	7404-xx	02	03	03	03	04	04			
	PROGRAM REVERT	7440-xx	00	00	01	01	01	02			
7745	Program Revert Rotary		-	-	A	A	A	A			
7746	Program Revert Selection		-	-	-	-	-	-			
7747	Program Revert Control		-	-	A	A	A	A			
7748	51 Column Read Control		A	B	C	C	D	E			
7749	51 Column Keying Controls		-	-	-	-	-	-			
7750	51 Col. Punch/Print Data Transfer		-	-	-	-	-	-			
7751	51 Column Check Hole Control		-	-	-	-	-	-			
7752	51 Column Pun Column Decodes		-	-	-	-	-	-			
7753	51 Column Stacker Control		A	B	C	C	C	C			

BASIC LOGIC CONTENTS

TYPE NUMBER/NAME: 8001, 8010 DATA RECORDER

P.C.B. LOC. / LOGIC NO.	P.C. BOARD TITLE / LOGIC DRAWING TITLE	ART. NO. / P.C.B. NO.	ARTWORK DASH NUMBER / P.C. BOARD ASSEMBLY DASH NUMBER / LOGIC DRAWING REVISION									
12		5113-xx	01	01	01	02	02	03	03			
	PUNCH DATA	5114-xx	01	01	02	02	03	03	03			
7754	2nd Card Alert Control		A	B	B	C	C	C	D			
7755	Punch Mem. Control & Data, Part 1		-	-	-	A	A	A	A			
7756	Punch Mem. Control & Data, Part 2		-	-	A	B	B	B	B			
7757	Punch Memory		-	-	-	A	A	A	A			
7758	Even Punch Transfer Gates		-	-	-	A	A	A	A			
7759	Punch Mem. Odd Col. Reg., Part 1		-	-	-	A	A	A	A			
7760	Punch Mem. Odd Col. Reg., Part 2		-	-	-	A	A	A	A			
7761	Punch Data Blank		-	-	-	A	A	A	A			
7762	Punch Actuators & Drivers, Part 1		-	A	A	B	B	B	B			
7763	Punch Actuators & Drivers, Part 2		-	A	A	B	B	B	B			
7764	Punch Actuators & Drivers, Part 3		-	A	A	B	B	B	B			
7765	Punch Actuators & Drivers, Part 4		-	A	A	B	B	B	B			
13		5115-xx	01	01	02	02	02	02	05	05	06	06
	PUNCH CHECK	5116-xx	01	02	02	03	04	05	05	06	06	06
7766	(Punch Chk) Post-Pun Rd. Data, Pt.1		-	-	-	-	-	-	-	-	-	-
7767	(Punch Chk) Post-Pun Rd. Data, Pt.2		-	-	-	-	-	-	-	-	-	-
7768	(Punch Chk) Punch Chk. Comparator		-	-	-	-	-	-	-	-	-	-
7769	(Punch Chk) Memory 1 Gating, Part 1		-	-	-	-	-	-	-	-	-	-
7770	(Punch Chk) Memory 1 Gating, Part 2		-	-	-	-	-	-	-	-	-	-
7771	(Punch Chk) Memory 1		-	-	-	-	-	-	-	-	-	-
7772	(Punch Chk) Memory 2		-	-	-	-	-	-	-	-	-	-
7773	(Punch Chk) Chk. Transfer Counter		-	-	-	-	-	-	-	-	-	-
7774	(Punch Chk) Trans. & Compare Ctrl.		-	-	A	B	C	C	D	E	F	F
7775	(Punch Check) Detection		-	-	A	A	A	A	B	B	B	C
16		5121-xx	01	01	02	02	02	02	05	05	06	
	PRINTER MEMORY	5122-xx	01	02	02	03	04	05	05	06	06	
7776	Printer Bus Register Data		-	-	A	A	B	B	C	C	C	
7777	Print 12 to 6 Bit Translate		-	-	A	A	B	B	C	C	C	
7778	Non-Print Detection, Part 1		-	-	-	-	A	A	B	B	B	
7779	Non-Print Detection, Part 2		-	-	-	-	-	-	-	A	B	
7780	Print Edit		-	A	B	B	B	B	B	B	B	
7781	Print Leading Zero Suppress		-	-	A	B	C	C	D	D	D	
7782	Print Memory Clocking		-	-	A	B	B	C	D	D	D	
7783	Print Memories		-	-	A	A	A	A	A	A	A	
7784	Print Data Outputs		-	-	-	-	-	A	B	B	B	
7785	Odd Column Registers		-	-	-	-	-	-	-	-	-	
7786	Print Even Col. Registers, Part 1		-	-	-	-	-	-	-	-	-	
7787	Print Even Col. Registers, Part 2		-	-	-	-	-	-	-	-	-	
7788	Hammer Alert Control		-	-	-	A	A	A	A	A	A	
7789	Print Blank Control		-	-	-	-	-	-	-	-	-	
7790	Print Hammers 1 & 2 Comparison		-	-	-	-	-	-	-	-	-	
7791	Print Hammers 3 & 4 Comparison		-	-	-	-	-	-	-	-	-	

DWG. NO. A 7549 REV D SH 5 OF 8

E88-112

BASIC LOGIC CONTENTS

TYPE NUMBER/NAME: 8001, 8010 DATA RECORDER

P.C.B. LOC. / LOGIC NO.	P.C. BOARD TITLE / LOGIC DRAWING TITLE	ART. NO. / P.C.B. NO.	ARTWORK DASH NUMBER / P.C. BOARD ASSEMBLY DASH NUMBER / LOGIC DRAWING REVISION			
17		5123-xx	01	01		
	PRINTER DRIVERS	5124-xx	01	01		
7792	Print Rotary Switch		-	-		
7793	Print Mode Control		-	-		
7794	Print Code Register		-	-		
7795	Print Memory Load		-	-		
7796	Print Sprocket		-	B		
7797	Print Readout Control		-	-		
7798	Hammer Found Registers		-	-		
7799	Print Firing Register		-	-		
7801	Print Drivers		-	-		
18		5125-xx	X	09	10	
	SELF CHK MOD 10 OR 11	5126-xx	X	10	10	
7802	(Self Check) Control, Part 1		For previous information refer to sheet 7.	J	K	
7803	(Self Check) Control, Part 2			F	F	
7804	(Self Check) Control, Part 3			E	E	
7805	(Self Check) Control, Part 4			B	B	
7806	(Self Check) Control, Part 5			C	D	
7807	(Self Check) Control, Part 6			E	E	
7808	(Self Check) Adder, Part 1			C	D	
7809	(Self Check) Adder, Part 2			B	C	
7810	(Self Check) Adder, Part 3			-	-	
7811	(Self Check) Adder, Part 4			B	B	
7812	(Self Check) Adder, Part 5			A	A	
7813	(Self Check) Check Hole Generator			F	F	
7814	(Self Check) Mis. Control			F	G	
19		4109-xx	04	04	05	05
	INTERSPERSED	4110-xx	04	05	05	05
7815	Production Statistics, Part 1		-	-	-	-
7816	Production Statistics, Part 2		-	-	-	-
7817	Production Statistics, Part 3		-	-	-	-
7818	Interspersed Master Card, Part 1		-	B	B	B
7819	Interspersed Master Card, Part 2		A	B	B	C
7820	Interspersed Master Card, Part 3		-	-	-	-
20		2011-xx	05			
	MAINTENANCE BOARD	2012-xx	05			
7821	8001, 8010 Maint. Card, Part 1		-			
7822	8001, 8010 Maint. Card, Part 2		-			
7823	8001, 8010 Maint. Card, Part 3		-			
7824	8001, 8010 Maint. Card, Part 4		A			
7825	8001, 8010 Maint. Card, Part 5		-			
7826	8001, 8010 Maint. Card, Part 6		A			
9619	Logic Function Locator, DR		-			
10248	Signal Title Cross Ref - 80 Col.		-			

DR80-B-6 | DWG. NO. A 7549 | REV D | SH 6 OF 8

BASIC LOGIC CONTENTS

TYPE NUMBER/NAME: 8001, 8010 DATA RECORDER

P.C.B. LOC. / LOGIC NO.	P.C. BOARD TITLE / LOGIC DRAWING TITLE	ART. NO. / P.C.B. NO.	ARTWORK DASH NUMBER / P.C. BOARD ASSEMBLY DASH NUMBER / LOGIC DRAWING REVISION													
		5105-xx	01	01	01	01										
	MOTION CONTROL, PART 2	5106-xx	01	02	03	04										
8		8881-xx					00	00	00	00	00	04				
	MOTION CONTROL, PART 2	8882-xx					00	01	02	03	04	04				
		9593-xx											00	00	01	02
	MOTION CONTROL, PART 2	9594-xx											00	01	01	02
7709	Output Feed Detail FF's, Part 1		-	-	-	-	A	A	A	A	A	B	C	C	C	C
7710	Output Feed Detail FF's, Part 2		-	-	-	-	A	A	A	A	A	B	C	C	C	C
7711	Notch Timing Control		-	-	-	-	A	A	A	A	A	A	B	B	B	B
7712	Stacker Control		-	-	-	-	A	A	B	B	B	B	C	C	D	D
7713	Drive Motor Control		-	-	-	A	B	B	B	B	B	C	D	E	F	F
7714	Actuators		-	-	-	-	A	B	C	C	C	D	E	E	F	F
7715	Hopper/Stacker Switches		-	-	A	A	B	B	B	B	B	B	C	C	C	C
7716	Click Coil Counter		-	-	-	-	A	A	B	B	B	B	C	C	C	C
7717	Stacker Drive Signals		-	-	-	-	A	A	A	A	A	A	B	B	B	B
7718	Card Registration (Sheet 1&2)		-	-	-	A	B	D	D	E	E	E	F	F	G	G
		5099-xx	01	01												
	MOTION CONTROL, PART 1	5100-xx	01	02												
4		8877-xx			00	00	00	00	00	00	06	06	06	06	09	09
	MOTION CONTROL, PART 1	8878-xx			00	01	02	03	04	05	06	07	08	09	09	10
7658	Clock Oscillator & Counter		-	-	A	A	A	A	A	A	B	B	C	D	E	E
7659	Clock Output Gating		-	-	A	A	A	B	B	B	C	C	C	C	D	D
7660	Punch Sprocket		-	-	A	A	A	B	B	B	C	C	D	D	D	D
7661	Card Tracking Flip-Flops		-	-	A	A	A	A	A	A	A	A	B	B	B	B
7662	Stacker Jam Detection		-	-	A	A	A	B	B	B	C	C	C	C	C	C
7663	Motor Jam Check		-	-	A	A	A	B	C	C	D	D	D	D	D	D
7664	General Clear		-	-	A	A	A	B	B	B	C	C	C	D	D	D
7665	Position Control Flip-Flops		-	-	A	A	B	C	C	D	E	E	E	E	E	E
7666	Position Pulse Generation		-	-	A	A	B	B	B	C	D	D	D	D	E	E
7667	Position Difference		-	-	A	B	C	D	D	E	F	F	F	F	G	G
7668	Position Sensor		-	-	A	A	A	B	B	C	D	D	D	D	D	D
7669	Position Motor Control		-	-	A	A	A	A	B	B	C	D	D	E	F	F
18		5125-xx	01	01	01	03	03	03	05	05	06	06	06	08	08	09
	SELF CHK MOD 10 OR 11	5126-xx	01	02	03	03	04	05	05	06	06	07	08	08	09	09
7802	(Self Check) Control, Part 1		-	-	B	C	D	D	E	F	F	G	H	J	J	J
7803	(Self Check) Control, Part 2		-	A	A	B	C	C	D	D	D	D	E	F	F	F
7804	(Self Check) Control, Part 3		-	A	A	B	B	B	B	B	B	B	C	D	D	D
7805	(Self Check) Control, Part 4		-	-	-	A	A	A	A	A	A	A	A	B	B	B
7806	(Self Check) Control, Part 5		-	-	-	-	A	A	A	A	A	B	B	C	C	C
7807	(Self Check) Control, Part 6		-	A	A	A	B	B	C	C	C	C	C	D	D	D
7808	(Self Check) Adder, Part 1		-	-	-	A	A	A	A	A	A	A	B	C	C	C
7809	(Self Check) Adder, Part 2		-	A	A	A	A	A	A	A	A	A	A	B	B	B
7810	(Self Check) Adder, Part 3		-	-	-	-	-	-	-	-	-	-	-	-	-	-
7811	(Self Check) Adder, Part 4		-	A	A	A	A	A	A	A	A	A	A	B	B	B
7812	(Self Check) Adder, Part 5		-	-	-	-	-	-	-	-	-	-	-	A	A	A
7813	(Self Check) Check Hole Generator		-	A	B	B	C	C	D	D	E	E	E	F	F	F
7814	(Self Check) Misc. Control		A	A	B	B	B	C	D	E	E	E	E	F	F	F

A 9619

SWITCHES

Zero/Blank Fill (724)
Outstack (856)
Option (853)
Program Revert (745)
51 Column (715)
Mode (680)
Start (681)
Stop/Reset (681)
Interspersed Gangpunch (687)
Keyboard Data (633-635)
Keyboard Functions (638,639)
Auto Skip/Dup (645)
Program (645)
Auto Release (645)
Print Mode (792)
Stacker Full (715)
Hopper Empty (715)
Power On/Off (664)
Punch Suppress (792)
Punch Step Length (667)
Off Line (680)
Door Interlock (715)

MEMORIES

Program (695-703)
Input (725)
Dup (726)
Punch (757)
Print (783)
Print Edit (780)
ROM (623)
Verify (627)
Punch Check (771,772)

REGISTERS

Keyboard Data (624,625)
Bus (719,720)
Right Adjust Shift (721,722)
Read (741,742)
Print Code (794)
Program Select Holding (706)
Punch Memory Odd Column (759,760)
Print Odd Column (785)
Print Even Column (786,787)
Hammer Found (798)
Print Firing (799)
Input Data (832,833&859,860)

COILS & MAGNETS

Verify Correction Keystroke Counter (817)
KEYSTROKE Counter (816)
Card Counter (815)
Clicker (646)
Punch Magnets (762-765)
Pressure Roll (714)
Register Roll (717)
Stacker Deflector (717)
Primary Hopper (714)
Secondary Hopper (714)
Motor Start Relay (713)
Motor Control Relay (713)
Print Hammers (801)
Stacker Solenoids (717)
Stacker Pinch (712)
Vertical Feed (718)
Power Card Aligner (718)

COUNTERS

Char Meter (875)
Check Transfer (773)
Punch Transfer (730)
Click Coil (716)
Punch Step Length (671)
Backspace (648)
Field Erase (631)
51 Col. Data Transfer (750)
Read Column (735-737)
Read Timing (735-737)
Display Column (630)
Punch Column (676)
Unit Check (658)
Control (838&865)

LAMPS

Status Display (688,689)
Card Column (690)
C. E. Trap (822)

SENSORS

Position (668)
Hopper Card (718)
Prestacker Card (718)

OSCILLATORS

Skip/Dup (644)
Master (658)
Ready Control (827,853)

RELUCTANCE PICKUPS

Punch Sprocket (660)
Read Sprocket (660)
Home Sprocket (796)
Print Sprocket (796)

MOTORS

Drive Motor (713)
Servo Motor (669)

MISCELLANEOUS

Maintenance Card (821-826)
Zoning Drawing (6660)
A.C. & Power Dist. (3192)
Power Supply (2211)
Read Station (739,740)
Post Punch Read Station (766,767)

DR80-B-8

DRN ALESI 5/24/73	ENG RVK 31 JUL 73
CHK PORTER 5/22/73	MFG JWB 31 JUL 73

REV	DESCRIPTION	APPVD-DATE
-	REL/ECN 2125-3P	RVK 31 JUL 73

REVISIONS

TITLE LOGIC FUNCTION LOCATOR, DR.

DWG NO. A 9619 DASH NO. -00 SH 1 OF 1

A 10248

SIGNAL TITLE CROSS REFERENCE

Early logic drawings contain some signal titles which do not adequately describe the function. This cross reference is provided as a convenience for use with Machine Manuals where early and later logic drawings may be mixed.

OLD SIGNAL TITLE	NEW SIGNAL TITLE	OLD SIGNAL TITLE	NEW SIGNAL TITLE
ACCn	ACCUMULATOR n	EDIT CLOCK	PRINT EDIT CLOCK
ALERT HAM n	ALERT PRINT HAM n	EDIT DUP	PRINT EDIT DUP
		EDIT LOAD	PRINT EDIT LOAD
BIT n T	BIT n TRANSLATED	EDIT SKIP	PRINT EDIT SKIP
BLOCK 7	BLOCK 7 ADD	ELIMINATE MINUS	PRINT EDIT ELIMINATE MINUS
C n	CLOCK CTR n	ENABLE n	ENABLE n ADD
CAR	INITIAL CARRY	END RA	END RIGHT ADJUST
CAR n	POS n CARRY	EXOR n	EXCLUSIVE OR n
CARD	ACT HORIZ FEED DLY		
CARD IN VISIBLE	CARD IN VISIBLE STA	FE n	FIELD ERASE CTR n
CHECK COMPARE FF	PUN CK COL COMPARE FF	FEED TIME	INPUT FEED TIME
CHECK TRANSFER FF	PUN CK MEM 1 TO MEM 2 TRANSFER FF	FEC n	FIELD ERASE CTR n
		FIRE n	PRINT FIRE HAM n
CLEAR 14 X	PRE CLEAR 13		
CLEAR 7	CLEAR 7 CTR	GATE PROG DATA OUT	PROG REG TO BUS
CLEAR COL CTR	CLEAR PUN COL CTR	GATE PROG REV	GATE PROG REV TP3
CLEAR DC REG	CLEAR DELAY CHAR REG	GATE REVERT	GATE PROG REVERT
CLEAR RCC 1	CLR READ COL CTR	GEN SLEW	GENERATE SLEW
CLEAR REG A	CLEAR OUTPUT REG	GREATER THAN 4	ADDEND GREATER THAN 4
CLR ACC	CLR ACCUMULATOR		
CLR AUG	CLR AUGEND	HAM ALERT	PRINT HAM ALERT
CLR COL CTR	CLR DISP COL CTR	HAM n BLANK	PRINT HAM n BLANK
CLR DETAIL n	CLR PUN DETAIL FES	HAM n COMP	PRINT HAM n COMP
CLR RC	CLR READ TIMING CTR	HAM n FOUND	PRINT HAM n FOUND
CLOCK DC REG	CLOCK DELAY CHAR REG	HORIZ FEED	HORIZ FEED DLY
COL CTR n	DISP COL CTR n		
COL CTR FF n	DISP COL CTR FF n	I BLANK	INPUT OR VER BLANK
COUNT ALERT FF	KBD COUNT ALERT FF	INH STKR SEL	OVERFLOW MODE
COUNT 28	CARD MOVED 56 COLS	INHIBIT FIRE	INHIBIT PRINT FIRING
CY n	CYCLE CTR n	INHIBIT REVERT	INHIBIT PROG REVERT
		INPUT CLOCK n	INPUT MEM CLOCK n
DATA STROBE	KBD DATA STROBE	INPUT FEED	INPUT FEED ACTIVE
DEC ERASE CTR	DEC FIELD ERASE CTR	INPUT LOAD	INPUT MEM LOAD
DELAY CHAR n	RA DELAY CHAR n	INPUT MEM CLOCK	INPUT MEM CLOCK TIME
DROP RFC	DROP READY FOR CMD	INPUT TIME	INPUT MEM TIME
DR ROW n	READ REG ROW n	INPUT TO BUS	INPUT MEM TO BUS
DUP	KBD DUP		
DUPE	PROG DUP	JAM STKR SEL	JAM STKR 2 SEL
DUP LOAD	DUP MEM LOAD		
DUP OUT	DUP MEM OUT	KB SPT 3	KB SPT TP3
EOF BIT	PRINT EDIT END OF FIELD BIT	LE CARD	LEADING EDGE OF CARD
		LOAD ADD	LOAD ADD TIME
ED n	PRINT EDIT n BIT	LOAD AUG	LOAD AUGEND

DR80-B-9

DRN	Alesi 27 SEP 73	ENG	[illegible] 4/5/73
CHK	Porter 28 SEP 73	MFG	

REV	DESCRIPTION	APPVD-DATE
-	Rel/ECN 2125-5P	[illegible] 4 OCT 73

REVISIONS

TITLE SIGNAL TITLE CROSS REFERENCE - 80 COL.

DWG NO. A 10248 DASH NO. -00 SH 1 OF 2

SHEET	SH	1	2
INDEX	REV	-	-

A 10248

SIGNAL TITLE CROSS REFERENCE

Early logic drawings contain some signal titles which do not adequately describe the function. This cross reference is provided as a convenience for use with Machine Manuals where early and later logic drawings may be mixed.

OLD SIGNAL TITLE	NEW SIGNAL TITLE	OLD SIGNAL TITLE	NEW SIGNAL TITLE
LOAD 11 BIT	LOAD SIGN BIT 11	SEL n	PROG SEL n
LOAD DATA	LOAD PUNCH DATA	SEL STKR ALERT FF	SEL STKR 2 ALERT FF
LOAD FE CTR	LOAD FIELD ERASE CTR	SEL STKR FF	SEL STKR 2 FF
LOAD OUTPUT REG	LOAD OUTPUT REG TIME	SET FIRE n	SET PRINT FIRE n FF
LOAD REG A	LOAD OUTPUT REG TIME	SET PADDLE	ALERT STKR PADDLE
		SET RELEASE	SET OUTPUT RELEASE
		SET SEL n	SET PROG SEL n
		SET SS n	SET SEL STKR 2
N CLK	GEN PUN CLK	SHIFT n	LOAD HAM n REG
1K PUP	1K PULL UP	SHIFT n D	LOAD HAM n DLY
n OR LESS	PUN CT LESS THAN n	SKIP	KBD SKIP
n VERIFY CORRECTION	n VERIFY CORRECTION	START ON	START RELAY OFF
KEYSTROKE COUNTER	KEYSTROKE COUNTER COIL	START TIME OUT	START INPUT TIME OUT
		STEP N	PUN MEM CLOCK
ODD CYCLE	ODD MOVE INDEX	STEP COL CTR	STEP DISP COL CTR
OMIT ZERO BIT	PRINT EDIT ZERO SUP BIT	STEP 7	STEP 7 CTR
ON LINE LOAD	ON LINE PROG LOAD		
		TC n	TFR CTR n
PC n	PUNCH COL CTR n FF	TP 13	TP 1 OR 3
PCM n BIT n	PUNCH CK MEM n BIT n		
PCM n CLOCK	PUN CK MEM n CLOCK	UPPER ALL LIGHT	POST PUNCH RD UPPER ALL LIGHT
PCM n DATA n	PUN CK MEM n BIT n IN		
PCM n LOAD	LOAD PUN CK MEM n		
PPR n	POST PUNCH RD ROW n	VERT FEED ON	MAINT VERT FEED ON
PREVIOUS CARD OK	PREVIOUS CARD CK PUNCHED	VER LOAD	VER MEM LOAD
PRI HOPPER PINCH	PRI HOPPER FEED ALERT	X n	ADDEND n
PRINT HAMMER n	PRINT HAMMER n COIL		
PULL UP	PULL UP RES	Y n	AUGEND n
PUN COL CTR 22, 23	PUN COL CTR 22 OR 23		
PUNCH n ODD	PUNCH n REG ODD	51 COL READ	51ST COL READ
		80TH TRANSFER	80TH PRINT TRANSFER
PUNCH TRANSFER TIME 1	PUNCH TRANSFER TIME ALERT		
RCC n	READ COL CTR n		
RESET CK TRF	RESET PUN CK TRANS FF		
RESET SEL n	RESET PROG SEL n		
REVERT ACTIVE	PROG REVERT ACTIVE		
ROW n EVEN	ROW n EVEN COIL		
ROW n ODD	ROW n ODD COIL		
RTC n	READ TIMING CTR n		
SAMPLE PRINT	SAMPLE PRINT COMP		
SC n	PRINT SPT CODE n		
SEC HOPPER PINCH	SEC HOPPER FEED ALERT		

DR80-B-10

DRN Alesi 27 SEP 73 ENG [illegible] 73
CHK Porter 28 SEP 73 MFG [illegible]

REV	DESCRIPTION	APPVD-DATE
-	Rel/ECN 2125-5P	[illegible] 73

REVISIONS

TITLE SIGNAL TITLE CROSS REFERENCE - 80 COL.

DWG NO. A 10248 DASH NO. -00 SH 2 OF 2

DO-46-191

LOGIC / SCHEMATIC CONTENTS

PCB LOC 1

TYPE NUMBER/NAME: BASIC 8001, 8010 INTERFACE

DWG NO	TITLE	PCB/ASM DRAWING DASH NO
9927	PWB	00
9928	KEYBOARD TRANSLATE	00

LOGIC SCHEMATIC	LOGIC DRAWING TITLE SCHEMATIC SHEET NO	LOGIC/SCHEMATIC DRAWING REVISION
7620 Sh1	KEYBOARD TRANSLATE R.O.M.	–
" Sh2	" " "	–
" Sh3	" " "	–
" Sh4	" " "	–
" Sh5	" " "	–
" Sh6	" " "	–
" Sh7	" " "	–
" Sh8	" " "	–
7622	KEYBOARD TRANSLATE PT-1	A
7623 Sh1	KEYBOARD TRANSLATE PT-2	B
" Sh2	" " "	B
7624	KEYBOARD DATA REGISTER PT-1	C
7625	KEYBOARD DATA REGISTER PT-2	A
7626	VERIFY MEMORY CONTROL	C
7627	VERIFY MEMORY	B
7628	VERIFY COMPARATOR	A
7629	MEMORY TO BUS	A
7630	DISPLAY COLUMN COUNTER	B
7631	FIELD ERASE COUNTER	A
7632	INHIBIT SECONDARY FEED	C
7633	KEYBOARD DATA KEYS PT-1	B
7634	" " " PT-2	B
7635	" " " PT-3	B
9928	Sheet 1	–

REVISION OF CONTENTS	–		
DATE OF ECN	13 NOV 73	27 FEB 74	
APPROVAL			
ENGINEERING CHANGE NOTICE	2721-IC	2750-IC	

DR80-B-11

DWG NO 9928 REV – SHT OF –

4 3 2 1 C 7620

	INPUTS								OUTPUTS										GRAPHIC
	I_8	I_7	I_6	I_5	I_4	I_3	I_2	I_1	O_1	O_2	O_3	O_4	O_5	O_6	O_7	O_8	O_9	O_{10}	
	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	
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	0	0	0	0	0	0	1	1	1	1	1	1	0	0	1	0	1	1	%
	0	0	0	0	0	1	0	0	1	1	1	1	1	0	1	0	1	1	@
	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1	1	
	0	0	0	0	0	1	1	0	1	1	1	1	1	1	1	1	1	1	
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	0	0	0	1	0	0	1	0	1	1	1	1	1	1	1	1	1	1	
	0	0	0	1	0	0	1	1	1	1	0	1	1	0	1	0	1	1	<
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	0	0	0	1	0	1	0	1	1	1	1	1	1	1	1	1	1	1	
	0	0	0	1	0	1	1	0	1	1	1	1	1	1	1	1	1	1	
	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1	1	1	1	
	0	0	0	1	1	0	0	0	1	1	1	1	1	1	1	1	1	1	
	0	0	0	1	1	0	0	1	1	1	0	1	1	1	1	1	1	0	A
	0	0	0	1	1	0	1	0	1	1	1	1	1	1	1	1	1	1	
	0	0	0	1	1	0	1	1	1	1	0	1	1	0	1	1	0	0	•
	0	0	0	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	
	0	0	0	1	1	1	0	1	1	1	1	1	1	1	1	1	1	1	
	0	0	0	1	1	1	1	0	1	1	1	1	1	1	1	1	1	1	
	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
									NOT USED		12	11	Ø	8	9	BINARY 1-7			
									ROW										

REV	DESCRIPTION	ECN	APPVD-DATE
-	RELEASE	3528-IC	9 JUL 74

REVISIONS

DR80- B- 12

DRN J. ALESI 5 OCT 73 ENG N. Deecham

CHK

TITLE KEYBOARD TRANSLATE, READ ONLY MEMORY

DWG NO. C 7620 DASH NO. -00 SH 1 OF 3

	INPUTS								OUTPUTS										GRAPHIC
	I_8	I_7	I_6	I_5	I_4	I_3	I_2	I_1	O_1	O_2	O_3	O_4	O_5	O_6	O_7	O_8	O_9	O_{10}	
	0	0	1	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	
	0	0	1	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	1
	0	0	1	0	0	0	1	0	1	1	0	1	1	0	1	1	0	1	¢
	0	0	1	0	0	0	1	1	1	1	1	1	0	0	1	1	0	0	,
	0	0	1	0	0	1	0	0	1	1	1	1	1	0	1	1	0	0	#
	0	0	1	0	0	1	0	1	1	1	1	1	1	0	1	0	0	0	"
	0	0	1	0	0	1	1	0	1	1	0	1	1	0	1	0	0	0	'
	0	0	1	0	0	1	1	1	1	1	1	1	1	0	1	0	0	1	=
	0	0	1	0	1	0	0	0	1	1	1	1	0	1	1	1	1	1	Ø
	0	0	1	0	1	0	0	1	1	1	1	1	1	1	1	1	1	1	
	0	0	1	0	1	0	1	0	1	1	0	1	1	1	1	1	1	1	&
	0	0	1	0	1	0	1	1	1	1	1	0	1	0	1	1	0	0	$
	0	0	1	0	1	1	0	0	1	1	1	1	1	1	1	1	1	1	
	0	0	1	0	1	1	0	1	1	1	1	1	0	0	1	0	1	0	_
	0	0	1	0	1	1	1	0	1	1	1	1	0	0	1	0	0	1	>
	0	0	1	0	1	1	1	1	1	1	1	1	0	0	1	0	0	0	?
	0	0	1	1	0	0	0	0	1	1	1	0	1	1	1	1	1	1	-
	0	0	1	1	0	0	0	1	1	1	1	1	1	1	1	1	1	1	
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	0	0	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1	1	
	0	0	1	1	0	1	0	0	1	1	1	1	1	1	1	1	1	1	
	0	0	1	1	0	1	0	1	1	1	0	1	1	0	1	0	1	0	(
	0	0	1	1	0	1	1	0	1	1	1	1	0	0	1	1	0	1	0-8-2
	0	0	1	1	0	1	1	1	1	1	1	0	1	0	1	0	0	0	¬
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	0	0	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1	
	0	0	1	1	1	0	1	0	1	1	1	1	1	0	1	0	1	0	·
	0	0	1	1	1	0	1	1	1	1	0	1	1	0	1	1	0	0	.
	0	0	1	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	
	0	0	1	1	1	1	0	1	1	1	1	0	1	0	1	1	0	1	!
	0	0	1	1	1	1	1	0	1	1	0	1	1	0	1	0	0	1	+
	0	0	1	1	1	1	1	1	1	1	1	0	1	0	1	0	0	1	;
									NOT USED		12	11	Ø	8	9	BINARY 1-7			
									ROW										

DR80-B-13

REV	DESCRIPTION	ECN	APPVD-DATE
-	RELEASE	3528-1C	8 JUL 74

REVISIONS

DRN J. ALESI 5 OCT 73
ENG N. Deschow 19 JAN 74
CHK [illegible] 20 Jan 74
MFG

TITLE KEYBOARD TRANSLATE, READ ONLY MEMORY

DWG NO. C 7620 DASH NO. -00
SH 2 OF 5

C 7620

	INPUTS								OUTPUTS										GRAPHIC
	I_8	I_7	I_6	I_5	I_4	I_3	I_2	I_1	O_1	O_2	O_3	O_4	O_5	O_6	O_7	O_8	O_9	O_{10}	
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	0	1	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1	1	
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	0	1	0	1	0	0	0	0	1	1	1	0	1	1	1	1	1	1	–
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	0	1	0	1	0	0	1	1	1	1	1	0	1	0	1	1	0	0	Ū
	0	1	0	1	0	1	0	0	1	1	1	0	1	0	1	0	1	1	*
	0	1	0	1	0	1	0	1	1	1	1	1	1	1	1	1	1	1	
	0	1	0	1	0	1	1	0	1	1	1	1	1	1	1	1	1	1	
	0	1	0	1	0	1	1	1	1	1	1	1	1	1	1	1	1	1	
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	0	1	0	1	1	0	1	0	1	1	1	1	1	0	1	1	0	0	'A'
	0	1	0	1	1	0	1	1	1	1	0	1	1	0	1	1	0	0	•
	0	1	0	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	
	0	1	0	1	1	1	0	1	1	1	1	1	1	1	1	1	1	1	
	0	1	0	1	1	1	1	0	1	1	1	1	1	1	1	1	1	1	
	0	1	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
									NOT USED		12	11	Ø	8	9	BINARY 1-7			
									ROW										

DR80-B-14

DRN J. ALESI 6 OCT 73

TITLE KEYBOARD TRANSLATE, READ ONLY MEMORY

REV	DESCRIPTION	ECN	APPVD-DATE
–	RELEASE	3528	

REVISIONS

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DWG NO. C 7620 DASH NO. -00 SH 3 OF 3

	INPUTS								OUTPUTS										GRAPHIC
	I_8	I_7	I_6	I_5	I_4	I_3	I_2	I_1	O_1	O_2	O_3	O_4	O_5	O_6	O_7	O_8	O_9	O_{10}	
	0	1	1	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	
	0	1	1	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	1
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	0	1	1	0	1	1	1	1	1	1	1	1	0	0	1	0	0	0	?
	0	1	1	1	0	0	0	0	1	1	1	0	1	1	1	1	1	1	-
	0	1	1	1	0	0	0	1	1	1	1	1	1	1	1	1	1	1	
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	0	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1	1	
	0	1	1	1	0	1	0	0	1	1	1	1	1	1	1	1	1	1	
	0	1	1	1	0	1	0	1	1	1	0	1	1	0	1	0	1	0	(
	0	1	1	1	0	1	1	0	1	1	1	1	0	0	1	1	0	1	0-8-2
	0	1	1	1	0	1	1	1	1	1	1	0	1	0	1	0	0	0	¬
	0	1	1	1	1	0	0	0	1	1	1	1	1	0	1	1	0	1	:
	0	1	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1	
	0	1	1	1	1	0	1	0	1	1	1	1	1	0	1	0	1	0	'
	0	1	1	1	1	0	1	1	1	1	0	1	1	0	1	1	0	0	.
	0	1	1	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	
	0	1	1	1	1	1	0	1	1	1	1	0	1	0	1	1	0	1	!
	0	1	1	1	1	1	1	0	1	1	0	1	1	0	1	0	0	1	+
	0	1	1	1	1	1	1	1	1	1	1	0	1	0	1	0	0	1	;
									NOT USED		12	11	Ø	8	9	BINARY 1-7			
									ROW										

DR80-B-15

DRN J. ALESI 6 OCT 73 | ENG N. Drechak 23 Jan 74

CHK [illegible] 29 Jan 74 | MFG

TITLE: KEYBOARD TRANSLATE, READ ONLY MEMORY

REV	DESCRIPTION	ECN	APPVD-DATE
-	RELEASE	3528-1C	[illegible] 74

REVISIONS

DWG NO. C 7620 DASH NO. -00

E 79-082

	INPUTS								OUTPUTS										GRAPHIC
	I_8	I_7	I_6	I_5	I_4	I_3	I_2	I_1	O_1	O_2	O_3	O_4	O_5	O_6	O_7	O_8	O_9	O_{10}	
	1	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	
	1	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	0	0	0	1	1	1	1	1	1	0	0	1	0	1	1	%
	1	0	0	0	0	1	0	0	1	1	1	1	1	0	1	0	1	1	Ø
	1	0	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1	1	
	1	0	0	0	0	1	1	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	
	1	0	0	0	1	0	0	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	0	1	0	0	1	1	1	1	1	0	1	1	1	1	0	/
	1	0	0	0	1	0	1	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	0	1	0	1	1	1	1	1	0	1	0	1	1	0	0	Ȧ
	1	0	0	0	1	1	0	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	0	1	1	0	1	1	1	1	1	1	1	1	1	1	1	
	1	0	0	0	1	1	1	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	1	0	0	1	0	0	0	0	1	1	1	0	1	1	1	1	1	1	–
	1	0	0	1	0	0	0	1	1	1	1	0	1	1	1	1	1	0	J
	1	0	0	1	0	0	1	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	1	0	0	1	1	1	1	0	1	1	0	1	0	1	1	<
	1	0	0	1	0	1	0	0	1	1	1	0	1	0	1	0	1	1	*
	1	0	0	1	0	1	0	1	1	1	1	1	1	1	1	1	1	1	
	1	0	0	1	0	1	1	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	1	0	1	1	1	1	1	1	1	1	1	1	1	1	1	
	1	0	0	1	1	0	0	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	1	1	0	0	1	1	1	0	1	1	1	1	1	1	0	A
	1	0	0	1	1	0	1	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	1	1	0	1	1	1	1	1	1	1	0	1	1	0	0	Æ
	1	0	0	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	1	1	1	0	1	1	1	1	1	1	1	1	1	1	1	
	1	0	0	1	1	1	1	0	1	1	1	1	1	1	1	1	1	1	
	1	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
									NOT USED		12	11	Ø	8	9	BINARY 1-7			
									ROW										

REV	DESCRIPTION	ECN	APPVD-DATE
–	RELEASE	3528-1/C	8 JAN 74

REVISIONS

DR80-B-16

DRN J. ALESI 5 OCT 73 ENG N. ... 29 JAN 74

CHK ... 29 JAN 74 MFG

TITLE KEYBOARD TRANSLATE, READ ONLY MEMORY

DWG NO. C 7620 DASH NO. -00

SH 5 OF 8

	INPUTS								OUTPUTS										GRAPHIC
	I_8	I_7	I_6	I_5	I_4	I_3	I_2	I_1	O_1	O_2	O_3	O_4	O_5	O_6	O_7	O_8	O_9	O_{10}	
	1	0	1	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	
	1	0	1	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	[
	1	0	1	0	0	0	1	0	1	1	0	1	1	0	1	1	0	1	¢
	1	0	1	0	0	0	1	1	1	1	1	1	0	0	1	1	0	0	,
	1	0	1	0	0	1	0	0	1	1	1	1	1	1	1	1	1	1	
	1	0	1	0	0	1	0	1	1	1	1	1	1	0	1	0	0	0	"
	1	0	1	0	0	1	1	0	1	1	0	1	1	0	1	0	0	0	/
	1	0	1	0	0	1	1	1	1	1	1	1	1	0	1	0	0	1	=
	1	0	1	0	1	0	0	0	1	1	1	1	0	1	1	1	1	1	Ø
	1	0	1	0	1	0	0	1	1	1	1	1	1	1	1	1	1	1	
	1	0	1	0	1	0	1	0	1	1	0	1	1	1	1	1	1	1	&
	1	0	1	0	1	0	1	1	1	1	1	1	1	1	1	1	1	1	
	1	0	1	0	1	1	0	0	1	1	1	1	1	1	1	1	1	1	
	1	0	1	0	1	1	0	1	1	1	1	1	0	0	1	0	1	0	_
	1	0	1	0	1	1	1	0	1	1	1	1	0	0	1	0	0	1	>
	1	0	1	0	1	1	1	1	1	1	1	1	0	0	1	0	0	0	?
	1	0	1	1	0	0	0	0	1	1	1	0	1	1	1	1	1	1	-
	1	0	1	1	0	0	0	1	1	1	1	1	1	1	1	1	1	1	
	1	0	1	1	0	0	1	0	1	1	1	0	1	0	1	0	1	0	)
	1	0	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1	1	
	1	0	1	1	0	1	0	0	1	1	1	1	1	1	1	1	1	1	
	1	0	1	1	0	1	0	1	1	1	0	1	1	0	1	0	1	0	(
	1	0	1	1	0	1	1	0	1	1	1	1	0	0	1	1	0	1	0-8-2
	1	0	1	1	0	1	1	1	1	1	1	0	1	0	1	0	0	0	¬
	1	0	1	1	1	0	0	0	1	1	1	1	1	0	1	1	0	1	:
	1	0	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1	
	1	0	1	1	1	0	1	0	1	1	1	1	1	0	1	0	1	0	'
	1	0	1	1	1	0	1	1	1	1	0	1	1	0	1	1	0	0	•
	1	0	1	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	
	1	0	1	1	1	1	0	1	1	1	1	0	1	0	1	1	0	1	!
	1	0	1	1	1	1	1	0	1	1	0	1	1	0	1	0	0	1	+
	1	0	1	1	1	1	1	1	1	1	1	0	1	0	1	0	0	1	;
									NOT USED		12	11	Ø	8	9	BINARY 1-7			
									ROW										

REV	DESCRIPTION	ECN	APPVD-DATE
—	RELEASE	3528-1C	29 Jan 74

REVISIONS

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DR80-B-17

DRN J. ALESI 6 OCT 73 ENG V. Detchar 29 Jan 74

CHK 29 Jan 74 MFG

TITLE KEYBOARD TRANSLATE, READ ONLY MEMORY

DWG NO. C 7620 DASH NO. -00 SH 6 OF 8

4	3	2	1

C 7620

	INPUTS								OUTPUTS										GRAPHIC
	I_8	I_7	I_6	I_5	I_4	I_3	I_2	I_1	O_1	O_2	O_3	O_4	O_5	O_6	O_7	O_8	O_9	O_{10}	
	1	1	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	
	1	1	0	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	0	0	0	1	1	1	1	1	1	1	0	1	1	0	0	#
	1	1	0	0	0	1	0	0	1	1	1	1	1	0	1	0	1	1	@
	1	1	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1	1	
	1	1	0	0	0	1	1	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	
	1	1	0	0	1	0	0	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	0	1	0	0	1	1	1	1	1	0	1	1	1	1	0	/
	1	1	0	0	1	0	1	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	0	1	0	1	1	1	1	1	1	0	0	1	1	0	0	,
	1	1	0	0	1	1	0	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	0	1	1	0	1	1	1	1	1	1	1	1	1	1	1	
	1	1	0	0	1	1	1	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	1	1	0	1	0	0	0	0	1	1	1	0	1	1	1	1	1	1	—
	1	1	0	1	0	0	0	1	1	1	1	0	1	1	1	1	1	0	J
	1	1	0	1	0	0	1	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	1	0	0	1	1	1	1	1	0	1	0	1	1	0	0	$
	1	1	0	1	0	1	0	0	1	1	1	0	1	0	1	0	1	1	*
	1	1	0	1	0	1	0	1	1	1	1	1	1	1	1	1	1	1	
	1	1	0	1	0	1	1	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	1	0	1	1	1	1	1	1	1	1	1	1	1	1	1	
	1	1	0	1	1	0	0	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	1	1	0	0	1	1	1	0	1	1	1	1	1	1	0	A
	1	1	0	1	1	0	1	0	1	1	0	1	1	0	1	1	0	1	¢
	1	1	0	1	1	0	1	1	1	1	0	1	1	0	1	1	0	0	.
	1	1	0	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	1	1	1	0	1	1	1	1	1	1	1	1	1	1	1	
	1	1	0	1	1	1	1	0	1	1	1	1	1	1	1	1	1	1	
	1	1	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
									NOT USED		12	11	Ø	8	9	BINARY 1-7			
									ROW										

REV	DESCRIPTION	ECN	APPD-DATE
—	RELEASE	3528-15	9 Jan 74

REVISIONS

DR80-B-18

DRN J. ALESI 8 OCT 73 ENG J. Dearman 23 Jan 74
CHK M. [illegible] 24 Jan 74 MFG

TITLE KEYBOARD TRANSLATE, READ ONLY MEMORY

DWG NO. C 7620 DASH NO. -00 SH 7 OF 8

C 7620

	INPUTS								OUTPUTS										GRAPHIC
	I_8	I_7	I_6	I_5	I_4	I_3	I_2	I_1	O_1	O_2	O_3	O_4	O_5	O_6	O_7	O_8	O_9	O_{10}	
	1	1	1	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1
	1	1	1	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	:
	1	1	1	0	0	0	1	0	1	1	1	1	1	0	1	1	0	1	
	1	1	1	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	
	1	1	1	0	0	1	0	0	1	1	1	1	1	1	1	1	1	1	
	1	1	1	0	0	1	0	1	1	1	1	1	1	0	1	0	1	0	'
	1	1	1	0	0	1	1	0	1	1	1	1	1	0	1	0	0	1	=
	1	1	1	0	0	1	1	1	1	1	1	1	1	0	1	0	0	0	"
	1	1	1	0	1	0	0	0	1	1	1	1	0	1	1	1	1	1	Φ
	1	1	1	0	1	0	0	1	1	1	1	1	1	1	1	1	1	1	
	1	1	1	0	1	0	1	0	1	1	0	1	1	1	1	1	1	1	&
	1	1	1	0	1	0	1	1	1	1	1	1	0	0	1	1	0	0	,
	1	1	1	0	1	1	0	0	1	1	1	1	0	0	1	0	1	1	%
	1	1	1	0	1	1	0	1	1	1	1	1	0	0	1	0	1	0	_
	1	1	1	0	1	1	1	0	1	1	1	1	0	0	1	0	0	1	>
	1	1	1	0	1	1	1	1	1	1	1	1	0	0	1	0	0	0	?
	1	1	1	1	0	0	0	0	1	1	1	0	1	1	1	1	1	1	−
	1	1	1	1	0	0	0	1	1	1	1	1	1	1	1	1	1	1	
	1	1	1	1	0	0	1	0	1	1	1	0	1	0	1	1	0	1	!
	1	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1	1	
	1	1	1	1	0	1	0	0	1	1	1	1	1	1	1	1	1	1	
	1	1	1	1	0	1	0	1	1	1	1	0	1	0	1	0	1	0	)
	1	1	1	1	0	1	1	0	1	1	1	0	1	0	1	0	0	1	;
	1	1	1	1	0	1	1	1	1	1	1	0	1	0	1	0	0	0	¬
	1	1	1	1	1	0	0	0	1	1	1	0	0	1	1	1	1	1	}
	1	1	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1	
	1	1	1	1	1	0	1	0	1	1	0	1	1	0	1	1	0	1	¢
	1	1	1	1	1	0	1	1	1	1	0	1	1	0	1	1	0	0	.
	1	1	1	1	1	1	0	0	1	1	0	1	1	0	1	0	1	1	<
	1	1	1	1	1	1	0	1	1	1	0	1	1	0	1	0	1	0	(
	1	1	1	1	1	1	1	0	1	1	0	1	1	0	1	0	0	1	+
	1	1	1	1	1	1	1	1	1	1	0	1	1	0	1	0	0	0	|
									NOT USED		12	11	Ø	8	9	BINARY 1-7			
									ROW										

REV	DESCRIPTION	ECN	APPVD-DATE
–	RELEASE	3528-1C	8 JUL 74

REVISIONS

DR80-B-19

DRN J. ALESI 8 OCT 73 ENG N. Derchak 29 Jan 74

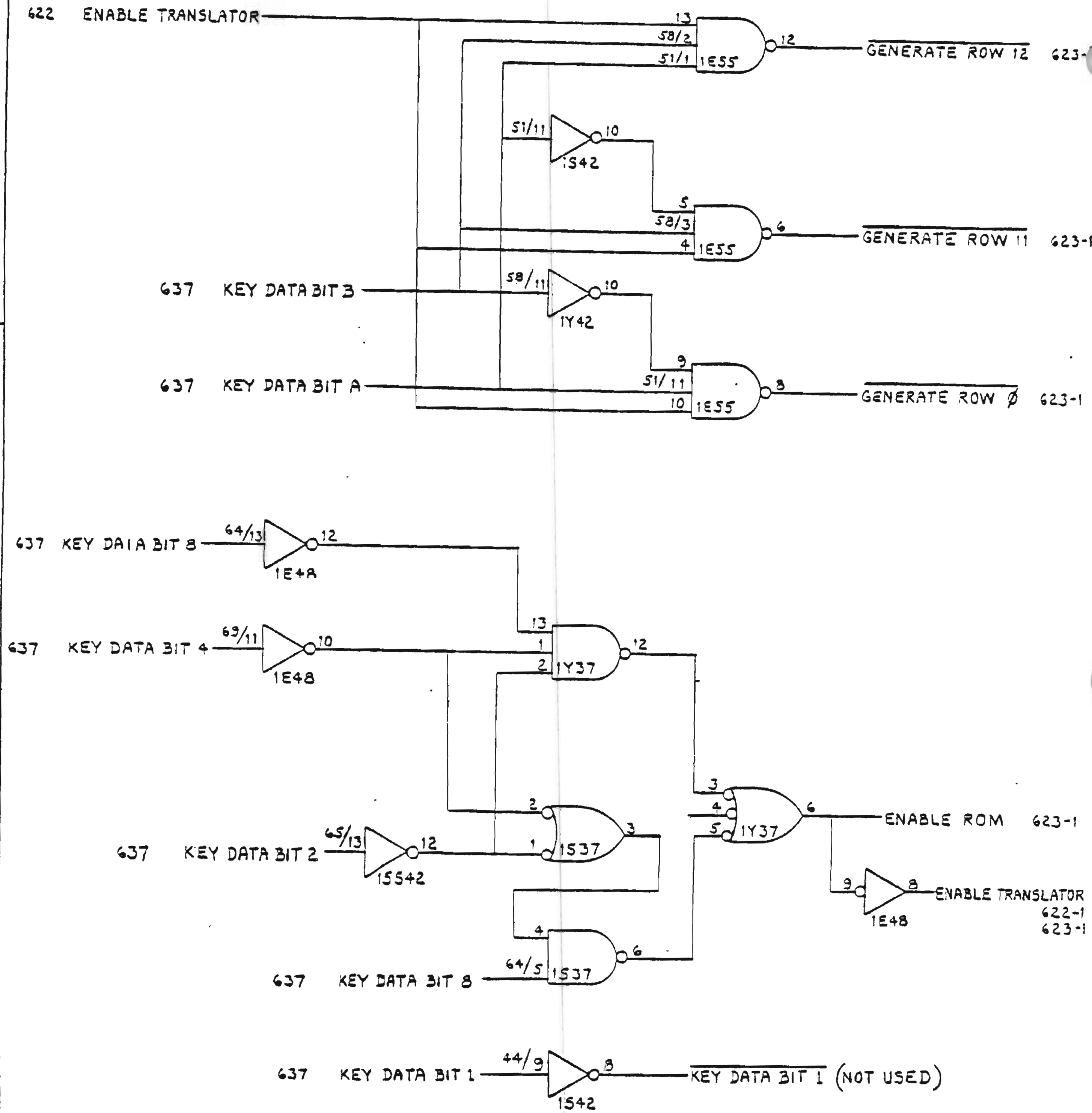
CHK N. Derchak 29 Jan 74 MFG

TITLE KEYBOARD TRANSLATE, READ ONLY MEMORY

DWG NO. C 7620 DASH NO. -00 SH 8 OF 8

E79-082

C 7622



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	9928-001	9927-001	2721-1C	

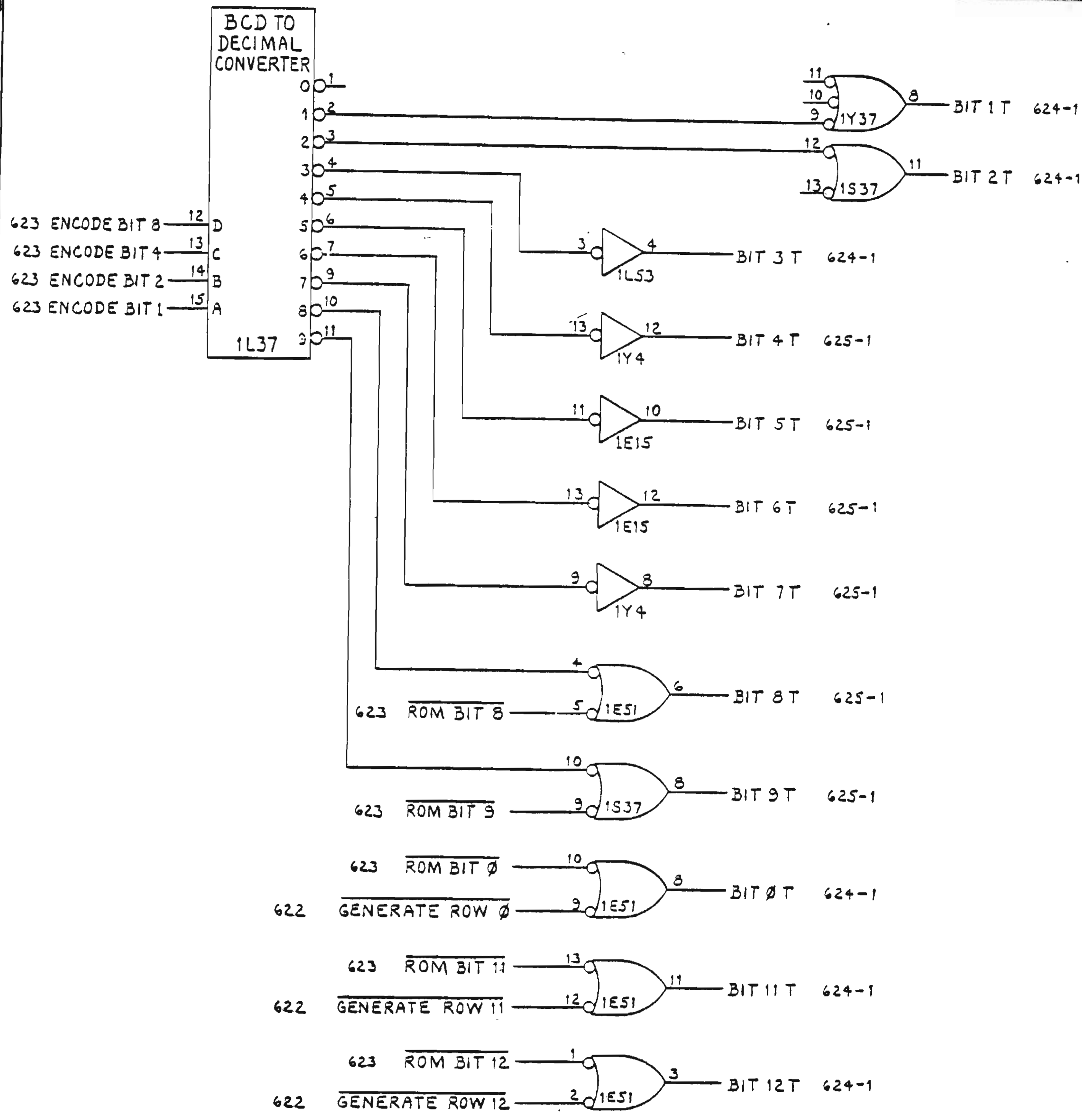
REVISIONS

DR80 - B - 20

DRN J. ALESI 2 NOV 73
CHK 7 NOV 73

TITLE KEYBOARD TRANSLATE PART 1

DWG NO. C 7622 DASH NO. -00 SH 1 OF 1



SHEET INDEX	SHT	1	2
	REV	B	B

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	9928-00	9927-00	12750-1C	
A	9928-00	9927-00	2721-1C	

REVISIONS

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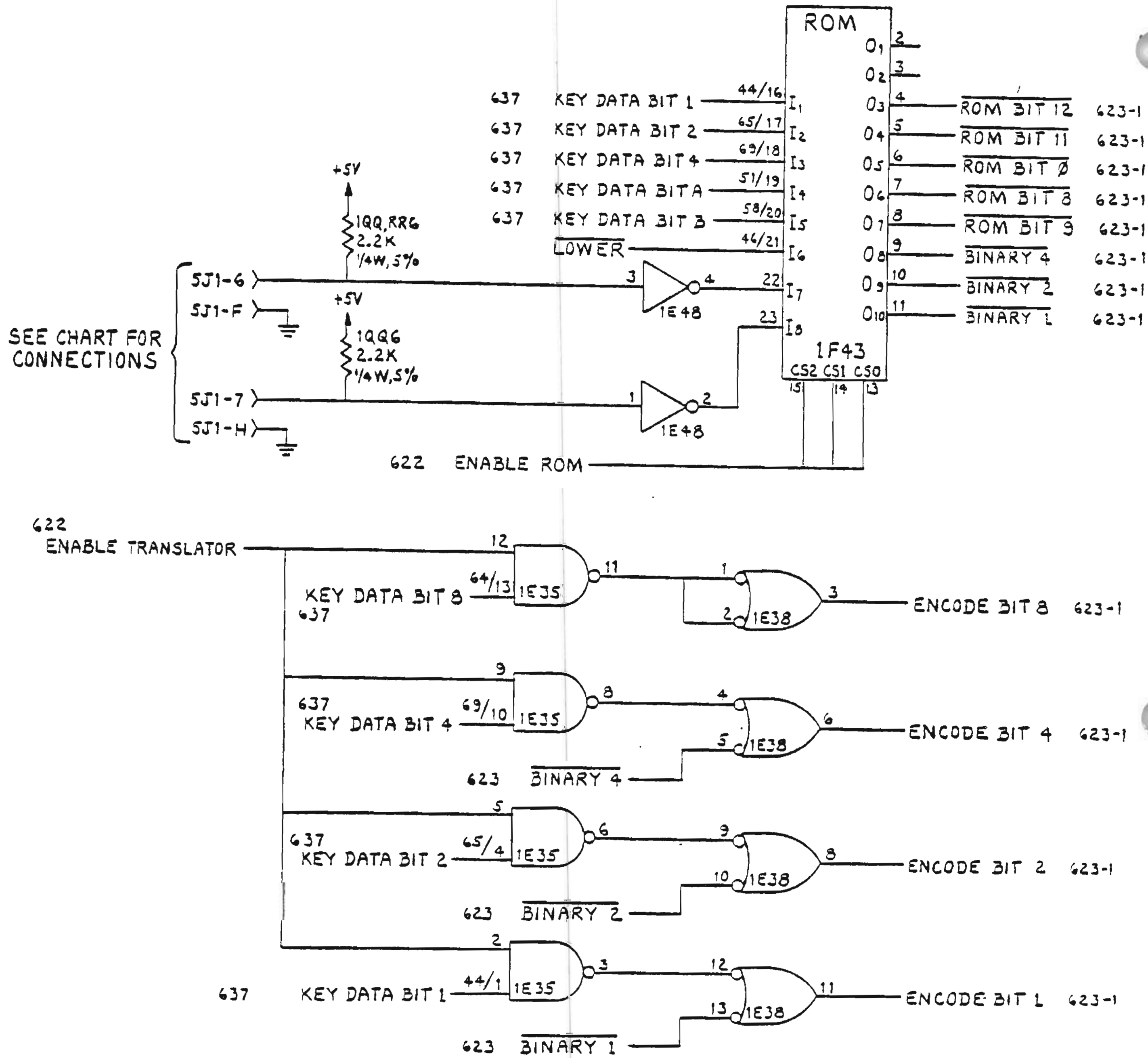
DR80-B-21

DRN J. ALESI 1 NOV 73 ENG 7 Nov 73
CHK B 7 NOV 73 MFG

TITLE KEYBOARD TRANSLATE PART 2

DWG NO. C 7623 DASH NO. -00 SH 1 OF 2

C 7623



FROM	TO	KEYBOARD ASSEMBLIES
NO CONNECTION	NO CONNECTION	2601-22,-23,-24,-25,-26,-27, -28,-30,-35,-36,-44,-45
5J1-6	5J1-F	2601-34
5J1-7	5J1-H	2601-31,-32,-33
5J1-6	5J1-F	2601-13,-14,-15,-17,-18 -19,-20,-21,-43
5J1-7	5J1-H	

(FOR ADDITIONAL INFORMATION ON ROM CONTENTS, SEE DWG. C-7620.)

DR80-B-22

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	9928-001	9927-001	2750-1C	
A	9928-001	9927-001	2721-1C	

REVISIONS

DRN J. ALESI NOV 73 ENG M. LESCHAK

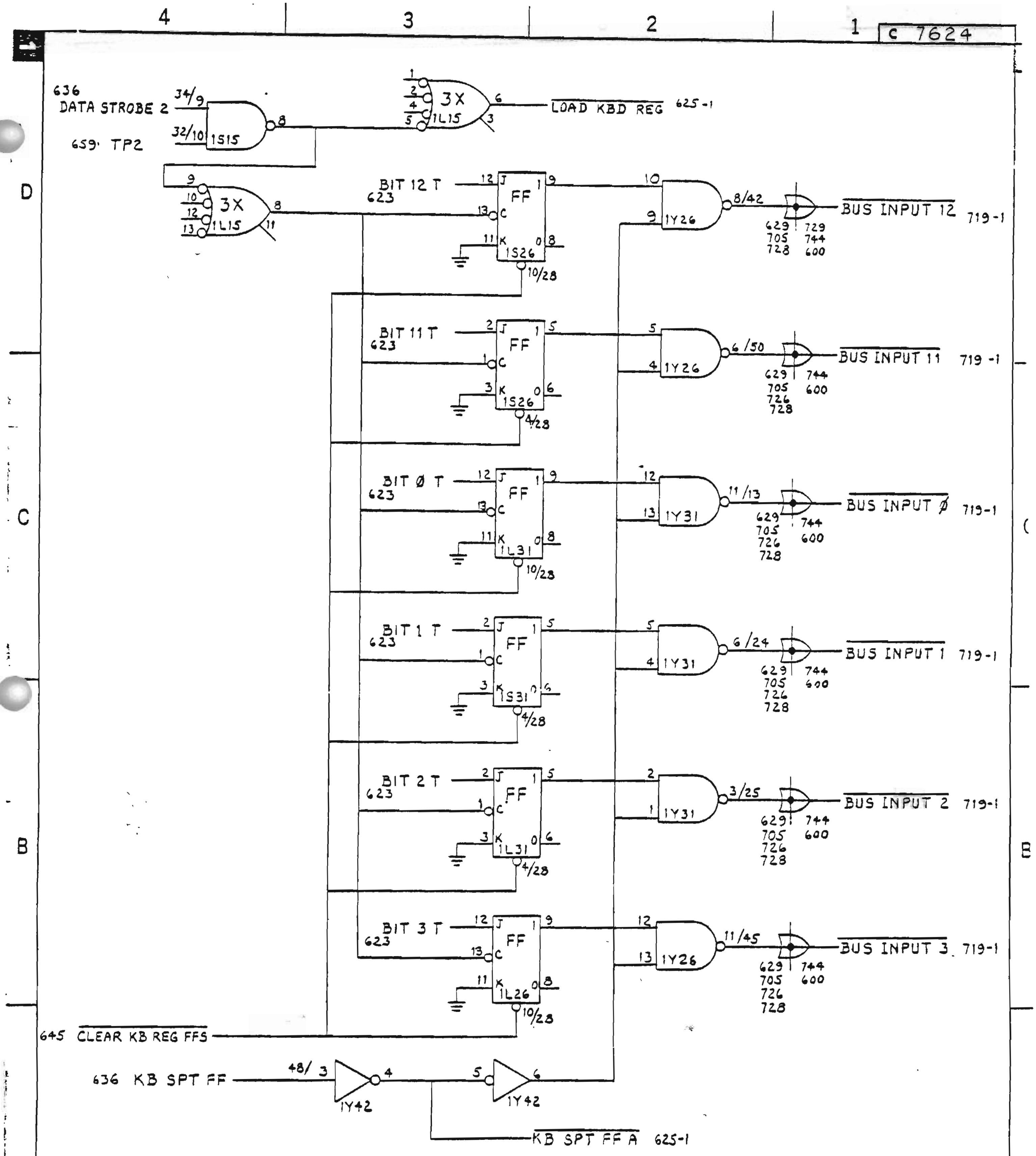
CHKD 7 NOV 73 MFG

TITLE KEYBOARD TRANSLATE PART 2

DWG NO. C 7623

DASH NO. -00

SH 2 OF 2



C	9928-00	9927-00	2721-1C	[illegible]
B	5094-04	5093-03	2265-1P	[illegible]
A	5094-03	5093-01	1681-2P	[illegible] 3-0-73
-	5094-01	5093-01	1418-2C	[illegible] 11-13-72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

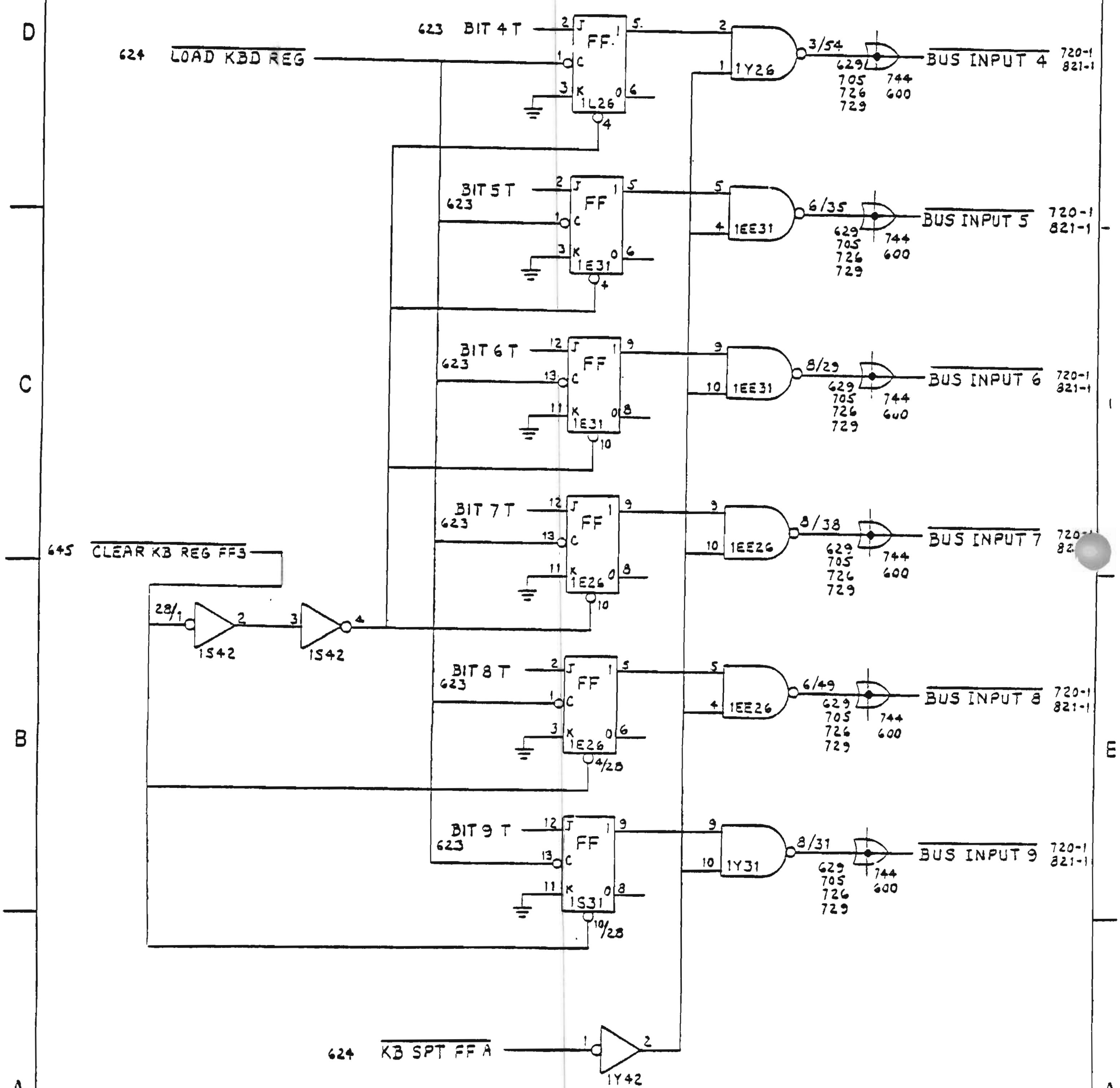
DR80-B-23

DRN J. ALESI 6/23/72 ENG L.C. Kre[illegible]

CHK [illegible] 11/2/72 MFG [illegible]

TITLE KEYBOARD DATA REGISTER, PART 1

DWG NO. C 7624 DASH NO. -00 SH 1 OF 1



C 7625

DR80-B-24

A	9928-00	9927-00	2721-1C	
-	5094-01	5093-01	1418-2C	11-8-72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

DRN J. ALESI 6/23/72

CHK 11/2/72

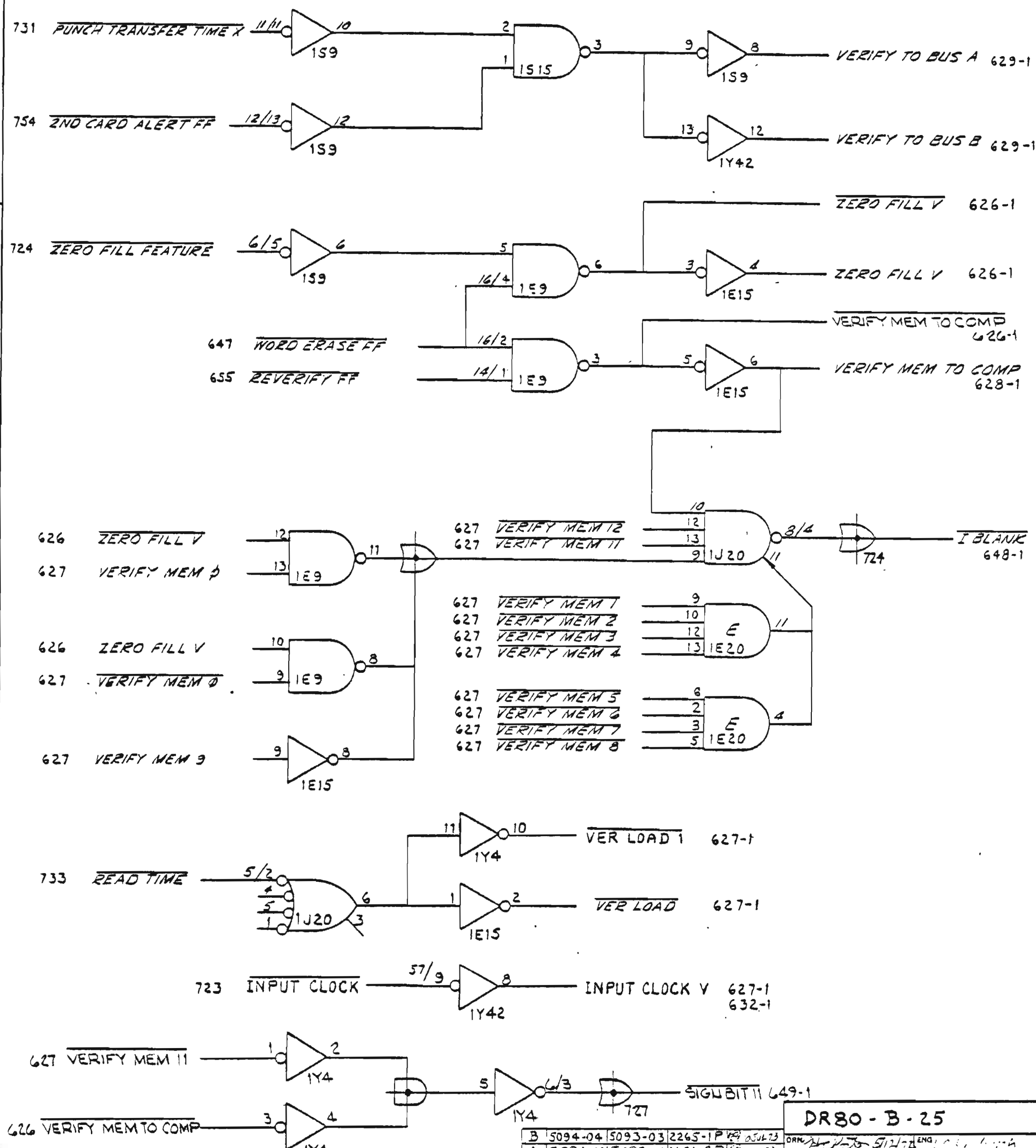
TITLE KEYBOARD DATA REGISTER, PART 2

DWG NO. C 7625

DASH NO. -00

SH 1 OF 1

C 7626



DR80 - B - 25

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	5094-04	5093-03	2265-1P	05JUL73
A	5094-03	5093-01	1631-2P	1-10-73
-	5094-01	5093-01	1418-2C	11-15-72
C	9928-00	9927-00	2721-1C8	

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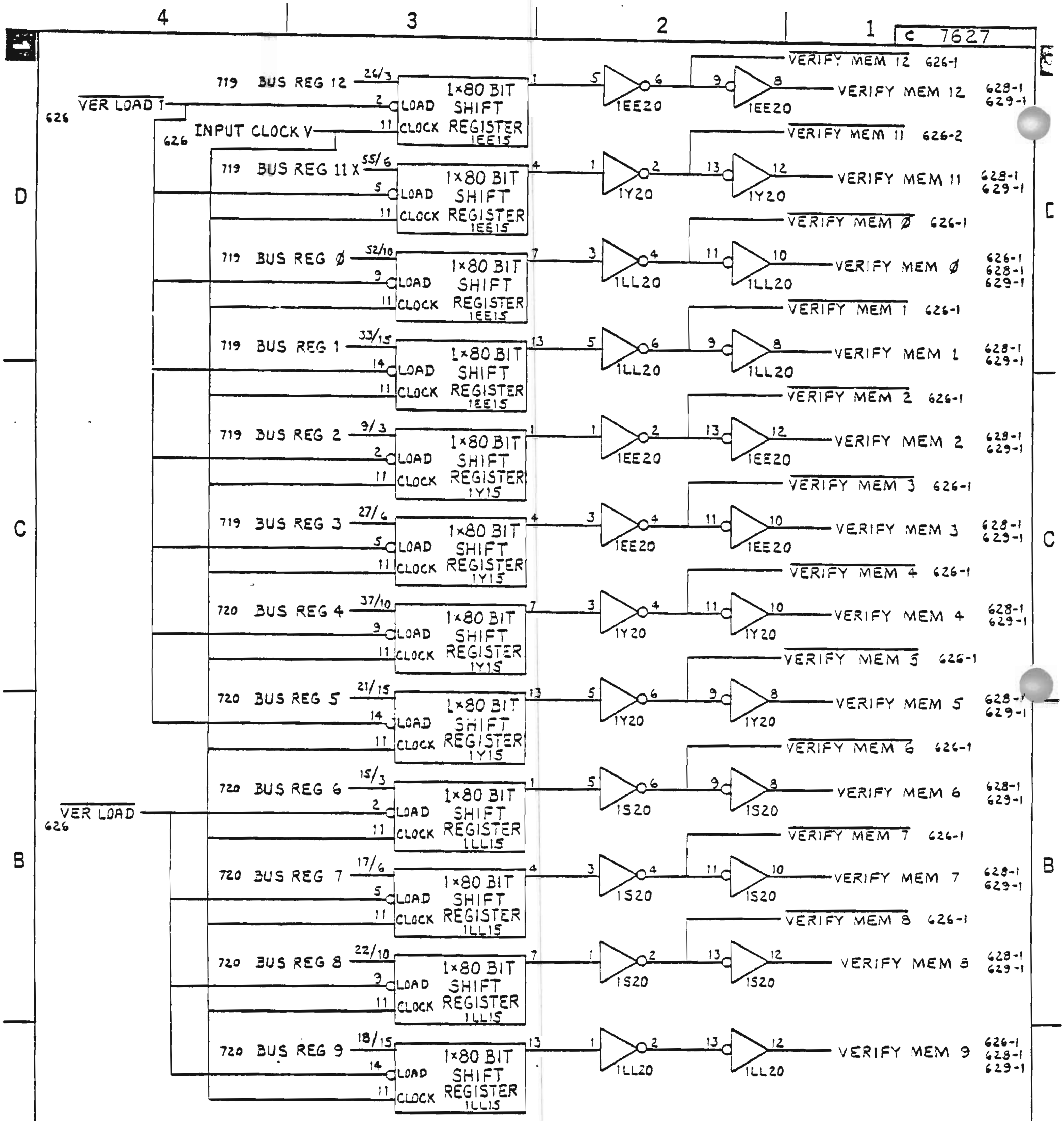
DRN / CHK / ENG / MFG

TITLE VERIFY MEMORY CONTROL

DWG NO. C 7626 DASH NO. -00 SH 1 OF 1



DR80-B-26

REV	COMP ASM	ARTWORK	ECN	APPRO-DATE
B	9928-00	19927-00	2721-1C	12 Nov 73
A	5094-03	5093-01	1681-2C	8-30-73
–	5094-01	5093-01	1418-2C	11-2-72

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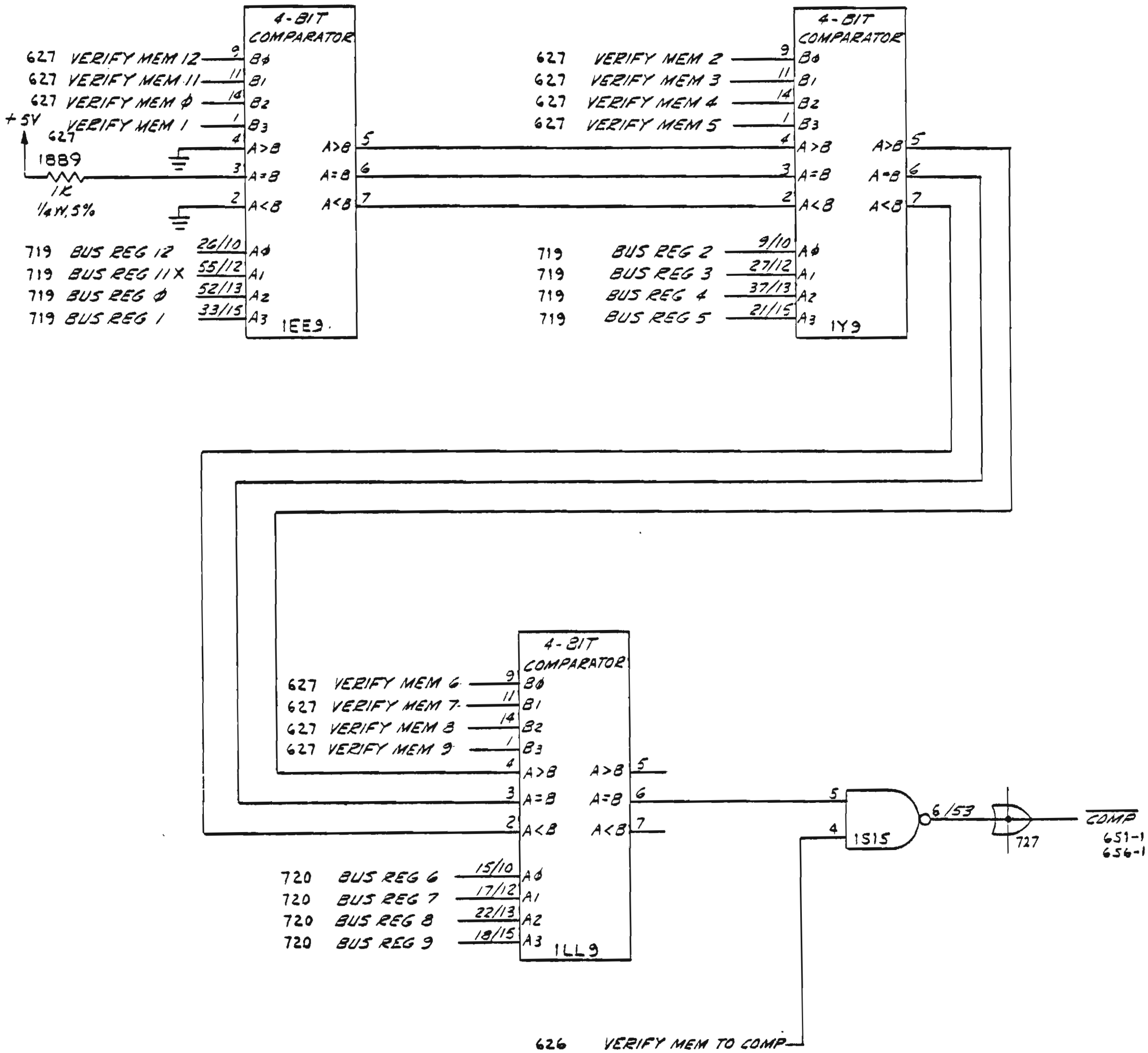
DRN J. ALESI 10/12/72 ENG L. C. Kralovich
CHK DELTUFFO 11/1/72 MFG

TITLE VERIFY MEMORY

DWG NO. C 7627 DASH NO. -00 SH 1 OF 1

C 7628



DR80-B-27

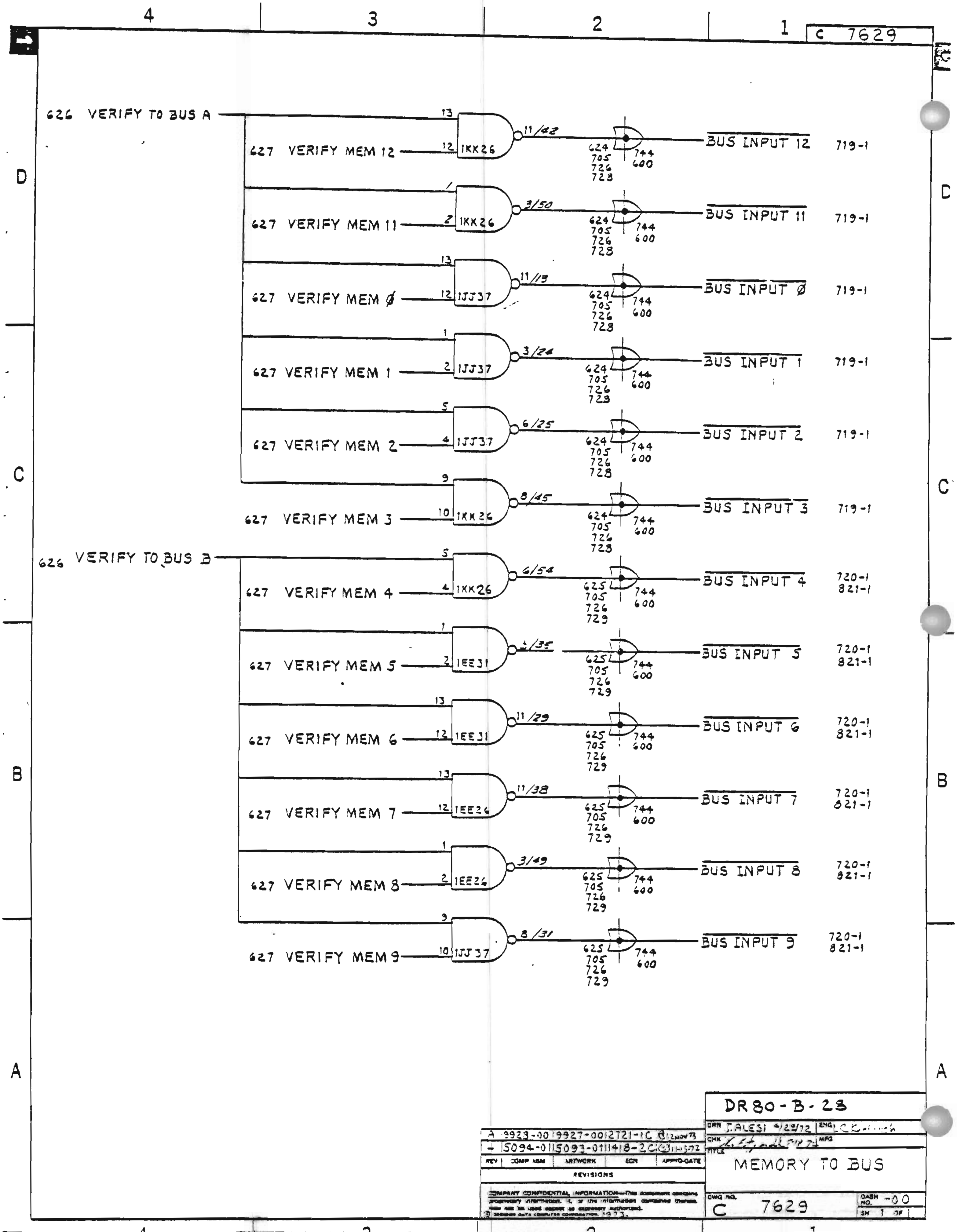
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	9929-00	9927-00	2721-1C	[illegible]
-	5094-01	5093-01	1418-2C	[illegible] 11-3-72

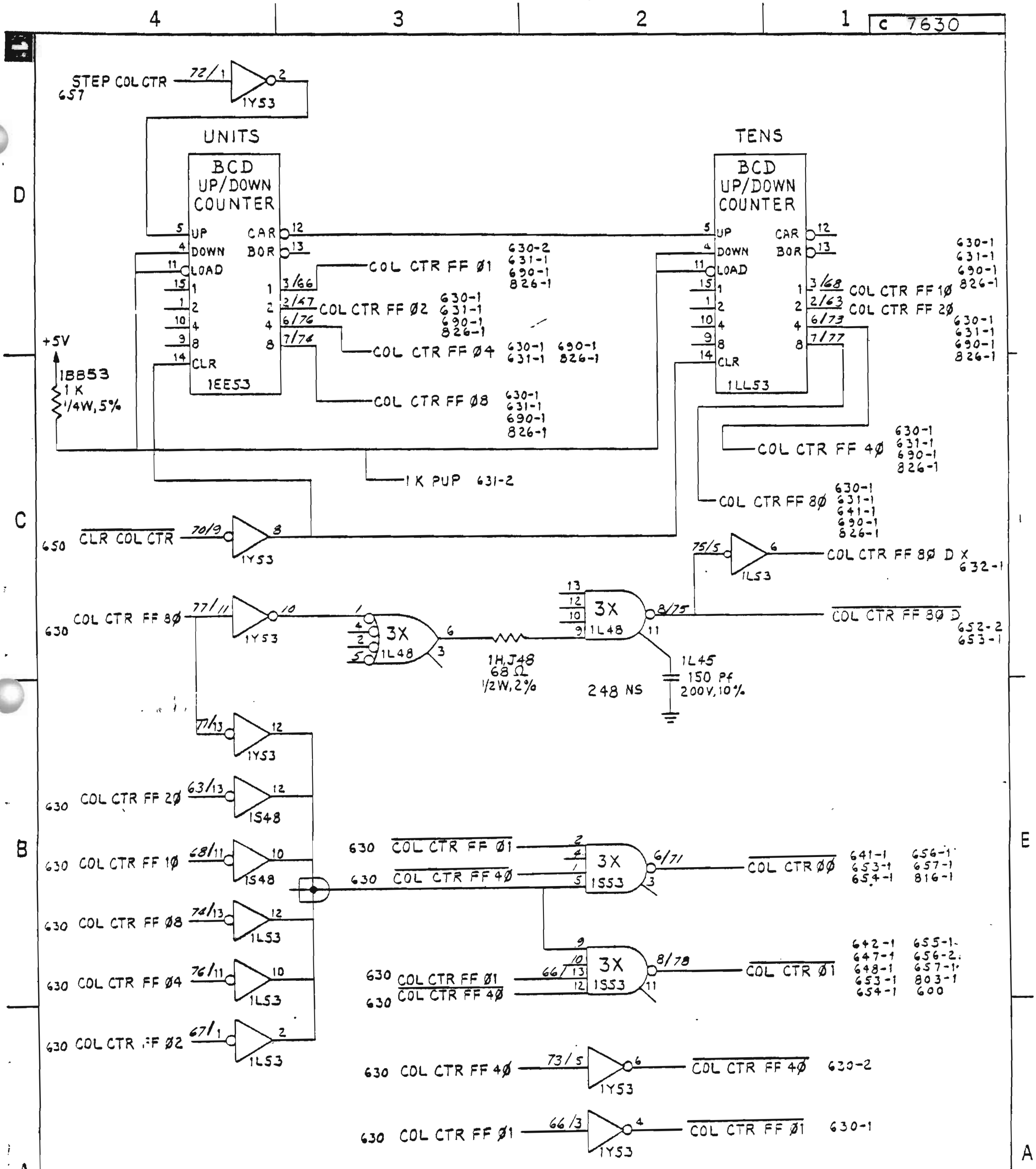
REVISIONS

DRN [illegible] 5/17/72 ENG L.C.K. [illegible]
CHK [illegible] 5-4-72 MFG [illegible]

TITLE VERIFY COMPARATOR

DWG NO. C 7628 DASH NO. -00 SH 1 OF 1


C 7629
626 VERIFY TO BUS A
627 VERIFY MEM 12
1KK26
BUS INPUT 12
719-1
627 VERIFY MEM 11
1KK26
BUS INPUT 11
719-1
627 VERIFY MEM Ø
1JJ37
BUS INPUT Ø
719-1
627 VERIFY MEM 1
1JJ37
BUS INPUT 1
719-1
627 VERIFY MEM 2
1JJ37
BUS INPUT 2
719-1
627 VERIFY MEM 3
1KK26
BUS INPUT 3
719-1
626 VERIFY TO BUS B
627 VERIFY MEM 4
1KK26
BUS INPUT 4
720-1
821-1
627 VERIFY MEM 5
1EE31
BUS INPUT 5
720-1
821-1
627 VERIFY MEM 6
1EE31
BUS INPUT 6
720-1
821-1
627 VERIFY MEM 7
1EE26
BUS INPUT 7
720-1
821-1
627 VERIFY MEM 8
1EE26
BUS INPUT 8
720-1
821-1
627 VERIFY MEM 9
1JJ37
BUS INPUT 9
720-1
821-1
744
600
DR80-B-28
DRN J. ALESI 4/22/72
MEMORY TO BUS
REVISIONS
COMPANY CONFIDENTIAL INFORMATION
DWG NO. C 7629
DASH NO. -00
SH 1 OF 1



DR80 - B - 29

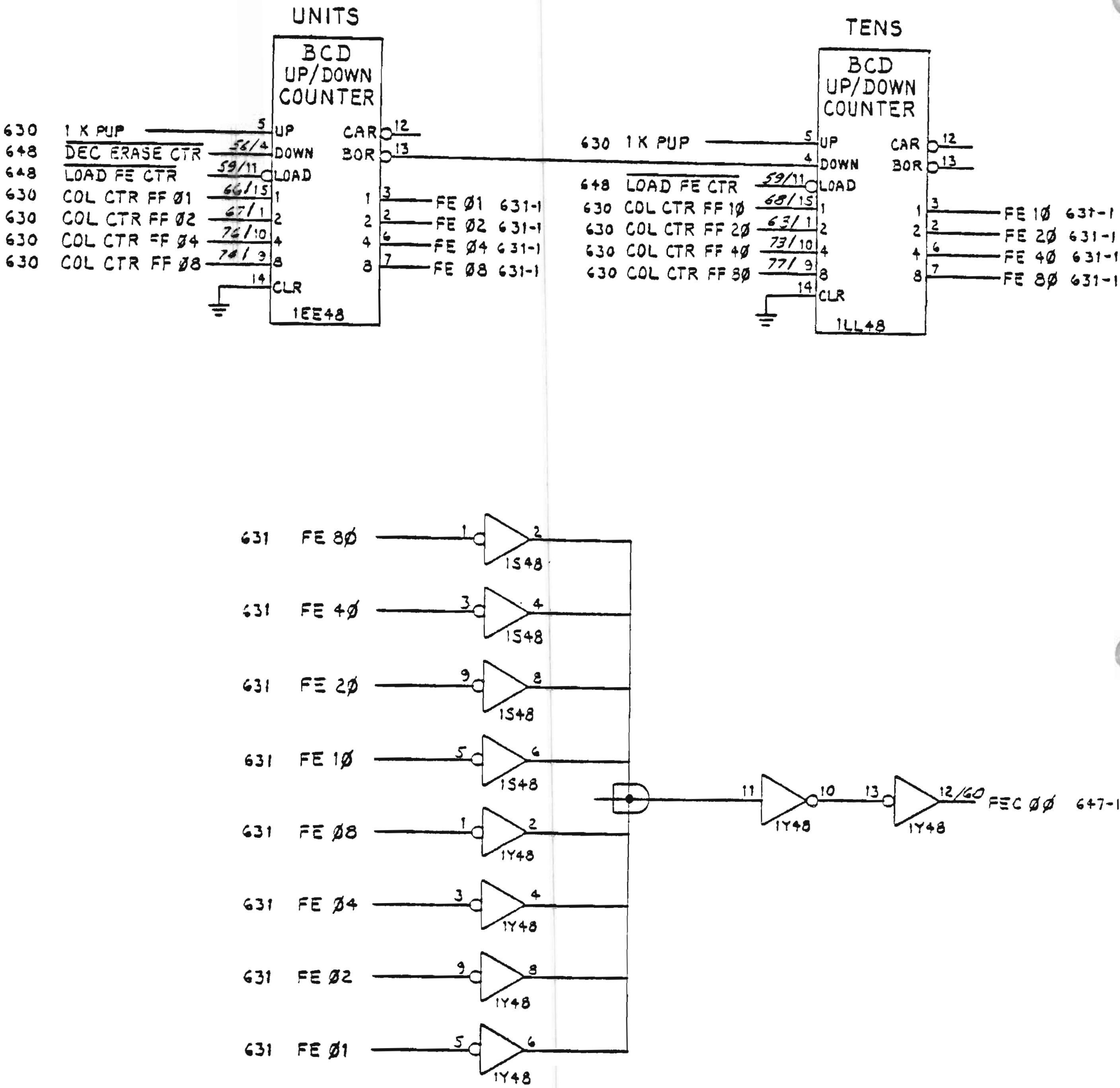
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	9928-00	9927-00	2721-1C	12-4-73
A	5094-03	5093-03	1681-3C	2-22-73
—	5094-01	5093-01	1418-20	1-3-72

REVISIONS

DRN J. ALESI 12/1/71 ENG. C. Kozlowich
CHK
MFG

TITLE DISPLAY COLUMN COUNTER

DWG NO. C 7630 DASH NO. -00 SH 1 OF 1



C 7631

A	9928-00	9927-00	2721-IC
—	5094-01	5093-01	1418-2C
REV	COMP.ASM	ARTWORK	ECN

REVISIONS

DR80-B-30

DRN J. ALESI 12/1/71

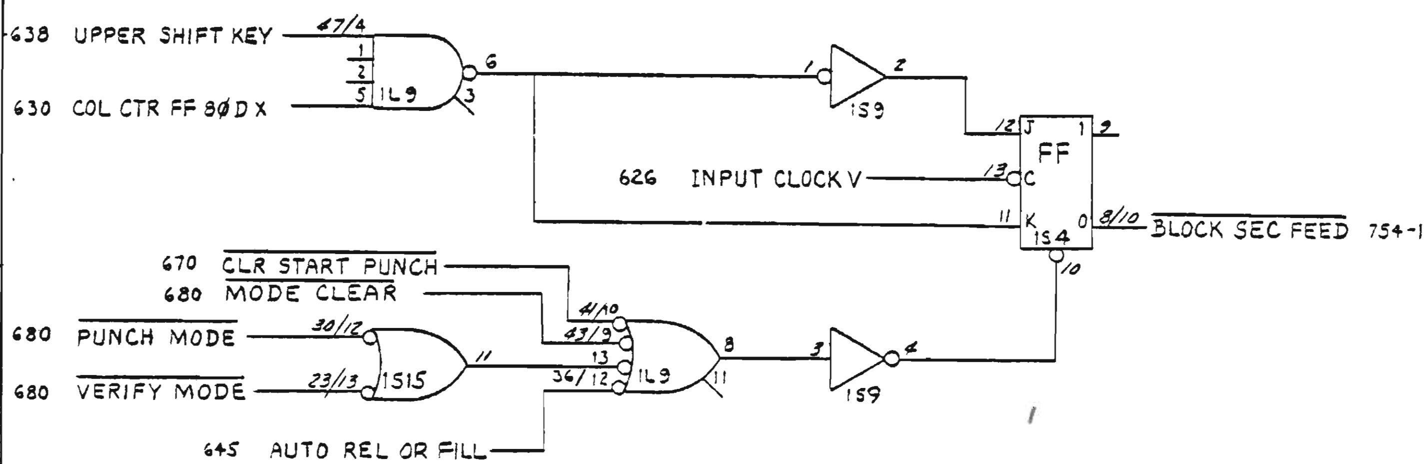
FIELD ERASE COUNTER

DWG NO. C 7631

DASH NO. -00

SH 1 OF 1

C 7632



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	9928-00	9927-00	2721-1C	12 NOV 73
B	5094-04	5093-04	2511-1P	8 Sep 73
A	5094-04	5093-03	2265-1P	5 JUN 73
-	5094-01	5093-01	1418-2C	11-13-72

REVISIONS

DR80-B-31

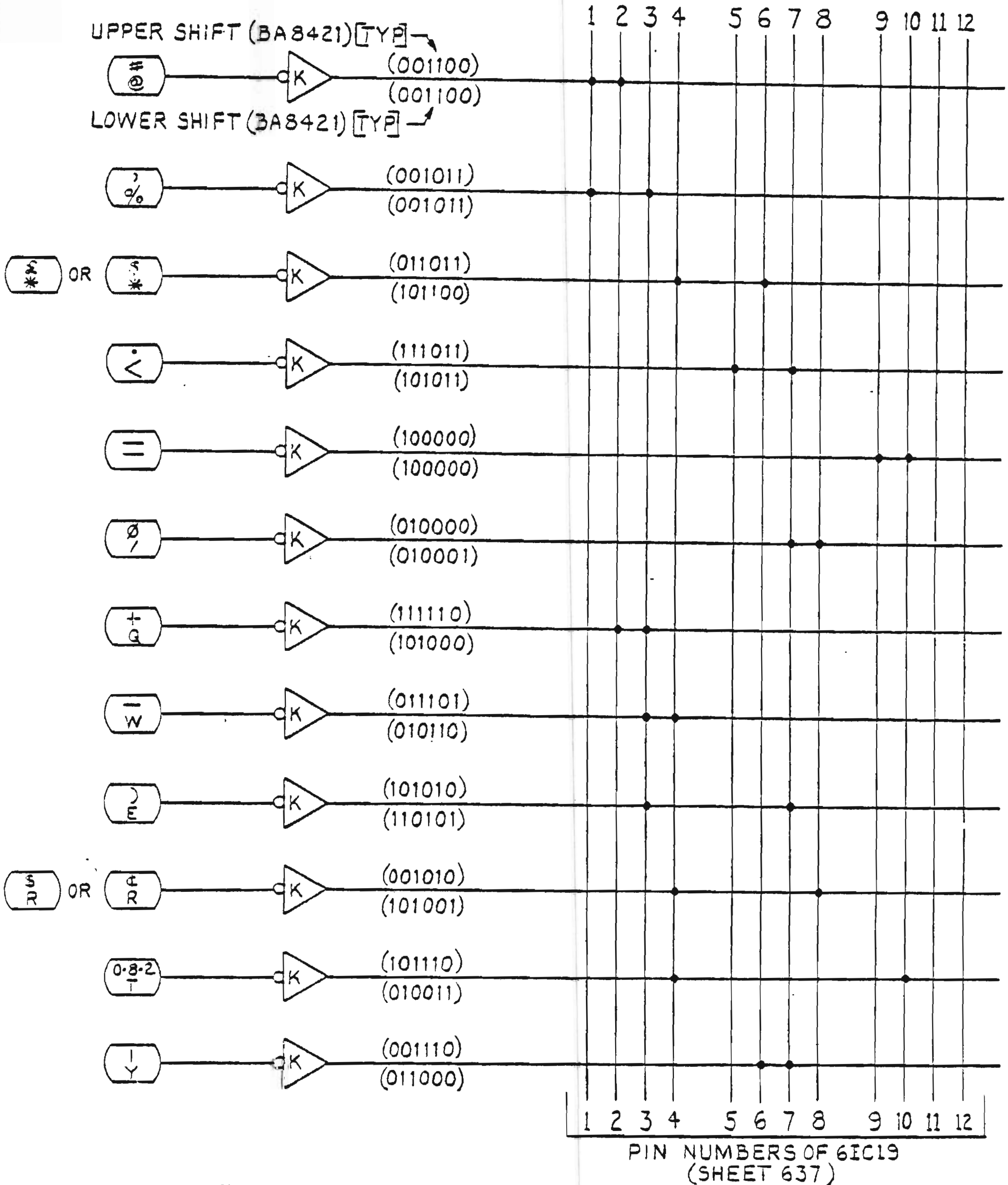
DRN J. ALESI 9/28/72 ENG

CHK MFG

TITLE INHIBIT SECONDARY FEED

DWG NO. C 7632 DASH NO. -00 SH 1 OF 1

C 7633



INFORMATION ON THIS SHEET FOR U.S., U.K., AND ITALIAN KEYBOARDS ONLY.

DR80 · 3 - 32

REV	COMP ASM	ART WORK	ECN	APPVD-DATE
3	9928-00	9927-00	2721-IC	
A	5094-01	5093-01	2407-IC	
—	5094-01	5093-01	1418-2C	11-3-72

REVISIONS

DRN J. ALESI 6/12/72

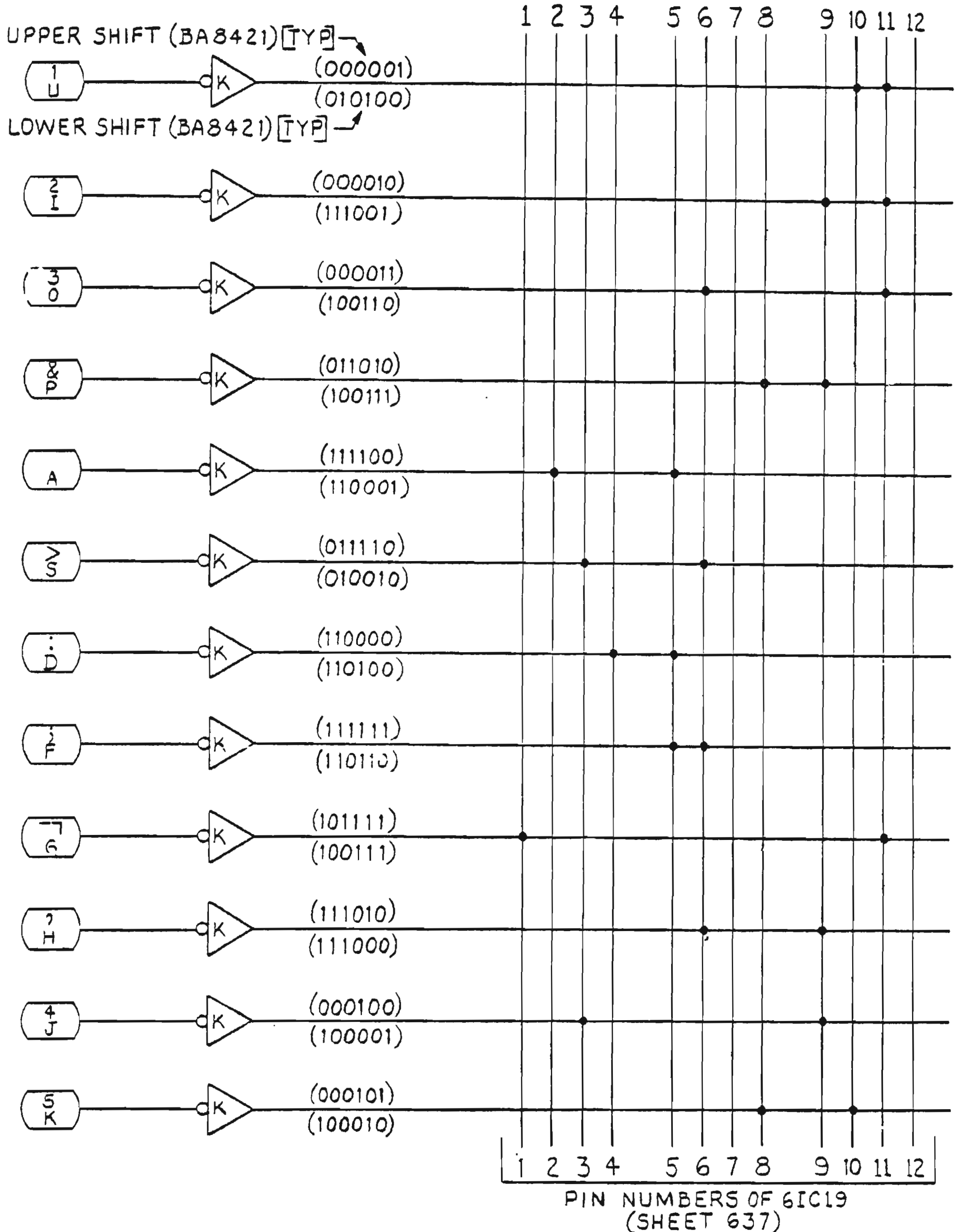
CHK 11/3/72

TITLE KEYBOARD DATA KEYS, PART 1

DWG NO. C 7633

DASH NO. -00

SH 1 OF 1



INFORMATION ON THIS SHEET FOR U.S., U.K., AND ITALIAN KEYBOARDS ONLY.

DR80-B-33

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	9928-00	9927-00	2721-1C	8-12-73
A	5094-01	5093-01	2407-1C	3-73
–	5094-01	5093-01	1418-2C	11-3-72

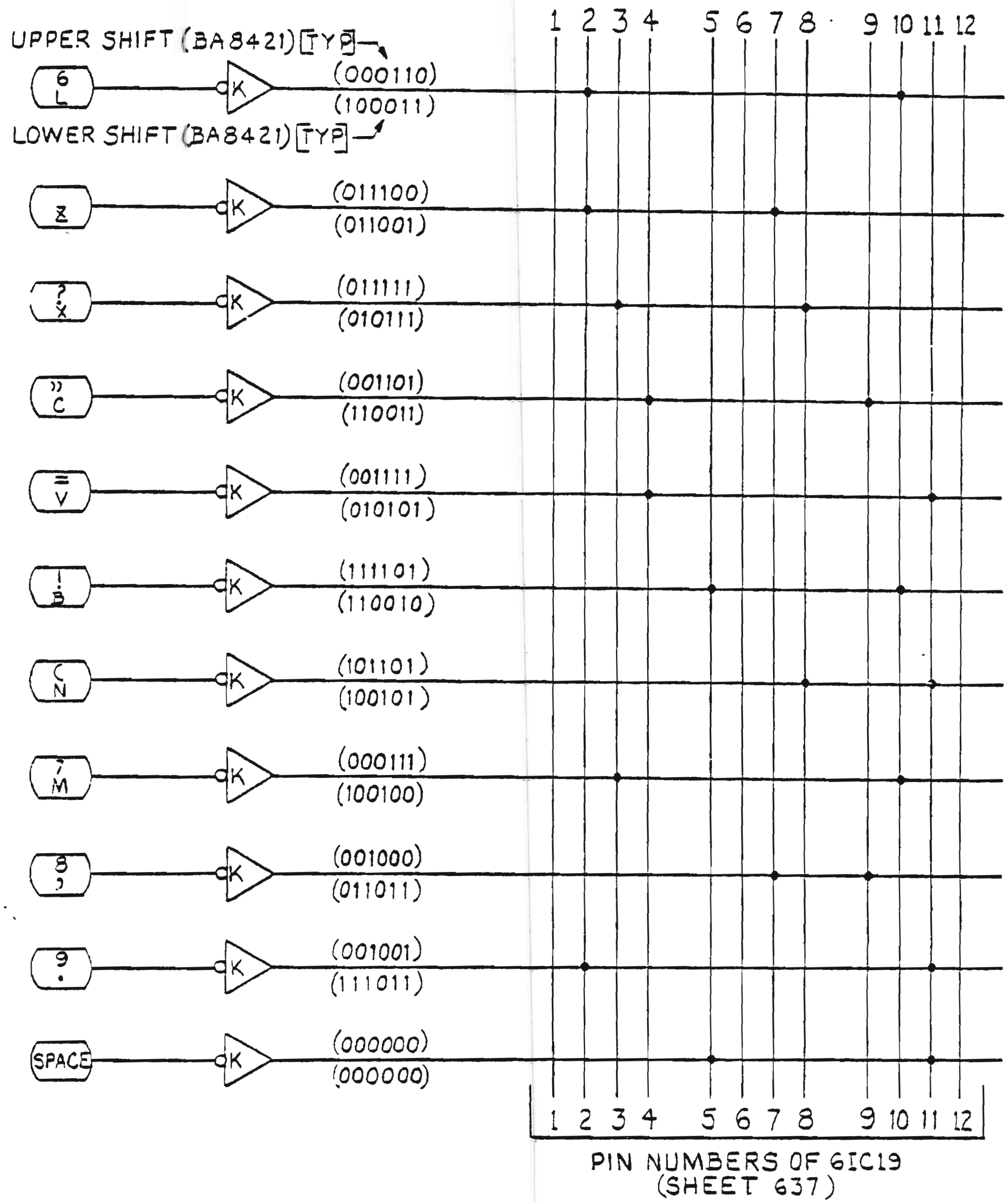
REVISIONS

DRN J. ALESI 6/12/72 ENG
CHK 11/1/72 MFG

TITLE KEYBOARD DATA KEYS, PART 2

DWG NO. C 7634 DASH NO. -00 SH 1 OF 1

C 7635



INFORMATION ON THIS SHEET FOR U.S., U.K. AND ITALIAN KEYBOARDS ONLY.

REV	COMP	ASM	ART WORK	ECN	APPVD-DATE
3	9928-00	9927-00	12721-1C		12 NOV 73
A	5094-01	5093-01	2407-1C		
–	5094-01	5093-01	1418-2C		11-13-72

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DR80-3-34

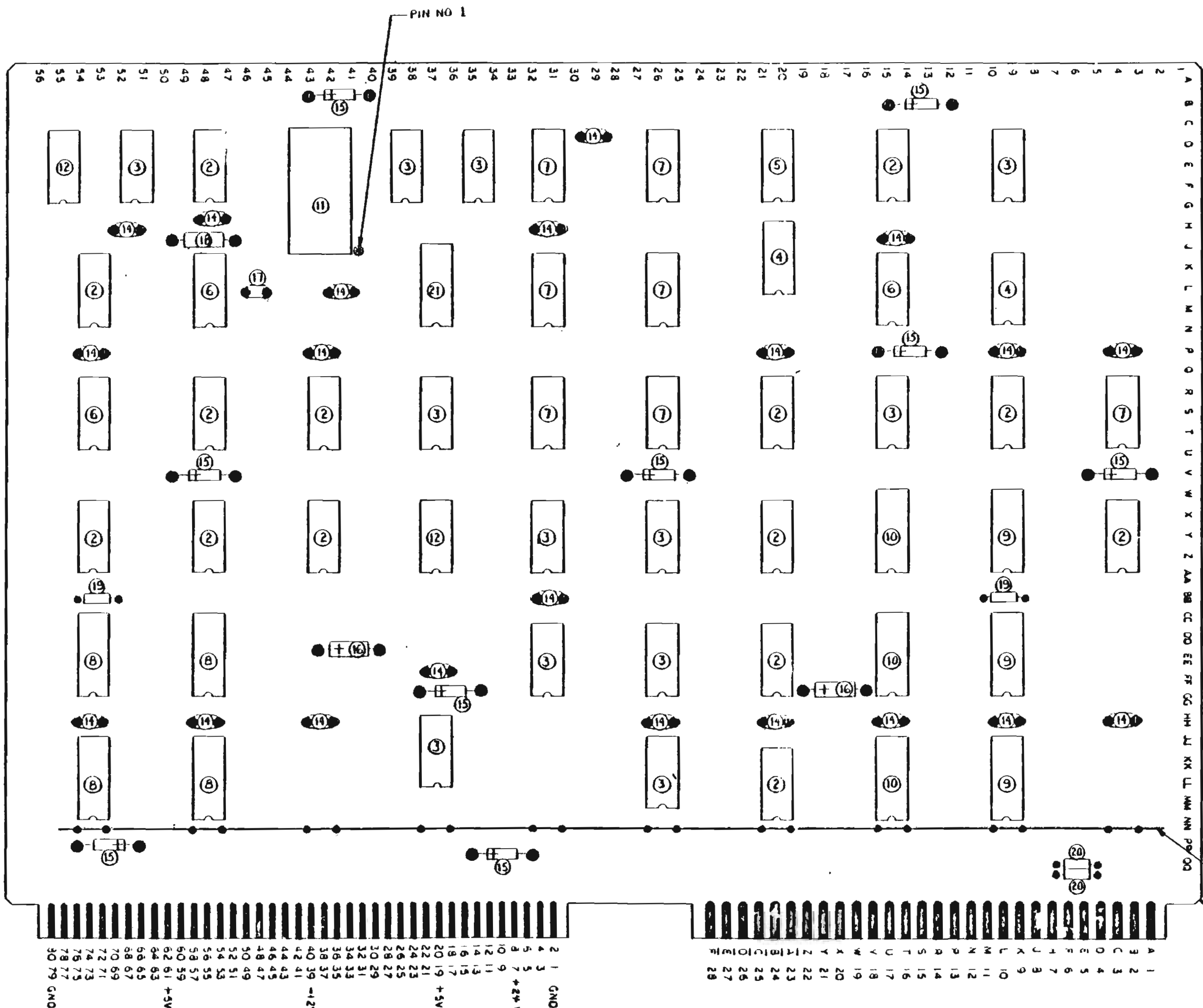
DRN TALESI 9/12/72 ENG

CHK DELTUFFO 11-1-72 MFG

TITLE KEYBOARD DATA KEYS, PART 3

DWG NO. C 7635 DASH NO. -00

REV	DESCRIPTION	APPVD DATE
—	RELEASE/ECN 2721-1C	[illegible]



LOGIC DIAGRAM CHART	
DWG NO	REV
7622	A
7623 SH 1,2	A
7624	C
7625	A
7626	C
7627	B
7628	A
7629	A
7630	B
7631	A
7632	C
7633	B
7634	B
7635	B

1C,D29 1G,H48 1H31 1H52 1H,J15 1P4 1P9 1P20 1P42 1P53 1BB31 1EE,FF37 1HH4 1HH9 1HH15 1HH20 1HH26 1HH42 1HH48 1HH53 — +5V, .02 µf 25V, +80% -30%

1DD,EE41 1FF,GG17 — -12V, 2.2 µf 25V, 20%

1A,B41 1B13 1P14 1V4 1V26 1V48 1FF,GG36 1PP52 1PP,QQ33 — +5V, 4.7 µf 10V, 20%

1L42 — -12V, .02 µf 25V, +80% -30%

TYPE	LOC	OUTPUT PIN NO.
6/1	L53	8
6/1	E48	6
6/1	S42	6
FLIP-FLOP	S4	5,6

SPARE GATES

FOR LIST OF PARTS SEE DETACHED PARTS LIST

COMPANY CONFIDENTIAL INFORMATION [illegible]

PART NO. DR80-B-35

DESCRIPTION: COMPONENT ASSEMBLY, KEYBOARD TRANSLATE

DWG NO. D 9928 DASH NO. -00 SH 1 OF 1

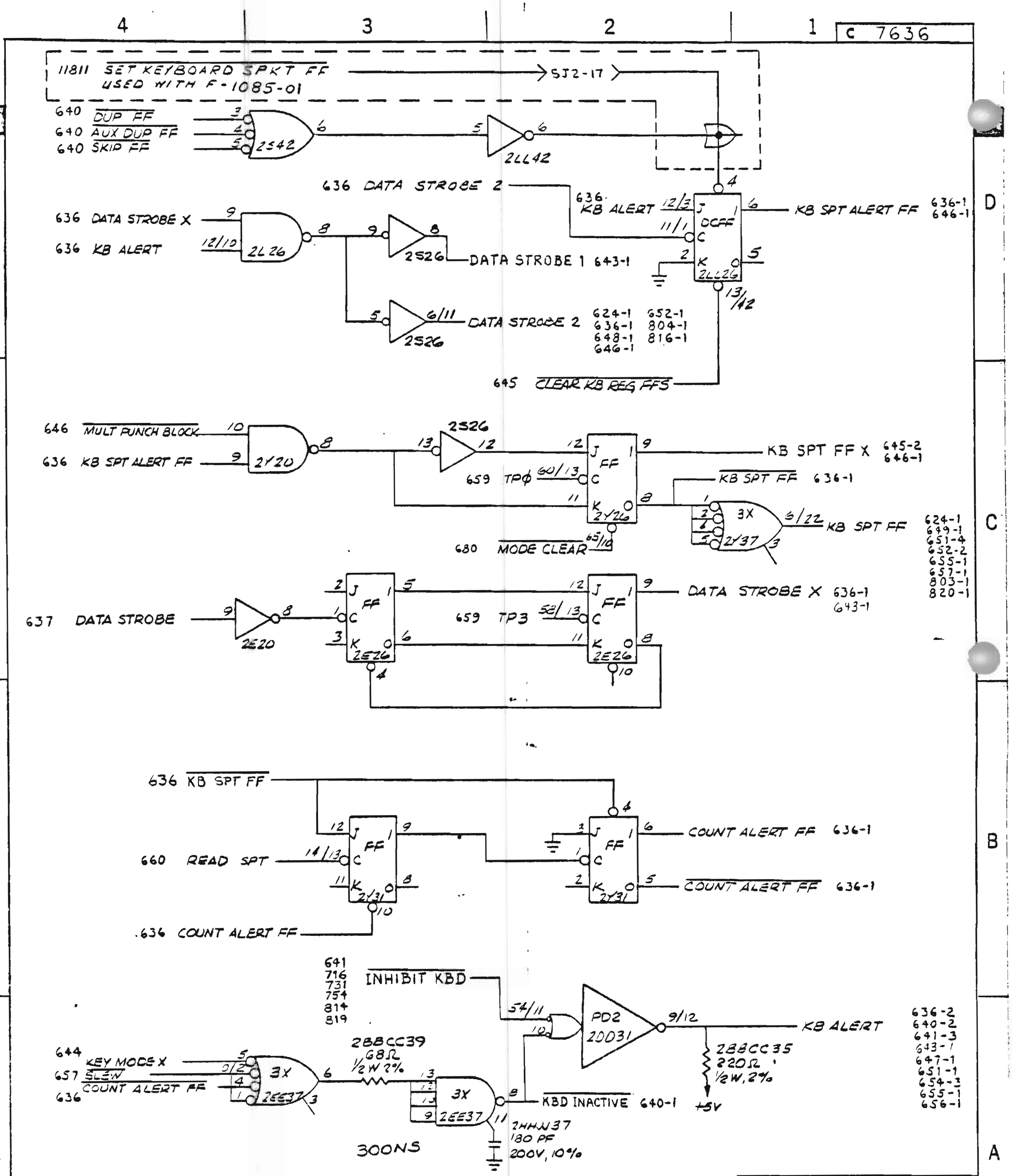
8266 a

DR80 - B - 36

LOGIC/SCHEMATIC INDEX

MANUAL	TYPE/FEATURE		
7545	8001/8010 DR 80 Col	PWB	5095-06
902193	8010-11 DR 360	ASM	5096-06

DOCUMENT	SH	RV	DESCRIPTION LOCATION: 2
			KEYBOARD DATA
7636		-H	Keyboard Sprocket Control
7637		-C	Keyboard Data Encode
7638		-C	Keyboard Function Keys Ft-1
7639		-A	Keyboard Function Keys Ft-2
7640		-B	Function Insert Control
7641		-F	Output Release Control
7642		-F	Read Alert Control
7643		-H	Keyboard Shift Control
7644		-C	Dup/Skip Logic
7645		-D	Keyboard Switches
7646		-B	Clicker Control
5096	02	-M	Schematic - Keyboard Data



DR80-B-38

H	5096-06	5095-06	5664-1C	
G	5096-06	5095-05	3904-1P	
F	5096-05	5095-05	3352-2	
E	5096-05	5095-04	3352-1P	
D	5096-03	5095-03	2266-2C	
C	5096-03	5095-02	2266-1P	
B	5096-02	5095-02	2013-1	
A	5096-02	5095-02		
—	5096-01	5095-01	1419-2	
REV	COMPANENT WORK	ECN	APPVD-DATE	

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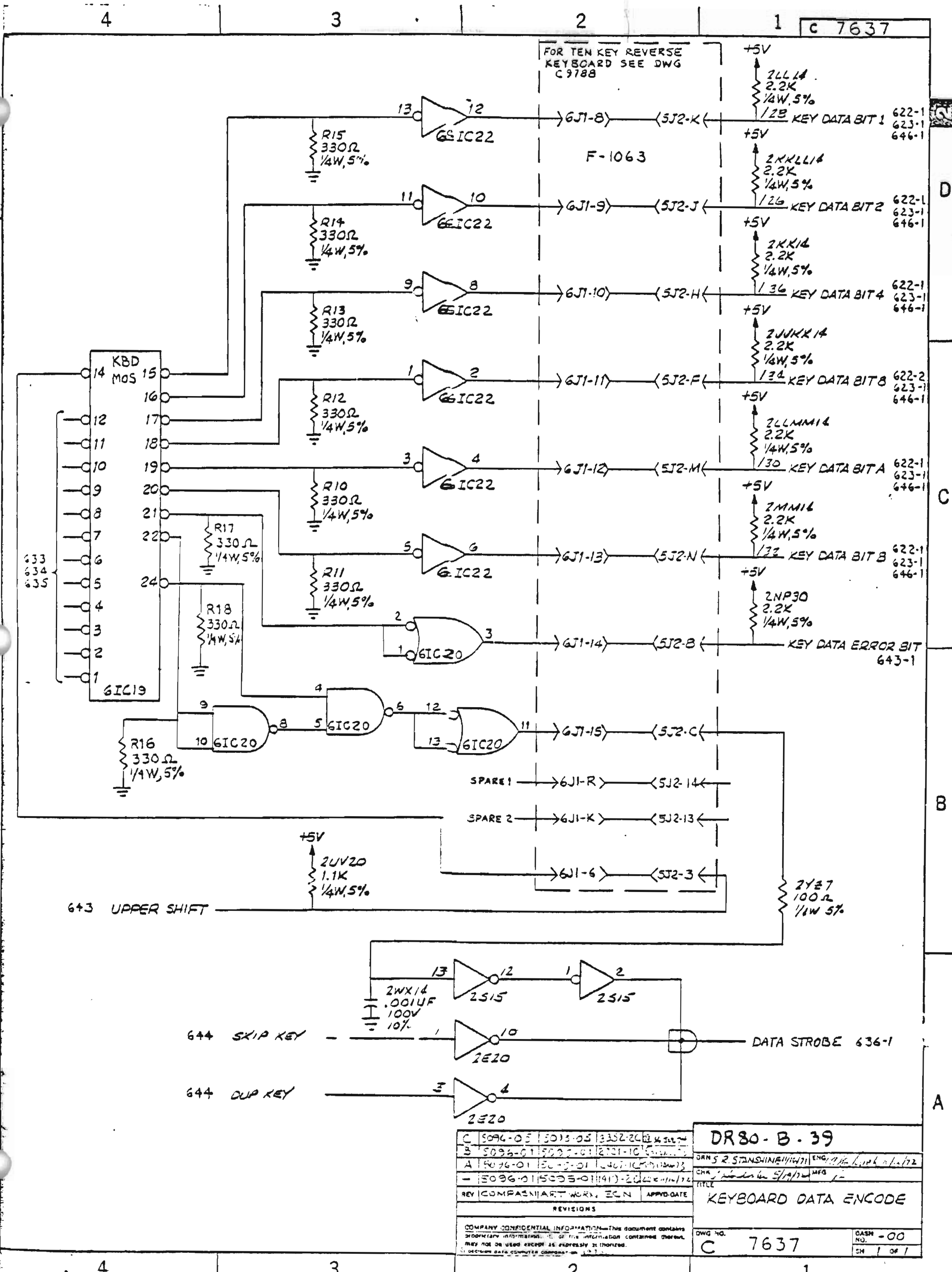
DRN S.R. STANSHINE 11/17/71 | ENG | CHK J. Howden 5/4/72 | MFG

TITLE: KEYBOARD SPROCKET CONTROL

DWG NO. C 7636 | DASH NO. -00 | SH 1 OF 1



C 7637
FOR TEN KEY REVERSE KEYBOARD SEE DWG C9788
F-1063
KBD MOS
6IC19
6IC22
6IC20
2S15
2E20
R15 330Ω 1/4W,5%
R14 330Ω 1/4W,5%
R13 330Ω 1/4W,5%
R12 330Ω 1/4W,5%
R10 330Ω 1/4W,5%
R11 330Ω 1/4W,5%
R17 330Ω 1/4W,5%
R18 330Ω 1/4W,5%
R16 330Ω 1/4W,5%
+5V
2LL14 2.2K 1/4W,5%
2KKLL14 2.2K 1/4W,5%
2KK14 2.2K 1/4W,5%
2JJKK14 2.2K 1/4W,5%
2LLMM14 2.2K 1/4W,5%
2MM14 2.2K 1/4W,5%
2NP30 2.2K 1/4W,5%
6J1-8 5J2-K KEY DATA BIT 1 622-1 623-1 646-1
6J1-9 5J2-J KEY DATA BIT 2 622-1 623-1 646-1
6J1-10 5J2-H KEY DATA BIT 4 622-1 623-1 646-1
6J1-11 5J2-F KEY DATA BIT 8 622-2 623-1 646-1
6J1-12 5J2-M KEY DATA BIT A 622-1 623-1 646-1
6J1-13 5J2-N KEY DATA BIT B 622-1 623-1 646-1
6J1-14 5J2-B KEY DATA ERROR BIT 643-1
6J1-15 5J2-C
SPARE 1 6J1-R 5J2-14
SPARE 2 6J1-K 5J2-13
6J1-6 5J2-3
633 634 635
2UV20 1.1K 1/4W,5%
643 UPPER SHIFT
2YZ7 100Ω 1/4W 5%
2WX14 .001UF 100V 10%
644 SKIP KEY
644 DUP KEY
DATA STROBE 636-1
DR80-B-39
KEYBOARD DATA ENCODE
DWG NO. C 7637
DASH NO. -00
SH 1 OF 1
REV COMPASN ARTWORK ECN APPVD-DATE
REVISIONS
C 5096-05 5095-05 3352-2C
B 5096-01 5095-01 2721-1C
A 5096-01 5095-01 2467-1C
- 5096-01 5095-01 1413-2C
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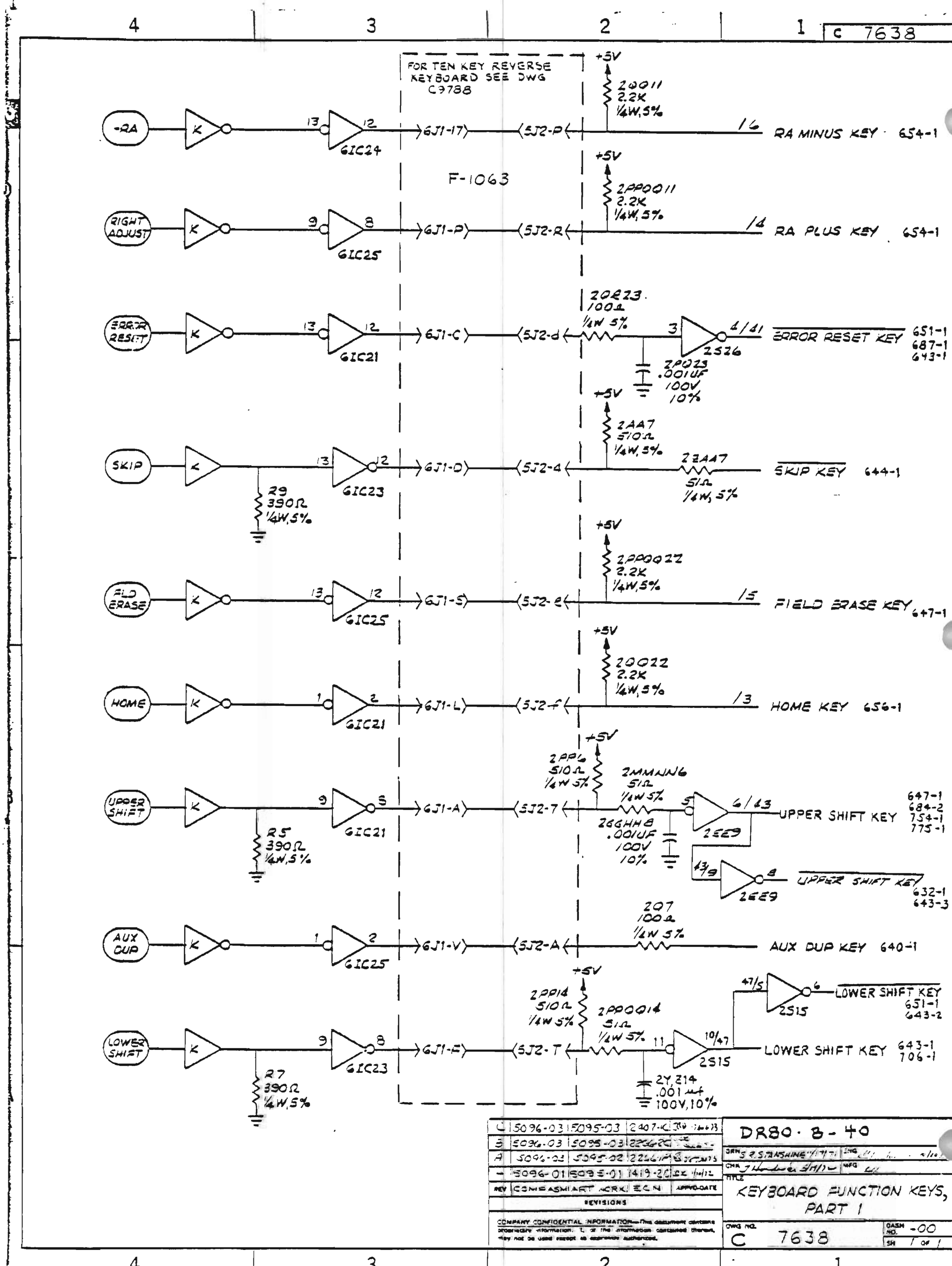
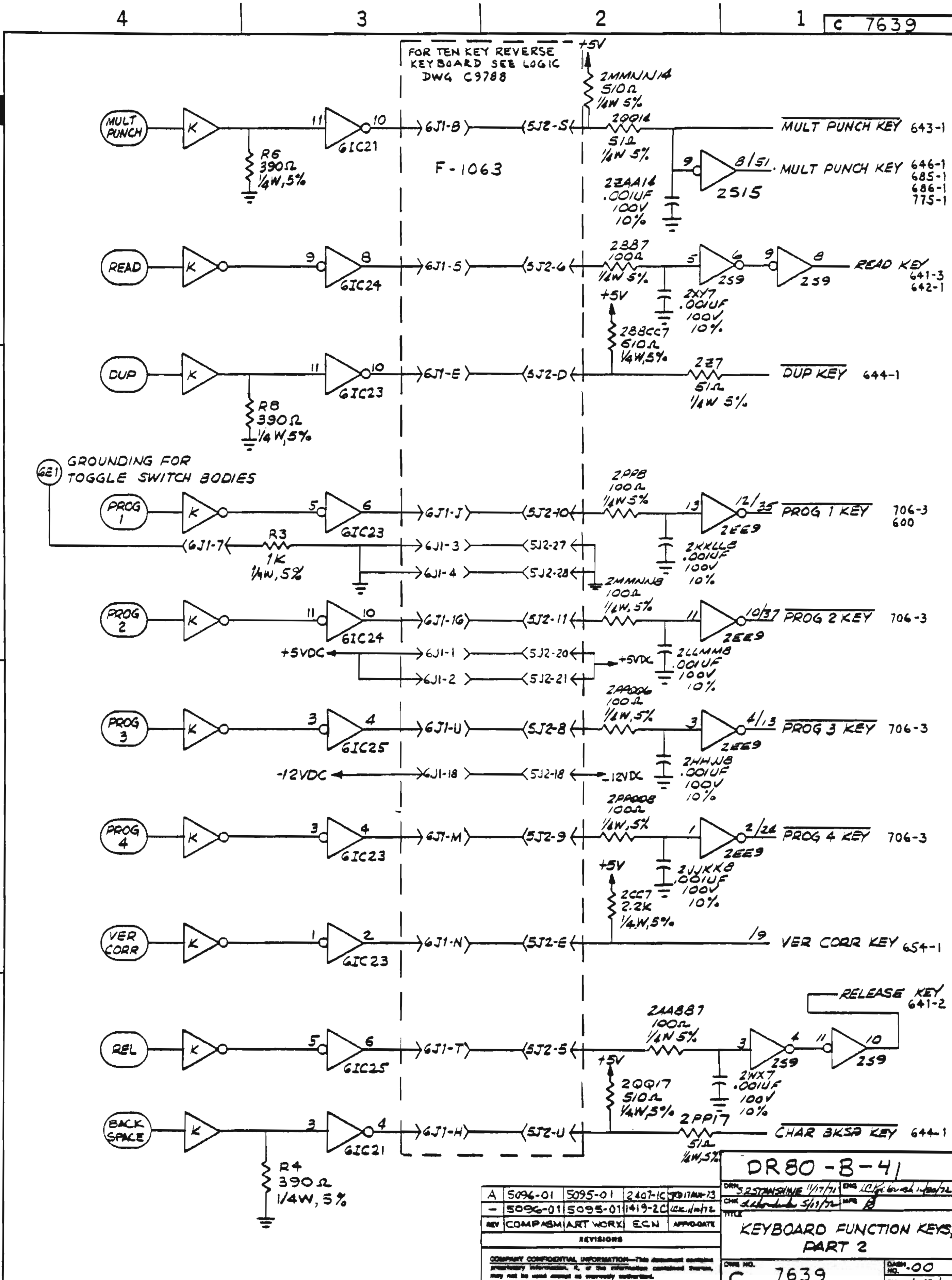
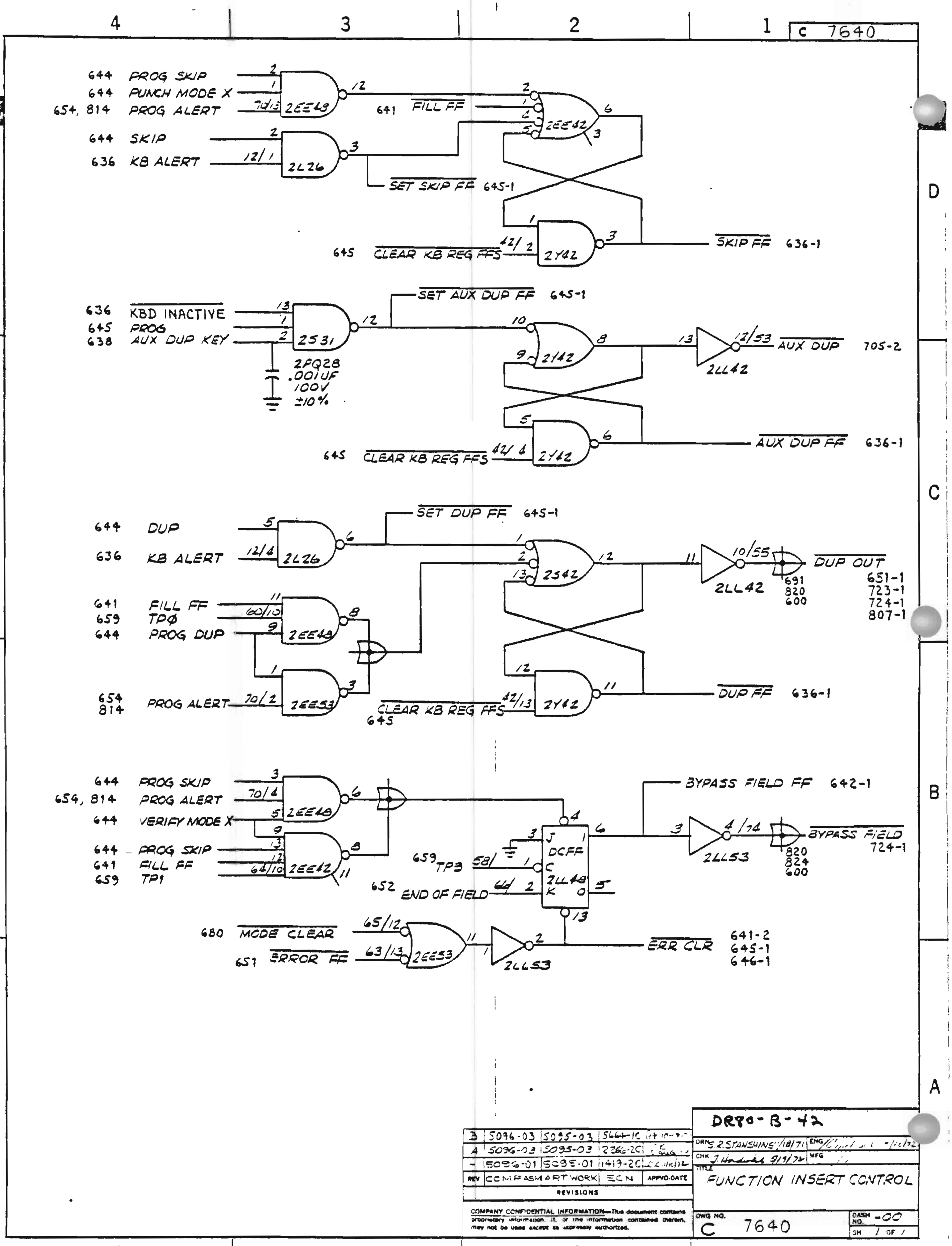

C 7638
FOR TEN KEY REVERSE KEYBOARD SEE DWG C9788
F-1063
-RA
6IC24
6J1-17
5J2-P
+5V
2.2K 1/4W,5%
RA MINUS KEY 654-1
RIGHT ADJUST
6IC25
6J1-P
5J2-R
RA PLUS KEY 654-1
ERROR RESET
6IC21
6J1-C
5J2-d
100Ω 1/4W 5%
.001UF 100V 10%
2S26
ERROR RESET KEY 651-1 687-1 643-1
SKIP
R9 390Ω 1/4W,5%
6IC23
6J1-D
5J2-4
510Ω 1/4W,5%
51Ω 1/4W,5%
SKIP KEY 644-1
FLD ERASE
6IC25
6J1-S
5J2-e
FIELD ERASE KEY 647-1
HOME
6IC21
6J1-L
5J2-f
HOME KEY 656-1
UPPER SHIFT
R5 390Ω 1/4W,5%
6IC21
6J1-A
5J2-7
2EE9
UPPER SHIFT KEY 647-1 684-2 754-1 775-1
UPPER SHIFT KEY 632-1 643-3
AUX DUP
6IC25
6J1-V
5J2-A
100Ω 1/4W 5%
AUX DUP KEY 640-1
LOWER SHIFT
R7 390Ω 1/4W,5%
6IC23
6J1-E
5J2-T
2S15
.001 uf 100V,10%
LOWER SHIFT KEY 651-1 643-2
LOWER SHIFT KEY 643-1 706-1
DR80 · 8 - 40
KEYBOARD FUNCTION KEYS, PART 1
REV | COMP ASM | ART WRK | ECN | APPRO-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION
DWG NO. C 7638
DASH NO. -00
SH 1 OF 1
D
C
B
A
4
3
2
1



C 7639
FOR TEN KEY REVERSE KEYBOARD SEE LOGIC DWG C9788
F-1063
+5V
2MMNN14 510Ω 1/4W 5%
2QQ16 51Ω 1/4W 5%
MULT PUNCH
K
R6 390Ω 1/4W, 5%
6IC21
6J1-B
5J2-S
MULT PUNCH KEY 643-1
2515
MULT PUNCH KEY 646-1 685-1 686-1 775-1
2ZAA14 .001UF 100V 10%
READ
6IC24
6J1-5
5J2-6
2BB7 100Ω 1/4W 5%
2XY7 .001UF 100V 10%
2S9
READ KEY 641-3 642-1
2BBCC7 510Ω 1/4W,5%
DUP
6IC23
R8 390Ω 1/4W,5%
6J1-E
5J2-D
2Z7 51Ω 1/4W 5%
DUP KEY 644-1
6E1
GROUNDING FOR TOGGLE SWITCH BODIES
PROG 1
6J1-J
5J2-10
2PP8 100Ω 1/4W 5%
2EE9
PROG 1 KEY 706-3 600
6J1-7
R3 1K 1/4W, 5%
6J1-3
5J2-27
6J1-4
5J2-28
2KKLL8 .001UF 100V 10%
2MMNN8 100Ω 1/4W, 5%
PROG 2
6J1-16
5J2-11
PROG 2 KEY 706-3
+5VDC
6J1-1
5J2-20
6J1-2
5J2-21
2LLMM8 .001UF 100V 10%
2PPQQ6 100Ω 1/4W, 5%
PROG 3
6IC25
6J1-U
5J2-8
PROG 3 KEY 706-3
-12VDC
6J1-18
5J2-18
2HHJJ8 .001UF 100V 10%
2PPQQ8 100Ω 1/4W, 5%
PROG 4
6J1-M
5J2-9
PROG 4 KEY 706-3
2JJKK8 .001UF 100V 10%
2CC7 2.2K 1/4W,5%
VER CORR
6J1-N
5J2-E
VER CORR KEY 654-1
RELEASE KEY 641-2
2AABB7 100Ω 1/4N 5%
REL
6J1-T
5J2-5
2WX7 .001UF 100V 10%
2QQ17 510Ω 1/4W,5%
2PP17 51Ω 1/4W,5%
BACK SPACE
6J1-H
5J2-U
CHAR BKSP KEY 644-1
R4 390Ω 1/4W, 5%
DR80-8-41
A 5096-01 5095-01 2407-1C
— 5096-01 5095-01 1419-2C
REV COMP ASM ART WORK ECN APPD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION
KEYBOARD FUNCTION KEYS, PART 2
DWG NO. C 7639
DASH NO. -00
SH 1 OF 1



C 7640
644 PROG SKIP
644 PUNCH MODE X
654, 814 PROG ALERT
2EE48
641 FILL FF
2EE42
644 SKIP
636 KB ALERT
2L26
SET SKIP FF 645-1
645 CLEAR KB REG FFS
2Y42
SKIP FF 636-1
636 KBD INACTIVE
645 PROG
638 AUX DUP KEY
2531
2PQ28
.001UF
100V
±10%
SET AUX DUP FF 645-1
2Y42
2LL42
AUX DUP 705-2
645 CLEAR KB REG FFS
AUX DUP FF 636-1
644 DUP
636 KB ALERT
2L26
SET DUP FF 645-1
2542
2LL42
DUP OUT
691
820
600
651-1
723-1
724-1
807-1
641 FILL FF
659 TPØ
644 PROG DUP
2EE48
654
814
PROG ALERT
2EE53
CLEAR KB REG FFS
645
2Y42
DUP FF 636-1
644 PROG SKIP
654, 814 PROG ALERT
644 VERIFY MODE X
2EE48
644 PROG SKIP
641 FILL FF
659 TP1
2EE42
659 TP3
652 END OF FIELD
DCFF
2LL48
BYPASS FIELD FF 642-1
2LL53
BYPASS FIELD
724-1
820
824
600
680 MODE CLEAR
651 ERROR FF
2EE53
2LL53
ERR CLR
641-2
645-1
646-1
DR80-B-42
B 5096-03 5095-03 5644-1C
A 5096-03 5095-03 2266-2C
- 5096-01 5095-01 1419-2C
REV COMP ASM ART WORK ECN APPVD-DATE
REVISIONS
DRN S. Z. STANSHINE 10/18/71
CHK 9/19/72
TITLE FUNCTION INSERT CONTROL
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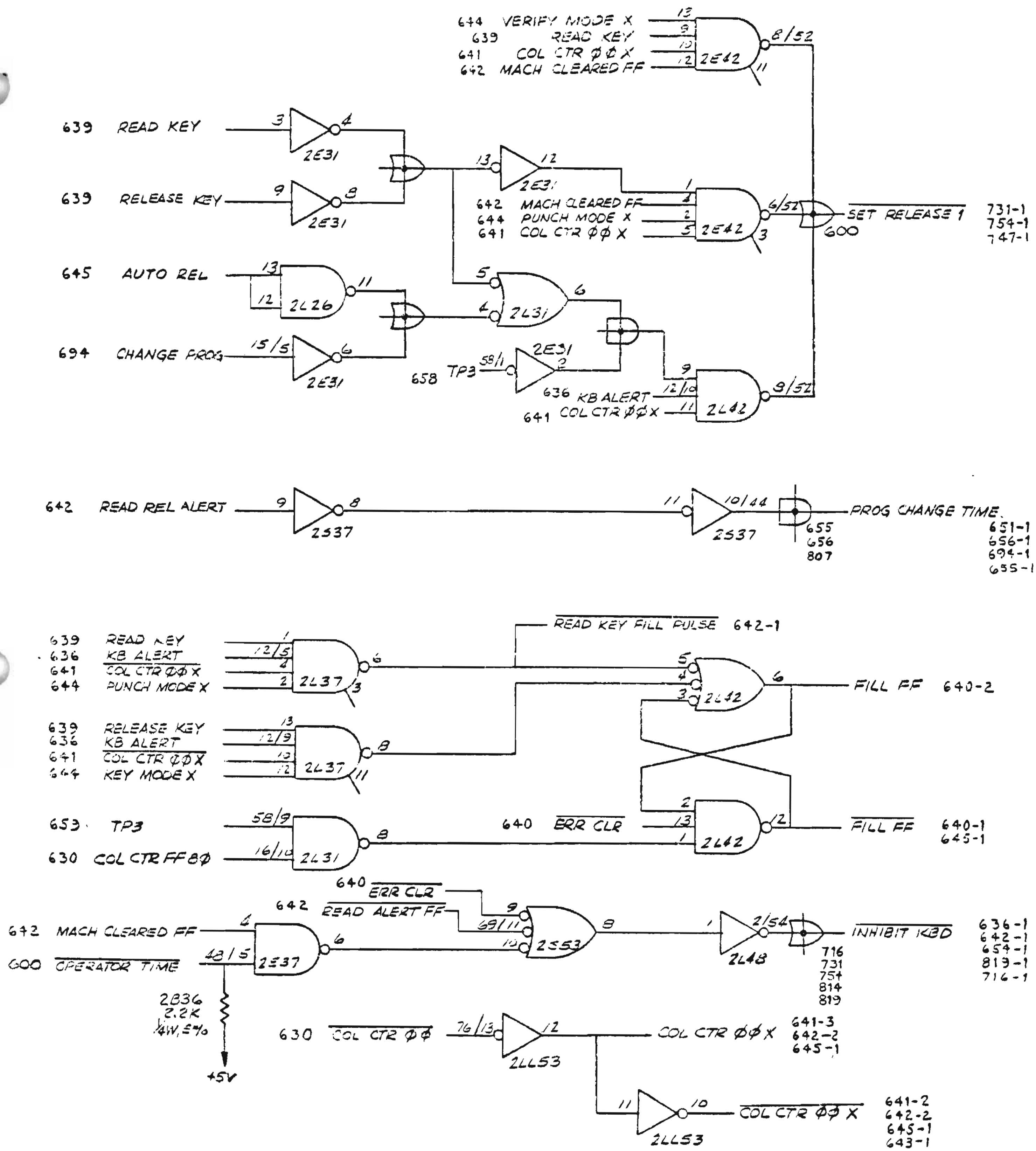
DWG NO. C 7640
DASH NO. -00
SH 1 OF 1



REV	COMP A SM	ART WORK	ECN	APPVD-DATE
F	5096-05	5095-05	3352-20	
E	5096-04	5095-03	2511-18	
D	5096-03	5095-03	2266-1	
C	5096-03	5095-02	2261-1	
B	5096-02	5095-02	2011-8	
A	5096-02	5095-02	1648-2	
–	5096-01	5095-01	1419-20	

REVISIONS

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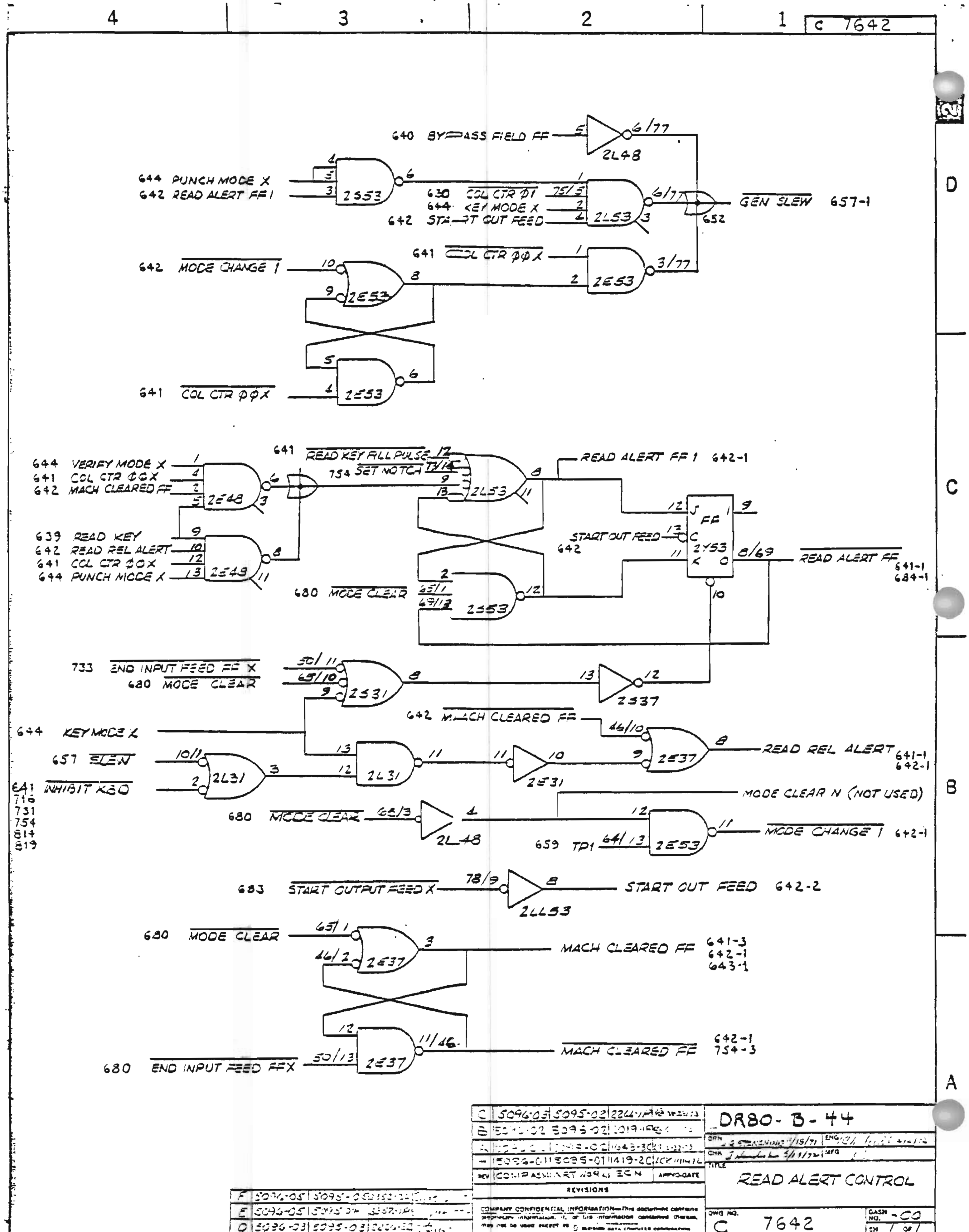
DR 80-B-43

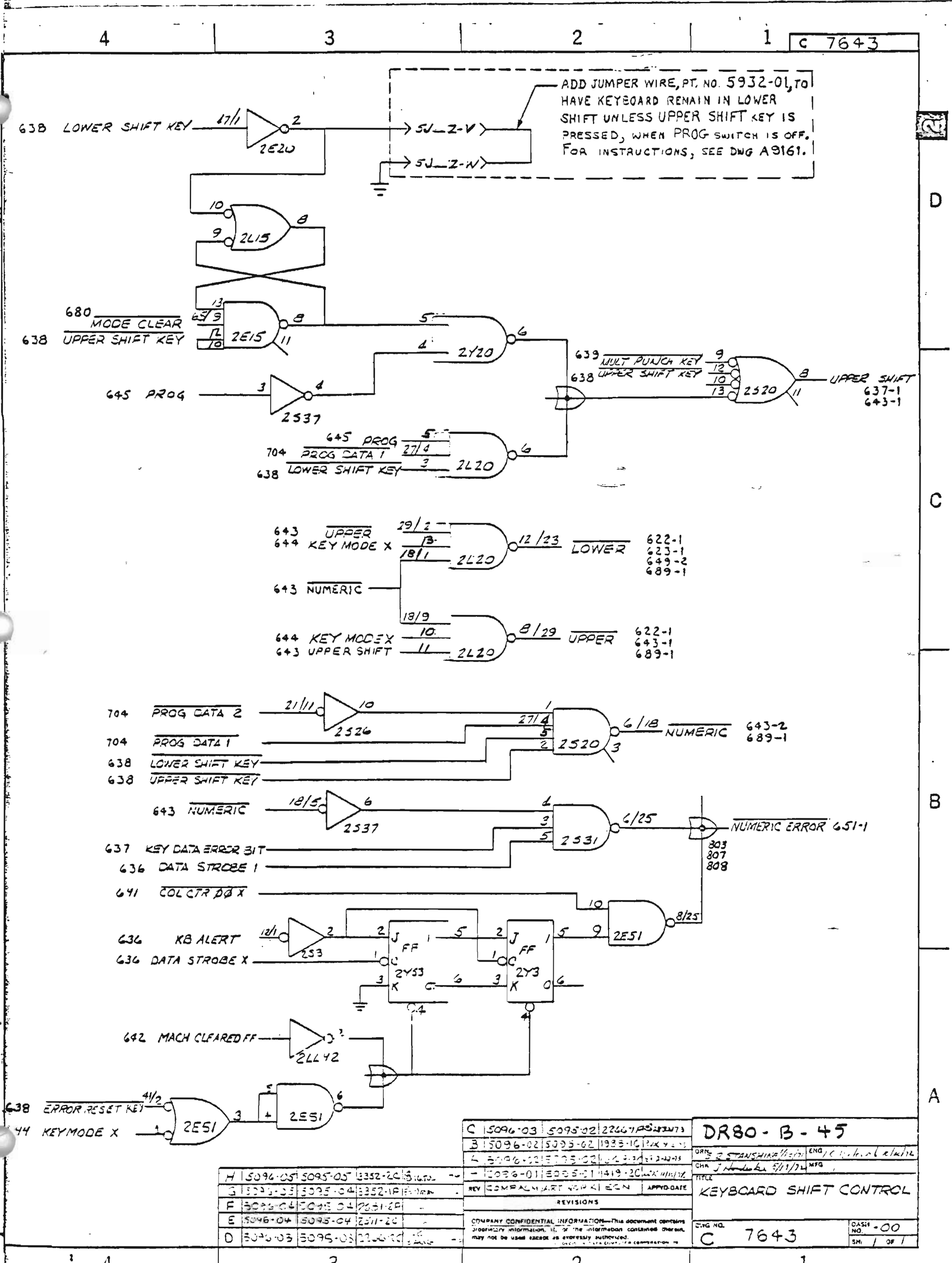
DRN B. STANSBURY 11/18/71 ENG

CHK 5/13/74 MFG

TITLE OUTPUT RELEASE CONTROL

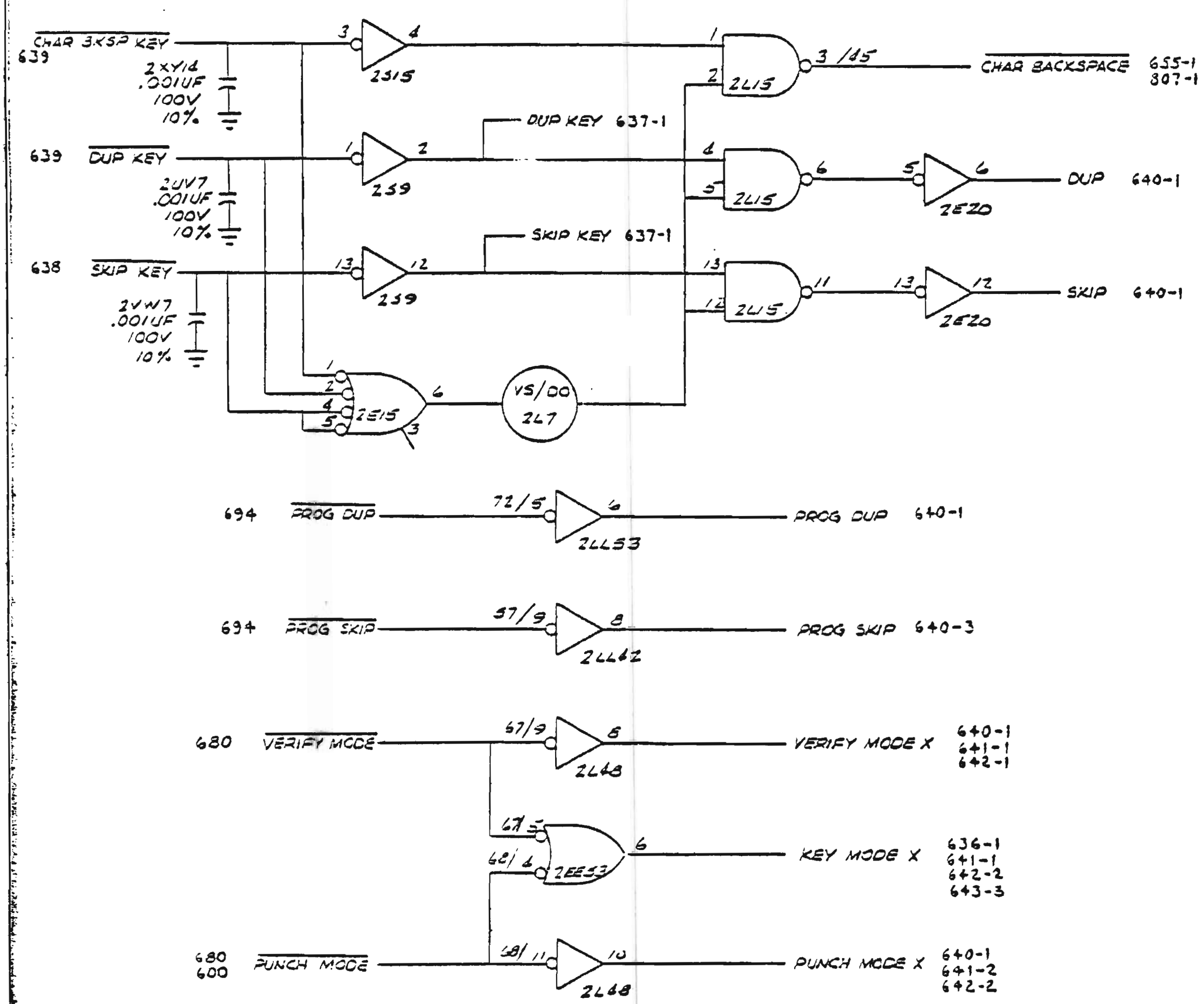
DWG NO. C 7641 DASH NO -00 SH 1 OF 1


C 7642
640 BYPASS FIELD FF
2L48
644 PUNCH MODE X
642 READ ALERT FF 1
2S53
630 COL CTR Ø1
644 KEY MODE X
642 START OUT FEED
2L53
652
GEN SLEW 657-1
641 COL CTR ØØX
642 MODE CHANGE 1
2E53
641 COL CTR ØØX
644 VERIFY MODE X
641 COL CTR ØOX
642 MACH CLEARED FF
2E48
639 READ KEY
642 READ REL ALERT
641 COL CTR ØOX
644 PUNCH MODE X
2E49
641 READ KEY FILL PULSE
754 SET NOTCH
2L53
READ ALERT FF 1 642-1
START OUT FEED
642
FF
2Y53
READ ALERT FF 641-1 684-1
680 MODE CLEAR
2S53
733 END INPUT FEED FF X
680 MODE CLEAR
2S31
2S37
644 KEY MODE X
642 MACH CLEARED FF
2E37
READ REL ALERT 641-1 642-1
657 SLEW
641 716 731 754 814 819 INHIBIT KBO
2L31
2E31
MODE CLEAR N (NOT USED)
680 MODE CLEAR
2L48
659 TP1
MODE CHANGE 1 642-1
683 START OUTPUT FEED X
2LL53
START OUT FEED 642-2
680 MODE CLEAR
MACH CLEARED FF 641-3 642-1 643-1
MACH CLEARED FF 642-1 754-3
680 END INPUT FEED FFX
DR80-B-44
REVISIONS
COMPANY CONFIDENTIAL INFORMATION
READ ALERT CONTROL
DWG NO. C 7642



C 7643
ADD JUMPER WIRE, PT. NO. 5932-01, TO HAVE KEYBOARD REMAIN IN LOWER SHIFT UNLESS UPPER SHIFT KEY IS PRESSED, WHEN PROG SWITCH IS OFF. FOR INSTRUCTIONS, SEE DWG A9161.
5J_Z-V
5J_Z-W
638 LOWER SHIFT KEY
2E20
2L15
680 MODE CLEAR
638 UPPER SHIFT KEY
2E15
2Y20
645 PROG
2S37
645 PROG
704 PROG DATA 1
638 LOWER SHIFT KEY
2L20
639 MULT PUNCH KEY
638 UPPER SHIFT KEY
2S20
UPPER SHIFT
637-1
643-1
643 UPPER
644 KEY MODE X
2L20
LOWER
622-1
623-1
649-2
689-1
643 NUMERIC
644 KEY MODE X
643 UPPER SHIFT
2L20
UPPER
622-1
643-1
689-1
704 PROG DATA 2
704 PROG DATA 1
638 LOWER SHIFT KEY
638 UPPER SHIFT KEY
2S26
2S20
NUMERIC
643-2
689-1
643 NUMERIC
2S37
637 KEY DATA ERROR BIT
636 DATA STROBE 1
2S31
NUMERIC ERROR 651-1
803
807
808
641 COL CTR ØØ X
2E51
636 KB ALERT
636 DATA STROBE X
2S3
FF
2Y53
FF
2Y3
642 MACH CLEARED FF
2LL42
638 ERROR RESET KEY
644 KEYMODE X
2E51
2E51
DR80-B-45
KEYBOARD SHIFT CONTROL
REVISIONS
COMPANY CONFIDENTIAL INFORMATION—This document contains proprietary information. It, or the information contained therein, may not be used except as expressly authorized.
DWG NO. C 7643
DASH NO. -00
SH 1 OF 1

4 3 2 1 C 7644



C	5096-05	5095-05	3352-22	
B	5096-03	5095-03		
A	5096-03	5095-02	2264	
-	5096-01	5095-01	419-2	
REV	CON. PASM	ART WORK	ECN	APPRO-DATE

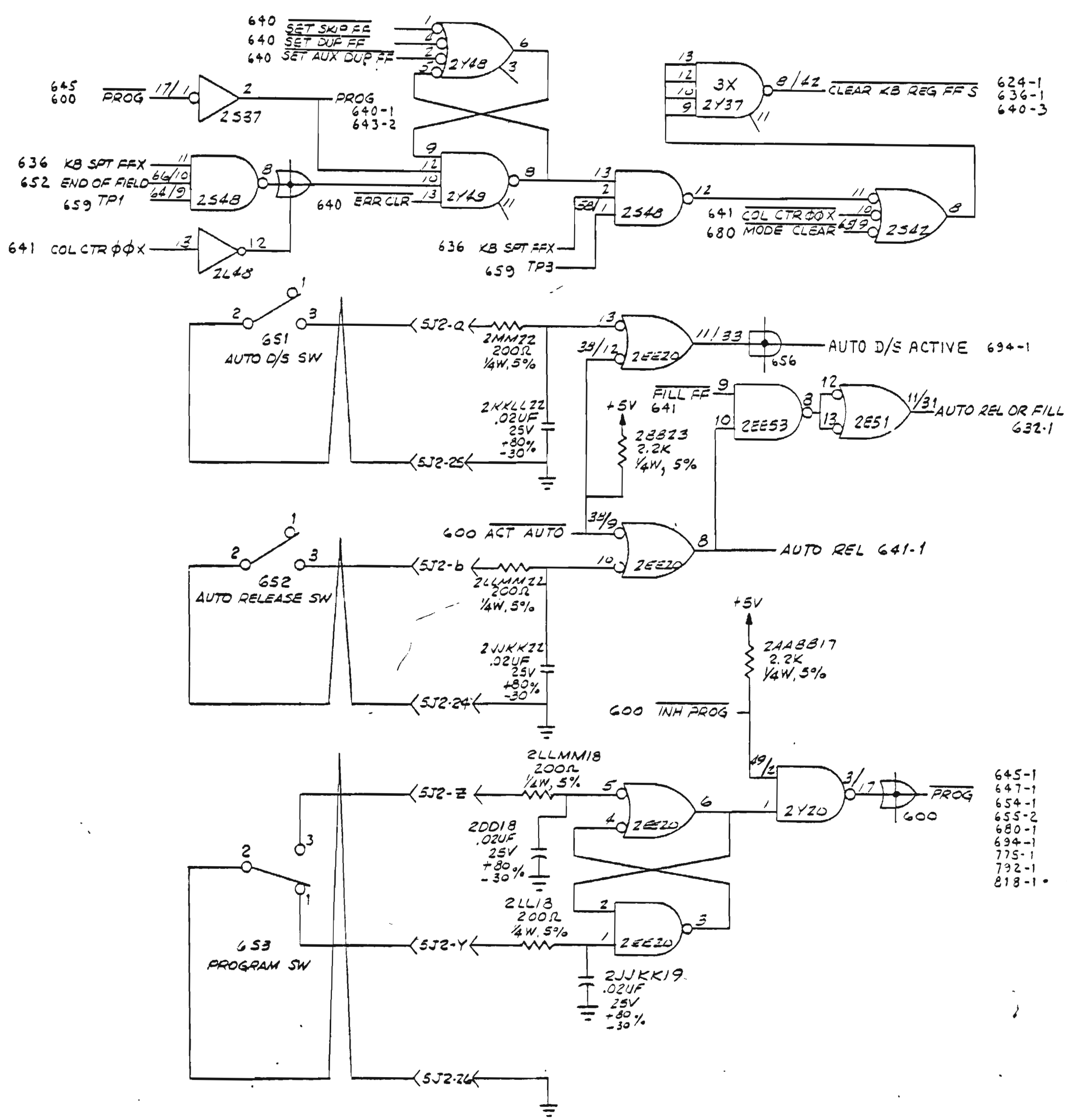
REVISIONS

DR80 - B - 46

DRN | ENG | CHK | MFG

TITLE DUP/SKIP LOGIC

DWG NO. C 7644 DASH NO. -00



REV	COMP ASM	ART WORK	ECN	APPVD-DATE
D	5096-05	5095-05	3352-2C	11 JUL 74
C	5096-04	5095-03	2511-1P	11-20-73
B	5096-03	5095-03	2266-2C	7 AUG 73
A	5096-02	5095-02	1643-3C	2-22-73
-	5096-01	5095-01	1419-2C	11/10/72

REVISIONS

DR80-B-47 D

DRN S.R. STANSWINE 11/10/72 ENG

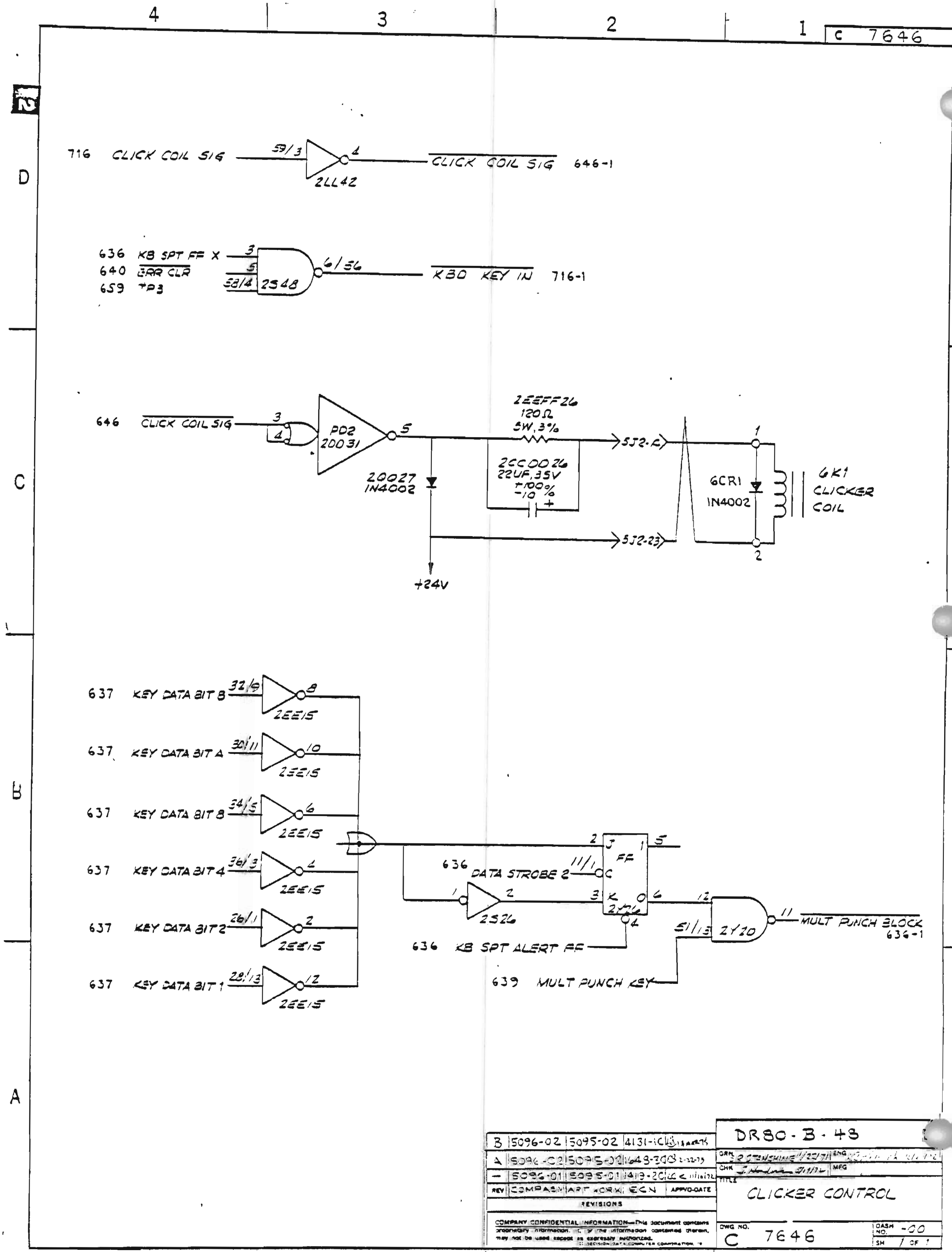
CHK J. Hardin 5/19/72 MFG

TITLE KEYBOARD SWITCHES

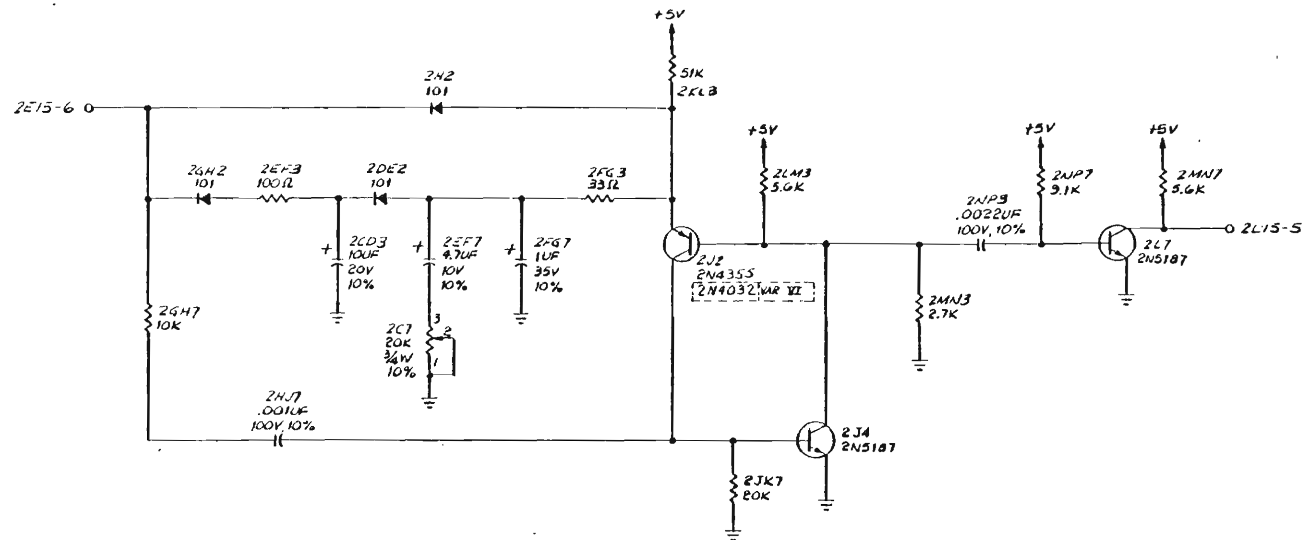
DWG NO. C 7645

DASH NO. -00

SH 1 OF 1

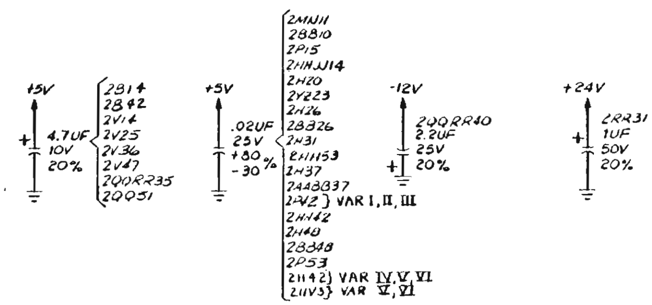


C 7646
716 CLICK COIL SIG
59/3
2LL42
CLICK COIL SIG 646-1
636 KB SPT FF X
640 ERR CLR
659 TP3
2S48
6/56
KBD KEY IN 716-1
646 CLICK COIL SIG
PD2
20031
2EEFF26
120Ω
5W, 3%
2CCDD26
22UF, 35V
+100% −10%
2DD27
IN4002
5J2-A
5J2-23
+24V
6CR1
IN4002
6K1
CLICKER
COIL
637 KEY DATA BIT B
637 KEY DATA BIT A
637 KEY DATA BIT 8
637 KEY DATA BIT 4
637 KEY DATA BIT 2
637 KEY DATA BIT 1
2EE15
636 DATA STROBE 2
FF
2S26
636 KB SPT ALERT FF
2Y20
MULT PUNCH BLOCK 636-1
639 MULT PUNCH KEY
DR80-B-43
CLICKER CONTROL
B 5096-02 5095-02 4131-1C
A 5096-02 5095-02 1643-30
— 5096-01 5095-01 1419-2C
REV COMPASM ART WORK ECN APPVD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION—This document contains proprietary information, and the information contained therein, may not be used except as expressly authorized.
DECISION DATA COMPUTER CORPORATION
DWG NO. C 7646
DASH NO. -00
SH 1 OF 1



VS/DO

NOTES:
1. UNLESS OTHERWISE SPECIFIED: ALL RESISTANCE VALUES ARE IN OHMS, 1/2W, 2%.



* UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS.

* VARIATION REFERENCE

VAR	ASSEMBLY	ARTWORK
I	5096-01,02,03,04	5095-01
II	5096-02,03,04	5095-02
III	5096-03,04	5095-03
IV	5096-04	5095-04
V	5096-05	5095-05
VI	5096-06	5095-06

REV	DESCRIPTION	APPD DATE
A	RELEASE PT-01/ECN 1419-2C	
D	REVISED PER ECN 1648-3C	
H	REVISED PER ECN 2511-2C	
K	REVISED/ECN 3352-2C	
M	REVISED/ECN 3904 2C	

COMPANY CONFIDENTIAL INFORMATION

DR80-B-49 M

COMPONENT ASSEMBLY KEYBOARD DATA

D 5096

SH 2 OF

DR 80 - B - 50

LOGIC/SCHEMATIC CONTENTS

PCB LOC 3

TYPE NUMBER/NAME
BASIC 8001, 8010 INTERFACE

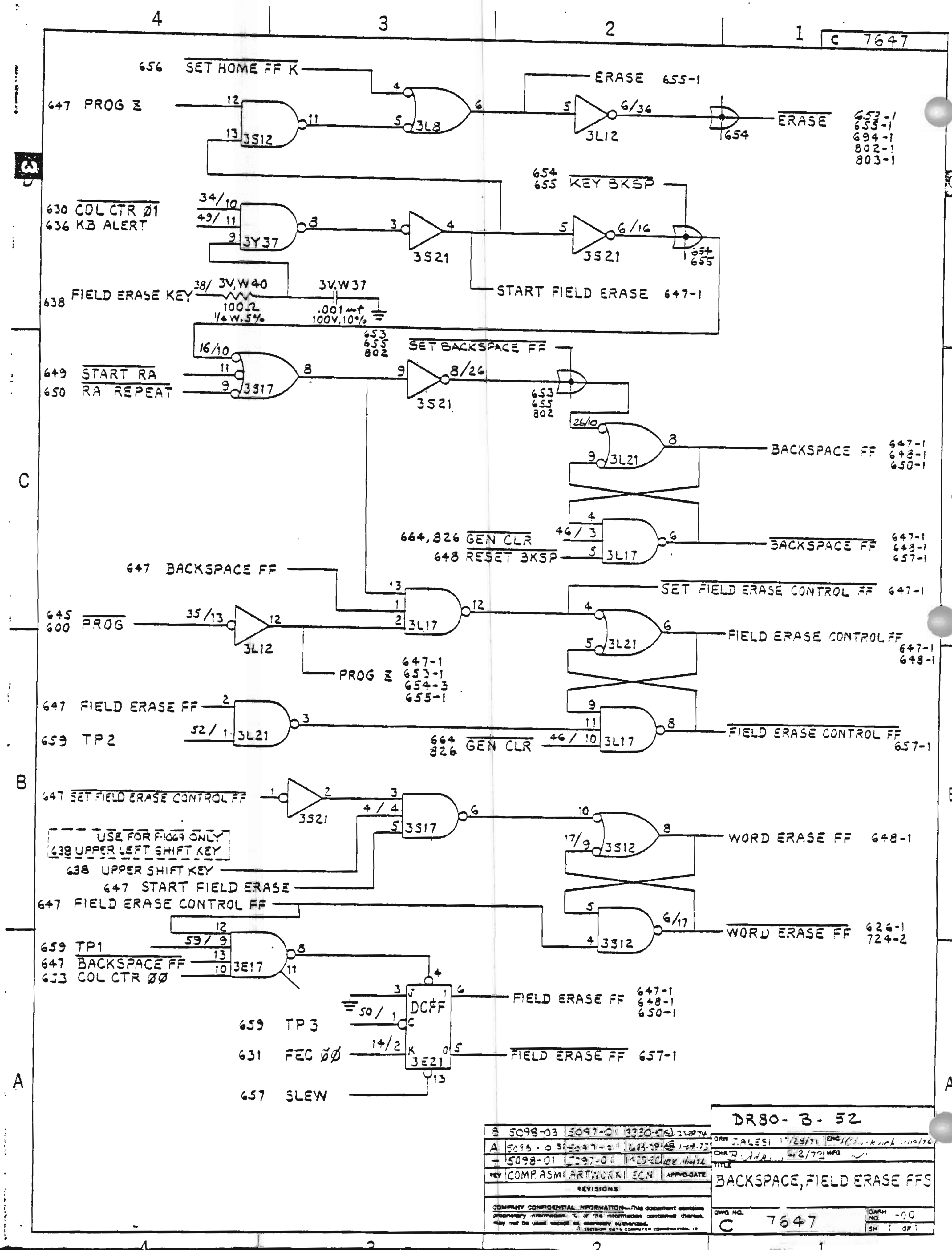
DWG NO	TITLE	PCB/ASM DRAWING DASH NO
5097	PWB	07
5098	KEYBOARD FUNCTIONS	07

LOGIC SCHEMATIC	LOGIC DRAWING TITLE SCHEMATIC SHEET NO	LOGIC/SCHEMATIC DRAWING REVISION
7647	BACKSPACE, FIELD ERASE FF's	B
7648	BACKSPACE COUNTER	A
7649	PUNCH RIGHT ADJUST PT-1	E
7650	" " " PT. 2	A
7651	ERROR CONTROL	D
7652	VERIFY RIGHT ADJUST PT-1	E
7653	" " " PT. 2	B
7654	VERIFY CORRECT CONTROL	C
7655	REVERIFY CONTROL	B
7656	FIRST COLUMN HOME FF	G
7657	SLEW, STEP CONTROL	A
5098	Sheet 3	K

REVISION OF CONTENTS	K
DATE OF ECN	10 SEP 74 29 OCT 73
APPROVAL	
ENGINEERING CHANGE NOTICE	3330-1C 2647-1C

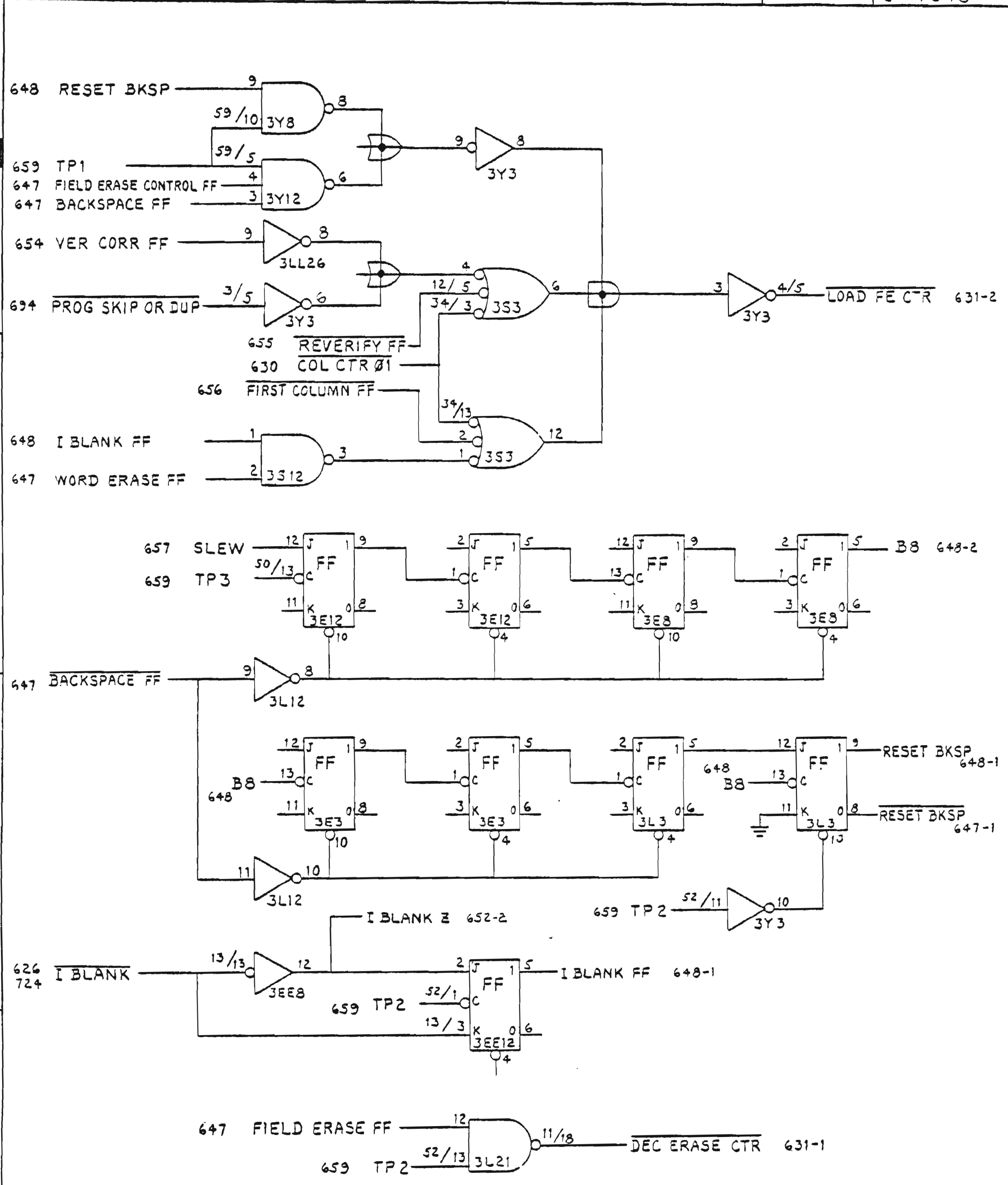
DWG NO 5098 REV — SHT — OF

DR80-B-51



C 7647
656 SET HOME FF K
647 PROG Z
ERASE 655-1
3S12
3L8
3L12
6/36
654
ERASE 653-1 655-1 694-1 802-1 803-1
654 655 KEY BKSP
630 COL CTR Ø1
636 KB ALERT
34/10
49/11
3Y37
3S21
6/16
654 655
638 FIELD ERASE KEY
38/ 3V,W40
100Ω 1/4W,5%
3V,W37
.001 mf 100V,10%
START FIELD ERASE 647-1
653 655 802
SET BACKSPACE FF
16/10
649 START RA
650 RA REPEAT
3S17
8/26
26/10
3L21
BACKSPACE FF 647-1 648-1 630-1
664,826 GEN CLR
648 RESET BKSP
46/3
3L17
BACKSPACE FF 647-1 648-1 657-1
647 BACKSPACE FF
645 600 PROG
35/13
SET FIELD ERASE CONTROL FF 647-1
FIELD ERASE CONTROL FF 647-1 648-1
PROG Z 647-1 653-1 654-3 655-1
647 FIELD ERASE FF
659 TP2
52/1
664 826 GEN CLR
46/10
FIELD ERASE CONTROL FF 657-1
647 SET FIELD ERASE CONTROL FF
4/4
USE FOR F-1069 ONLY
638 UPPER LEFT SHIFT KEY
638 UPPER SHIFT KEY
647 START FIELD ERASE
647 FIELD ERASE CONTROL FF
17/9
WORD ERASE FF 648-1
6/17
WORD ERASE FF 626-1 724-2
659 TP1
59/9
647 BACKSPACE FF
653 COL CTR ØØ
3E17
50/1
DCFF
FIELD ERASE FF 647-1 648-1 650-1
659 TP3
631 FEC ØØ
14/2
3E21
FIELD ERASE FF 657-1
657 SLEW
DR80-3-52
B 5098-03 5097-01 3330-04
A 5098-03 5097-01 683-29
- 5098-01 5097-01 1420-20
REV COMP ASM ARTWORK ECN APPVD-DATE
REVISIONS
DRN J.ALESI 11/29/71
TITLE BACKSPACE, FIELD ERASE FFS
COMPANY CONFIDENTIAL INFORMATION
DWG NO. C 7647
-00
SH 1 OF 1

C 7648



DR80- B- 53

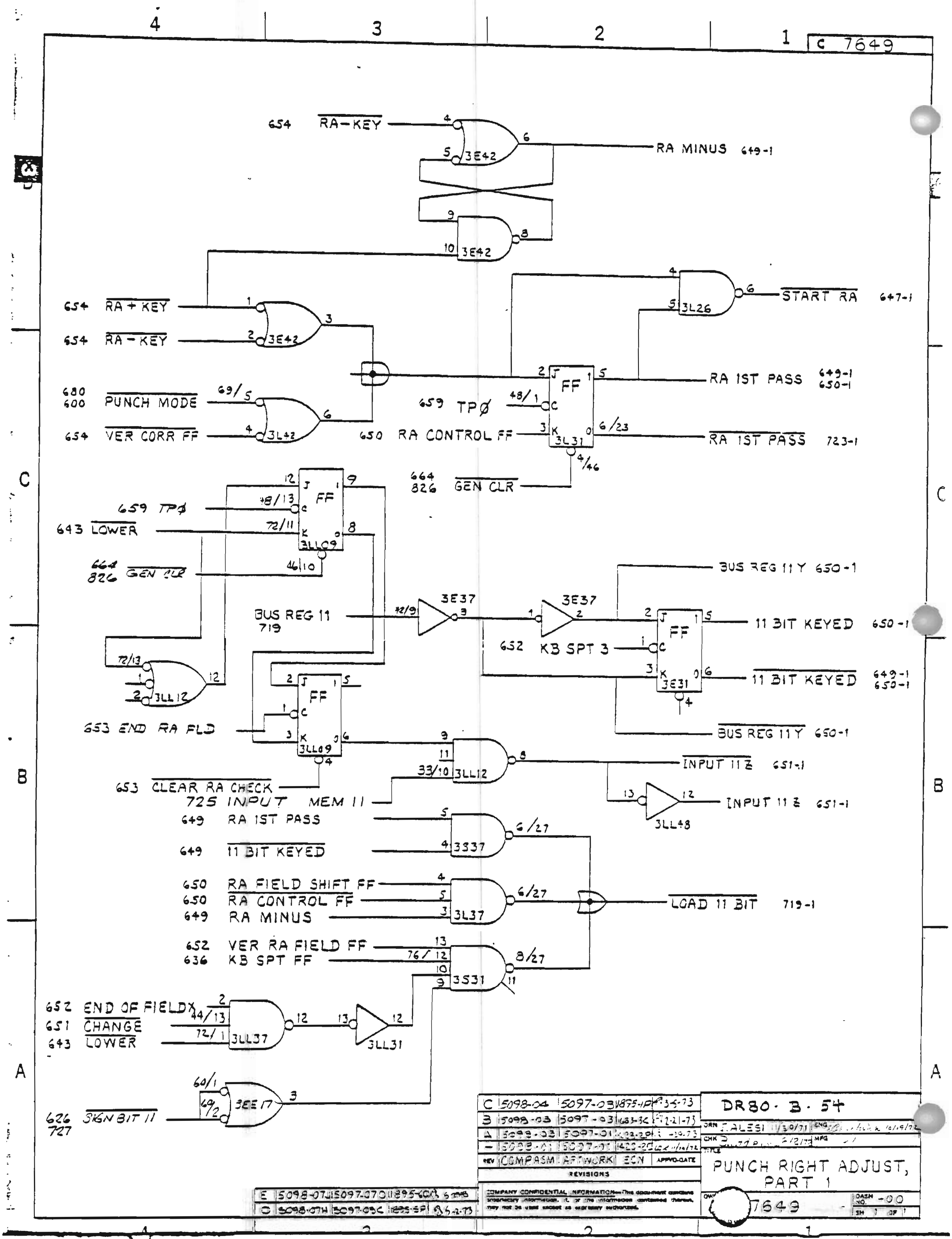
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	5098-05	5097-03	1895-29	
–	5098-01	5097-01	1420-2C	

REVISIONS

DRN J. ALESI 12/1/71

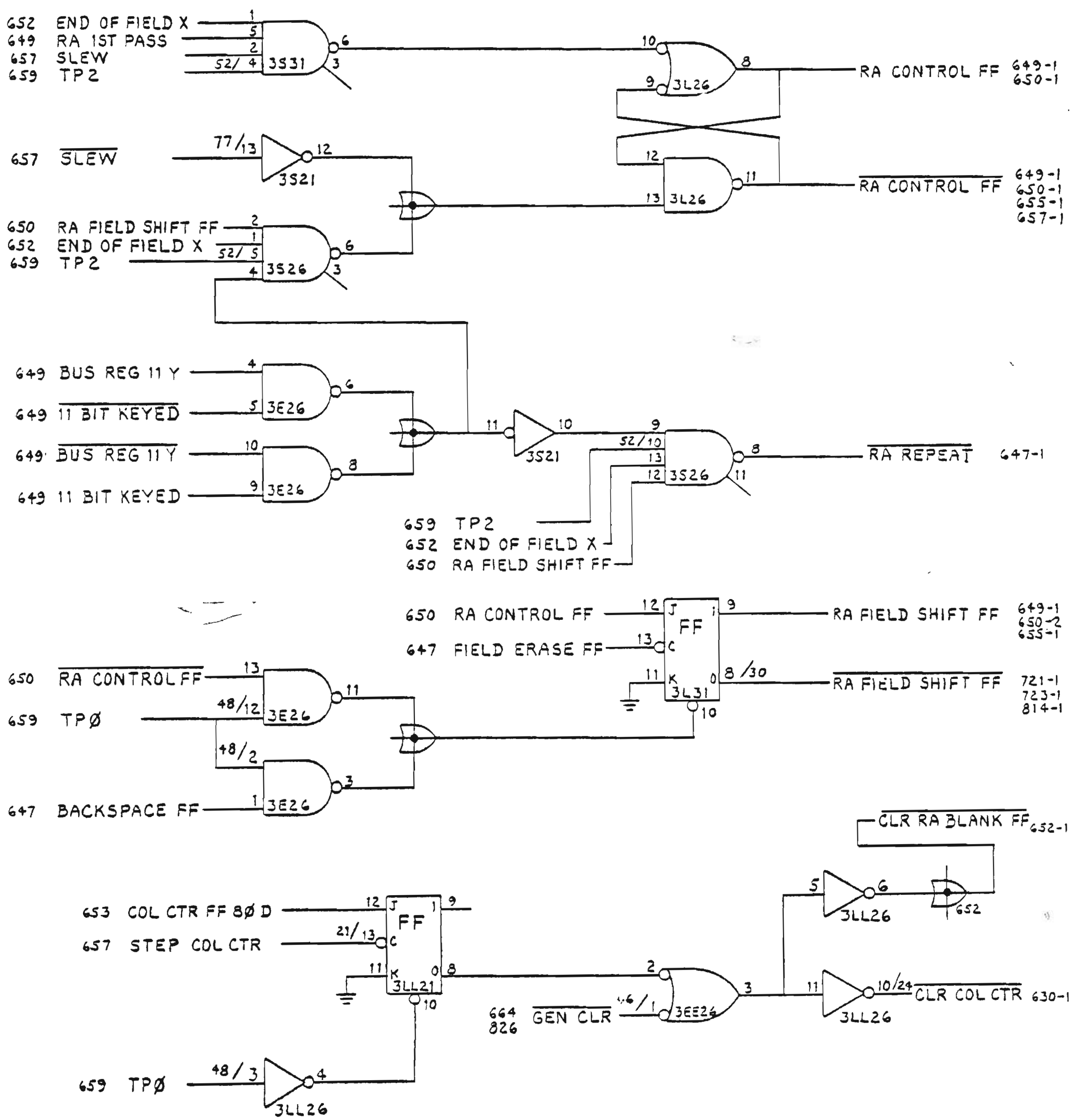
TITLE BACKSPACE COUNTER

DWG NO. C 7648 DASH NO. -00 SH 1 OF 1



C 7649
654 RA-KEY
3E42
RA MINUS 649-1
654 RA+KEY
654 RA-KEY
680 600 PUNCH MODE
654 VER CORR FF
3L42
3L26
START RA 647-1
FF
RA 1ST PASS 649-1 650-1
659 TPØ
650 RA CONTROL FF
3L31
RA 1ST PASS 723-1
664 826 GEN CLR
659 TPØ
643 LOWER
3LLC9
664 826 GEN CLR
BUS REG 11Y 650-1
BUS REG 11 719
3E37
652 KB SPT 3
11 BIT KEYED 650-1
11 BIT KEYED 649-1 650-1
3E31
3LL12
653 END RA FLD
3LL09
BUS REG 11Y 650-1
653 CLEAR RA CHECK
725 INPUT MEM 11
INPUT 11Z 651-1
3LL48
INPUT 11Z 651-1
649 RA 1ST PASS
649 11 BIT KEYED
3S37
650 RA FIELD SHIFT FF
650 RA CONTROL FF
649 RA MINUS
3L37
LOAD 11 BIT 719-1
652 VER RA FIELD FF
636 KB SPT FF
3S31
652 END OF FIELDX
651 CHANGE
643 LOWER
3LL37
3LL31
626 727 SIGN BIT 11
3EE17
DR80-B-54
PUNCH RIGHT ADJUST, PART 1
7649

C 7650



DR80 - B - 55

A	5098-03	5097-03	1633-30	2-21-73
-	5098-01	5097-01	1420-2C	11/10/72
REV	COMP.ASM	ARTWORK	ECN	APPVD-DATE

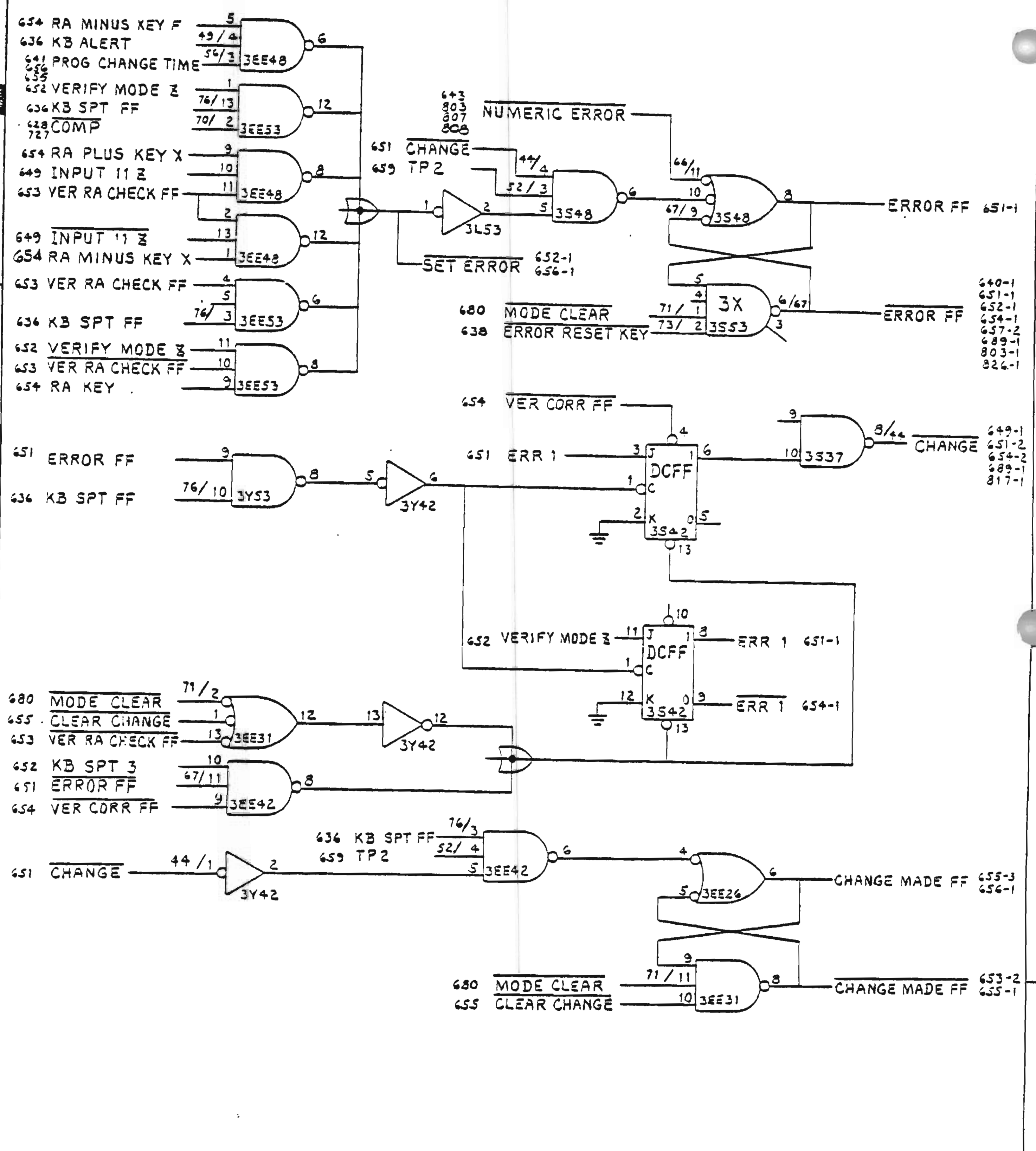
REVISIONS

DRN J. ALESI 11/29/72 ENG 11/17/72
CHK 6/2/72 MFG

TITLE PUNCH RIGHT ADJUST, PART 2

DWG NO. C 7650 DASH NO. -00 SH 1 OF 1

C 7651



REV	COMPASM	ARTWORK	ECN	APPVD-DATE
D	5098-07	5097-07	2681-2063	
C	5098-07	5097-07	1895	5-29-73
B	5098-03	5097-03	1683	
A	5098-03	5097-01	1623	
—	5098-01	5097-01	1420-2C	

REVISIONS

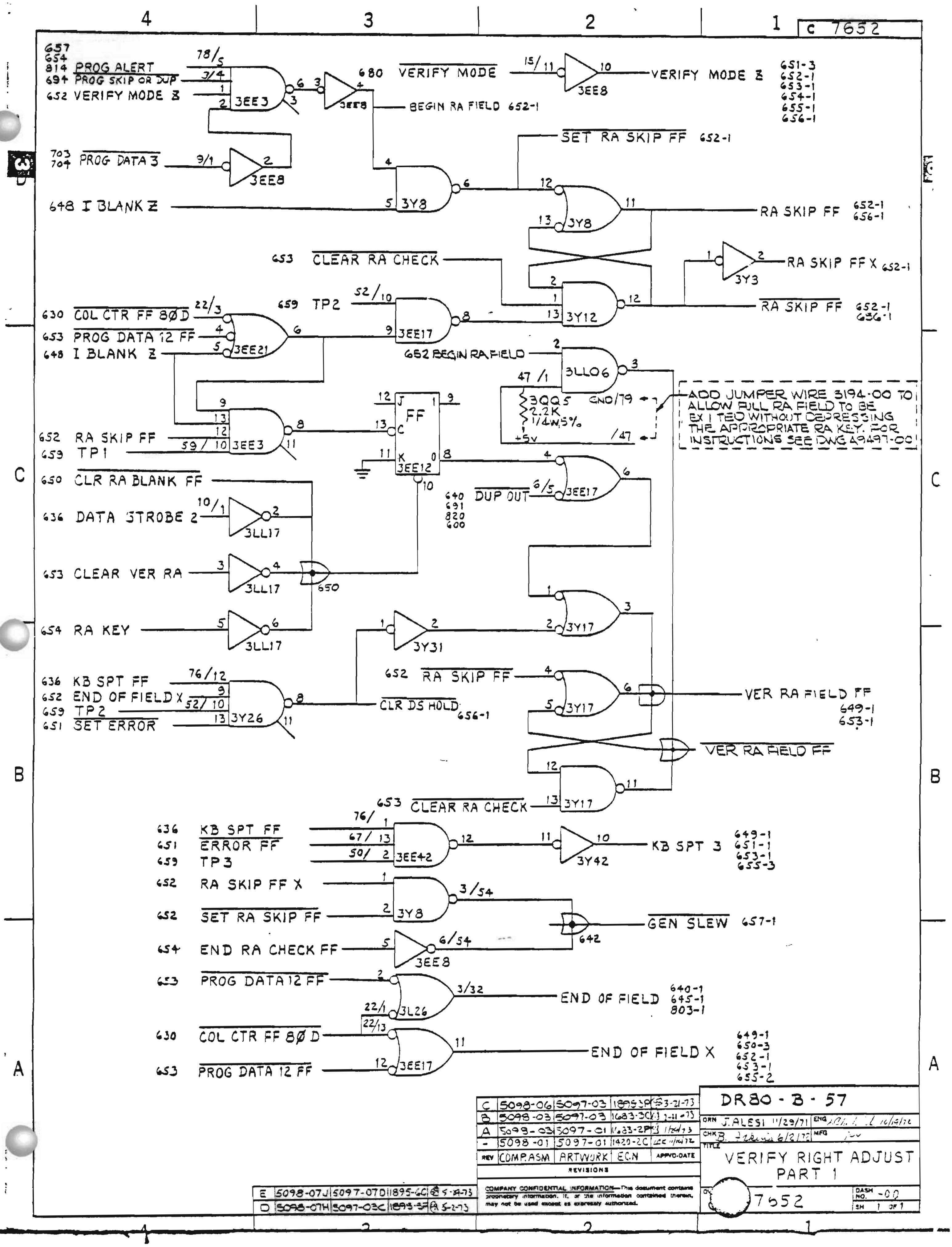
DR80-B-56

DRN I. ALESI 2/1/71 ENG
CHK 2/72 MFG

TITLE

ERROR CONTROL

DWG NO. C 7651 DASH NO. -00 SH 1 OF 1


C 7652
PROG ALERT
PROG SKIP OR DUP
VERIFY MODE Z
VERIFY MODE
BEGIN RA FIELD 652-1
PROG DATA 3
I BLANK Z
SET RA SKIP FF 652-1
RA SKIP FF
CLEAR RA CHECK
RA SKIP FF X
TP2
COL CTR FF 80D
PROG DATA 12 FF
652 BEGIN RA FIELD
ADD JUMPER WIRE 3194-00 TO ALLOW FULL RA FIELD TO BE EXITED WITHOUT DEPRESSING THE APPROPRIATE RA KEY. FOR INSTRUCTIONS SEE DWG A9497-00
3QQ5 2.2K 1/4W,5%
GND/79
+5v
RA SKIP FF
TP1
FF
CLR RA BLANK FF
DATA STROBE 2
DUP OUT
CLEAR VER RA
RA KEY
KB SPT FF
END OF FIELD X
TP2
SET ERROR
CLR DS HOLD
VER RA FIELD FF
KB SPT FF
ERROR FF
TP3
KB SPT 3
RA SKIP FF X
SET RA SKIP FF
GEN SLEW
END RA CHECK FF
PROG DATA 12 FF
END OF FIELD
COL CTR FF 80D
PROG DATA 12 FF
END OF FIELD X
DR80 - B - 57
J. ALESI 11/29/71
VERIFY RIGHT ADJUST
PART 1
7652
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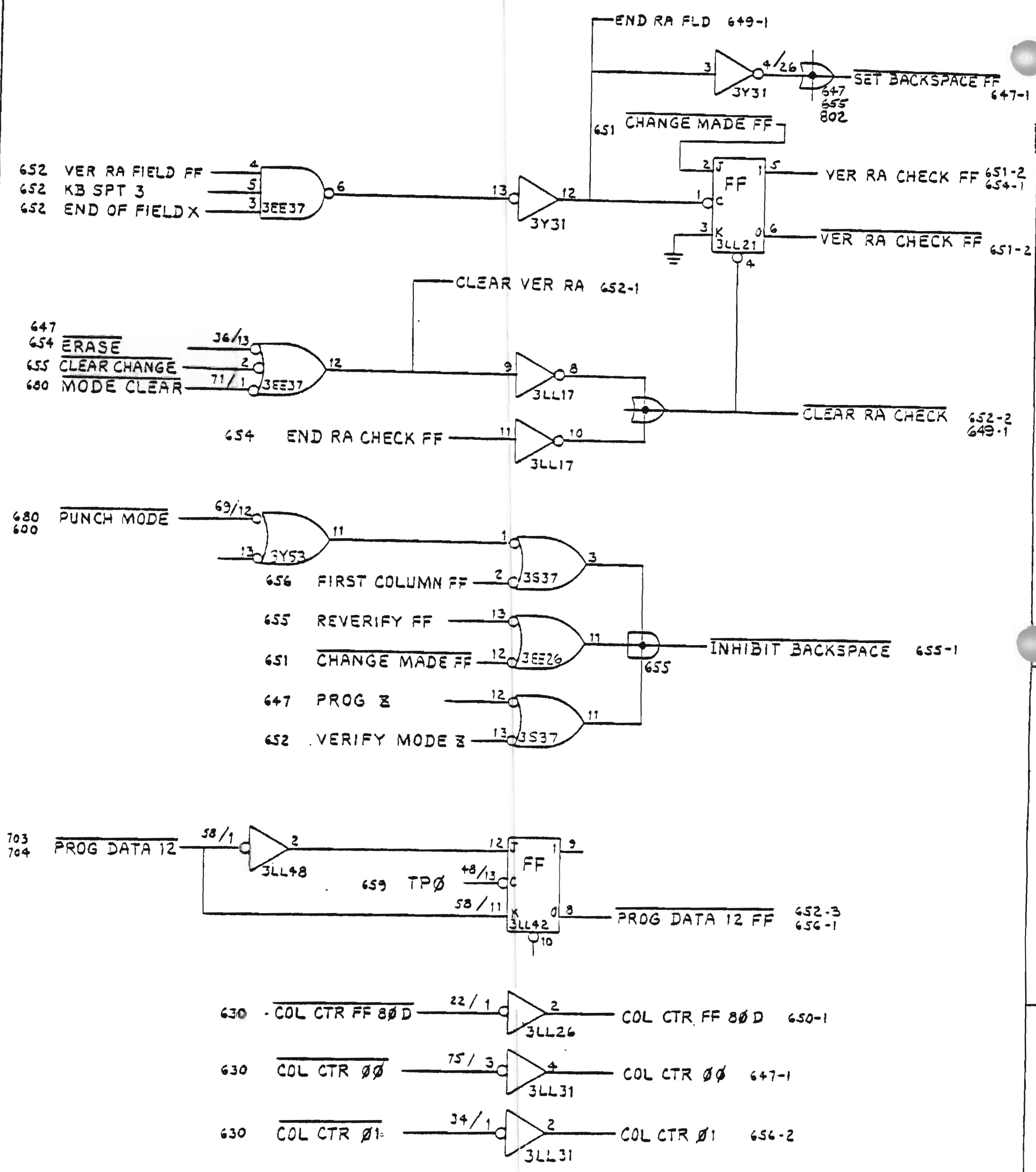
REVISIONS
REV COMP.ASM ARTWORK ECN APPVD-DATE

C 7653



DR80-B-58

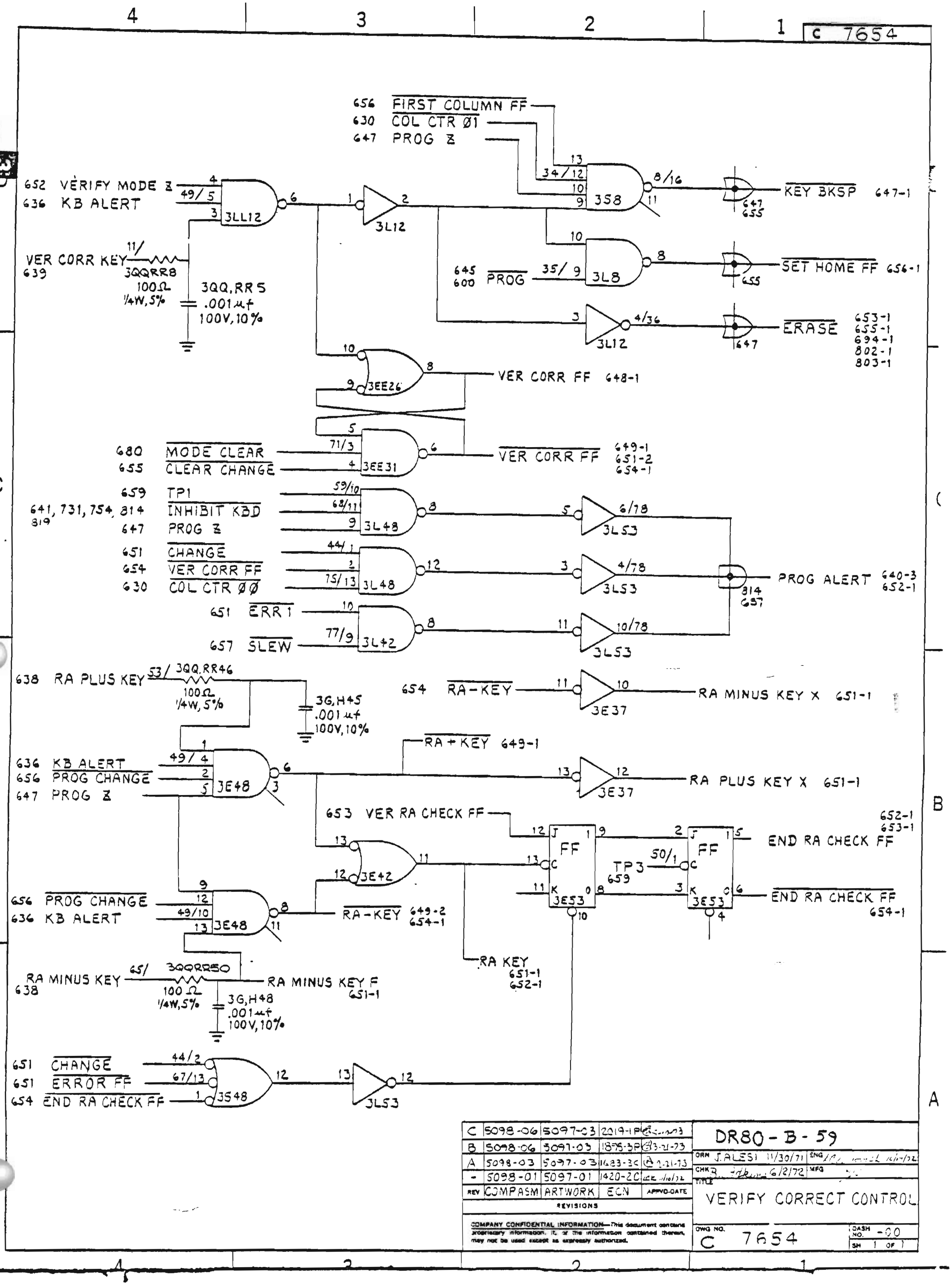
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	5098-07	5097-07	1985-C	5-29-73
A	5098-03	5097-03	1623-2C	1-21-73
—	5098-01	5097-01	1420-2C	4/14/72

REVISIONS

DRN J ALESI 11/29/71 ENG 11/14/72
CHK 6/2/72 MFG

TITLE VERIFY RIGHT ADJUST, PART 2

DWG NO. C 7653 DASH NO. -00 SH 1 OF 1


C 7654
652 VERIFY MODE Z
636 KB ALERT
3LL12
3L12
VER CORR KEY 639
3QQRR8 100Ω 1/4W, 5%
3QQ,RR5 .001uf 100V, 10%
656 FIRST COLUMN FF
630 COL CTR Ø1
647 PROG Z
358
KEY BKSP 647-1
645 600 PROG
3L8
SET HOME FF 656-1
3L12
ERASE 653-1 655-1 694-1 802-1 803-1
3EE26
VER CORR FF 648-1
680 MODE CLEAR
655 CLEAR CHANGE
3EE31
VER CORR FF 649-1 651-2 654-1
659 TP1
641, 731, 754, 814, 819 INHIBIT KBD
647 PROG Z
3L48
3L53
651 CHANGE
654 VER CORR FF
630 COL CTR ØØ
3L48
651 ERR1
657 SLEW
3L42
PROG ALERT 640-3 652-1
638 RA PLUS KEY
3QQ.RR46 100Ω 1/4W, 5%
3G,H45 .001uf 100V, 10%
654 RA-KEY
3E37
RA MINUS KEY X 651-1
RA+KEY 649-1
636 KB ALERT
656 PROG CHANGE
647 PROG Z
3E48
RA PLUS KEY X 651-1
653 VER RA CHECK FF
3E42
FF 3E53
TP3 659
FF 3E53
END RA CHECK FF 652-1 653-1
END RA CHECK FF 654-1
656 PROG CHANGE
636 KB ALERT
3E48
RA-KEY 649-2 654-1
RA KEY 651-1 652-1
RA MINUS KEY 638
3QQRR50 100Ω 1/4W, 5%
RA MINUS KEY F 651-1
3G,H48 .001uf 100V, 10%
651 CHANGE
651 ERROR FF
654 END RA CHECK FF
3S48
3L53
C 5098-06 5097-03 2019-1P
B 5098-06 5097-03 1875-3P 3-21-73
A 5098-03 5097-03 1483-3C 2-21-73
- 5098-01 5097-01 1420-2C
REV COMPASM ARTWORK ECN APPVD-DATE
REVISIONS
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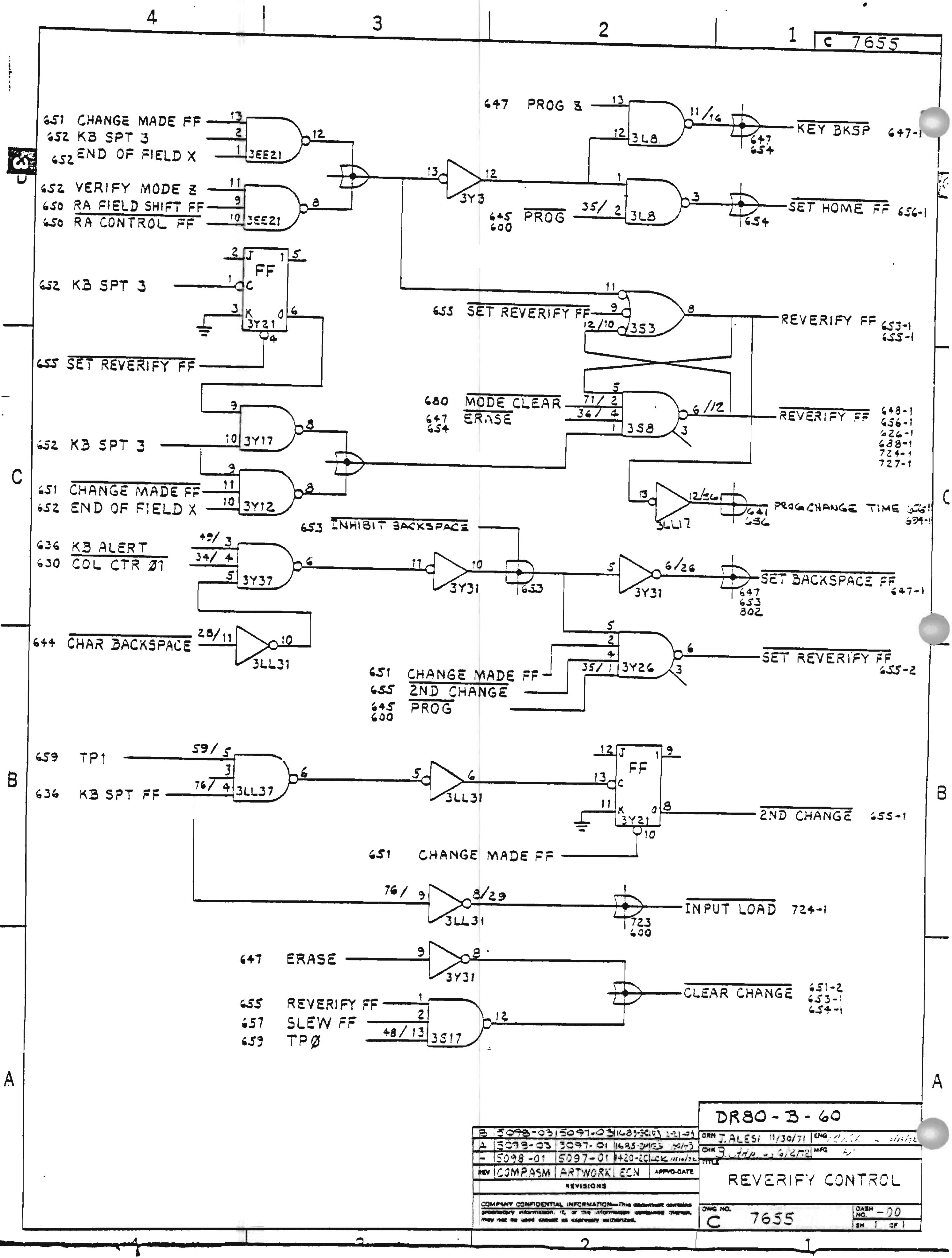
DR80-B-59
DRN J. ALESI 11/30/71
CHK 6/2/72
TITLE VERIFY CORRECT CONTROL
DWG NO. C 7654
DASH NO. -00
SH 1 OF 1



C 7655
651 CHANGE MADE FF
652 KB SPT 3
652 END OF FIELD X
3EE21
652 VERIFY MODE Z
650 RA FIELD SHIFT FF
650 RA CONTROL FF
3EE21
647 PROG Z
3L8
KEY BKSP 647-1
3Y3
645 600 PROG
3L8
SET HOME FF 656-1
652 KB SPT 3
FF
3Y21
655 SET REVERIFY FF
3S3
REVERIFY FF 653-1 655-1
655 SET REVERIFY FF
680 MODE CLEAR
647 654 ERASE
3S8
REVERIFY FF 648-1 656-1 626-1 688-1 724-1 727-1
3Y17
652 KB SPT 3
651 CHANGE MADE FF
652 END OF FIELD X
3Y12
3LL17
PROG CHANGE TIME
653 INHIBIT BACKSPACE
636 KB ALERT
630 COL CTR Ø1
3Y37
3Y31
3Y31
SET BACKSPACE FF 647-1
644 CHAR BACKSPACE
3LL31
651 CHANGE MADE FF
655 2ND CHANGE
645 600 PROG
3Y26
SET REVERIFY FF 655-2
659 TP1
636 KB SPT FF
3LL37
3LL31
FF
3Y21
2ND CHANGE 655-1
651 CHANGE MADE FF
3LL31
INPUT LOAD 724-1
647 ERASE
3Y31
655 REVERIFY FF
657 SLEW FF
659 TPØ
3S17
CLEAR CHANGE 651-2 653-1 654-1
DR80-B-60
B 5098-03 5097-03
A 5098-03 5097-01
- 5098-01 5097-01
REV COMP.ASM ARTWORK ECN APPVD-DATE
REVISIONS
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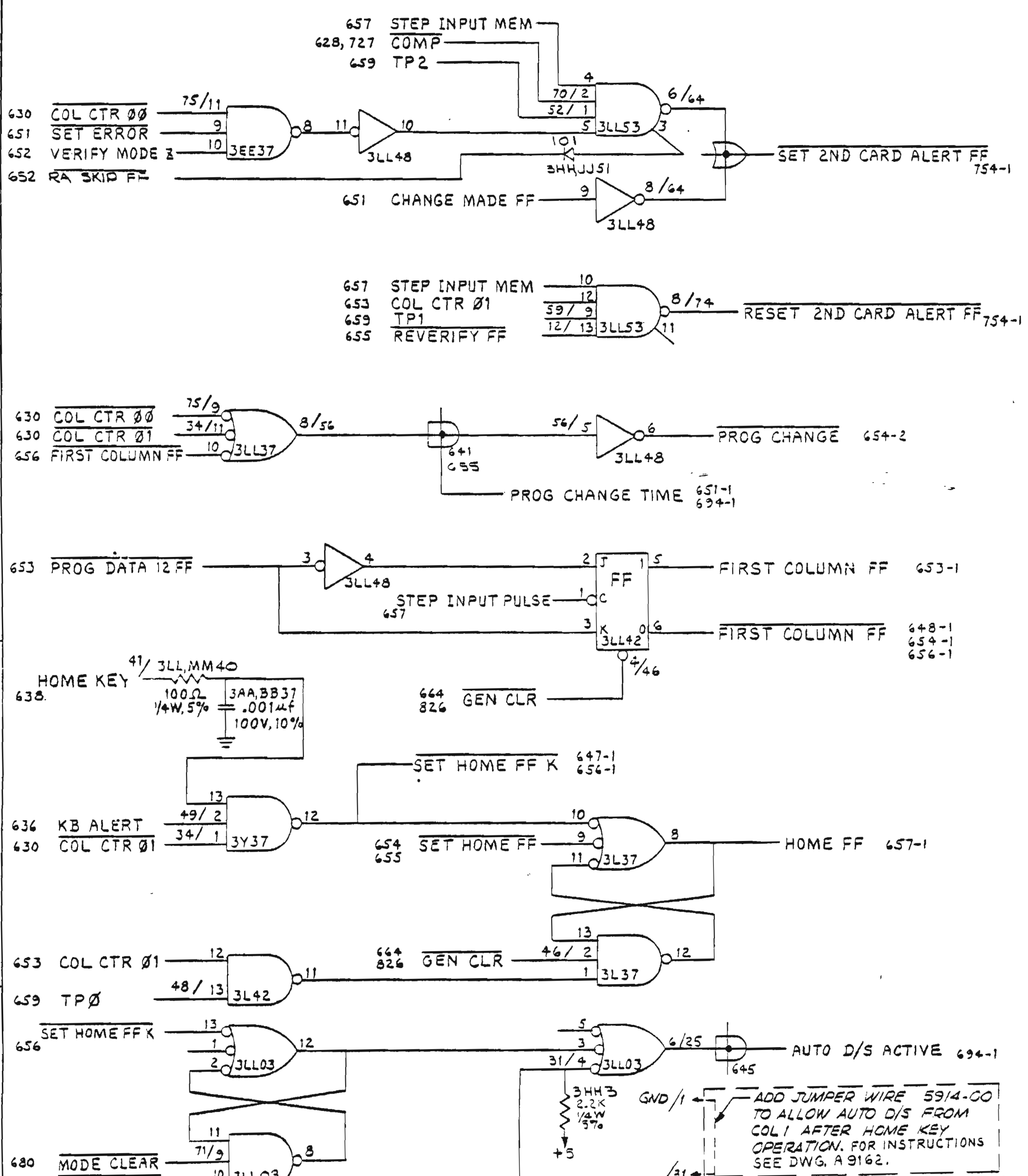
DRN J. ALESI 11/30/71
TITLE REVERIFY CONTROL
DWG NO. C 7655
DASH NO. -00
SH 1 OF 1



G	5098-07	5097-07	1895-6C	@ 5-4-73
F	5098-06	5097-03	1988-1C	RVK 4-73
E	5098-06	5097-03	1895-3P	
D	5098-05	5097-03	1895-2P	LCK 3/15/73

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	5098-03	5097-03	1683-3	2-21-73
B	5098-03	5097-01	1683	1-22-73
A	5098-02	5097-01	1683	1-21-73
-	5098-01	5097-01	1420-2C	LCK 1/14/72

REVISIONS

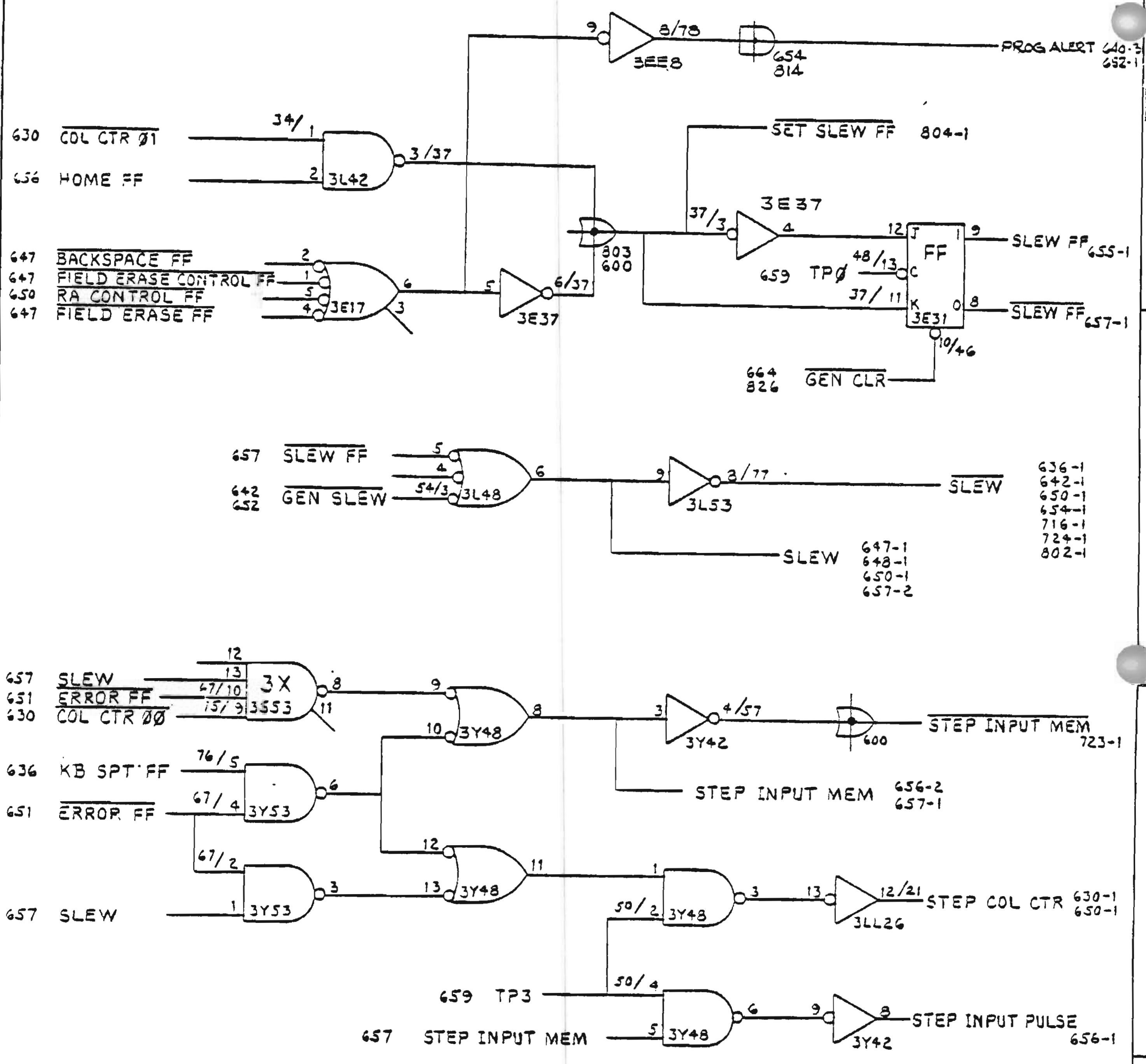
DR80-B-61

DRN J. ALESI 11/30/71
CHK B. 2/2/72

TITLE FIRST COLUMN, HOME FF

DWG NO. C 7656 CASH NO -00 SH 1 OF 1

C 7657



REV	COMP.ASM	ARTWORK	ECN	APPVD-DATE
A	5098-06	5097-03	1895-3P	
-	5098-01	5097-01	1420-2C	

REVISIONS

DR80 · B · 62

DRN J. ALESI 11/30/71

TITLE SLEW, STEP CONTROL

DWG NO. C 7657

DASH NO. -00

SH 1 OF 1

REV	DESCRIPTION	APPD DATE
J	RELEASE ECN 1895-6C	[illegible]
K	REVISED PER ECN 2617-1C	[illegible]

*UNLESS OTHERWISE SPECIFIED ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 5098-07 ON ARTWORK 5097-07

*VARIATION REFERENCE

VAR	ASSEMBLY	ARTWORK
I	5098-01,-02,-03,-04,-05,-06,-07	5097-01
II	5098-03,-04,-05,-06,-07	5097-03
III	5098-07	5097-07

VAR. I: 3HH31, 3HH26, 3BB17, 3BB21, 3H17
VAR II, III: 3GG,HH23, 3GG,HH31, 3AA,BB17, 3G,H17, 3AA,BB21
3G,H12, 3G,H37, 3G,H53, 3H8, 3H,J42, 3N,P32, 3P21, 3P48, 3P,Q3, 3AA,BB3, 3BB53, 3BB,CC42, 3HH7, 3BB12, 3HH36, 3HH48

+5V .02µf 25V +80% -30%

VAR. I: 3V,W16, 3V,W26
VAR. I, II: 3PP,QQ14
VAR II, III: 3V16, 3V26
3B47, 3B,C13, 3N,P3, 3N,P37, 3U,V48, 3AA,BB8, 3QQ54
VAR. III: 3QQ5

+5V 4.7µf 10V, 20%

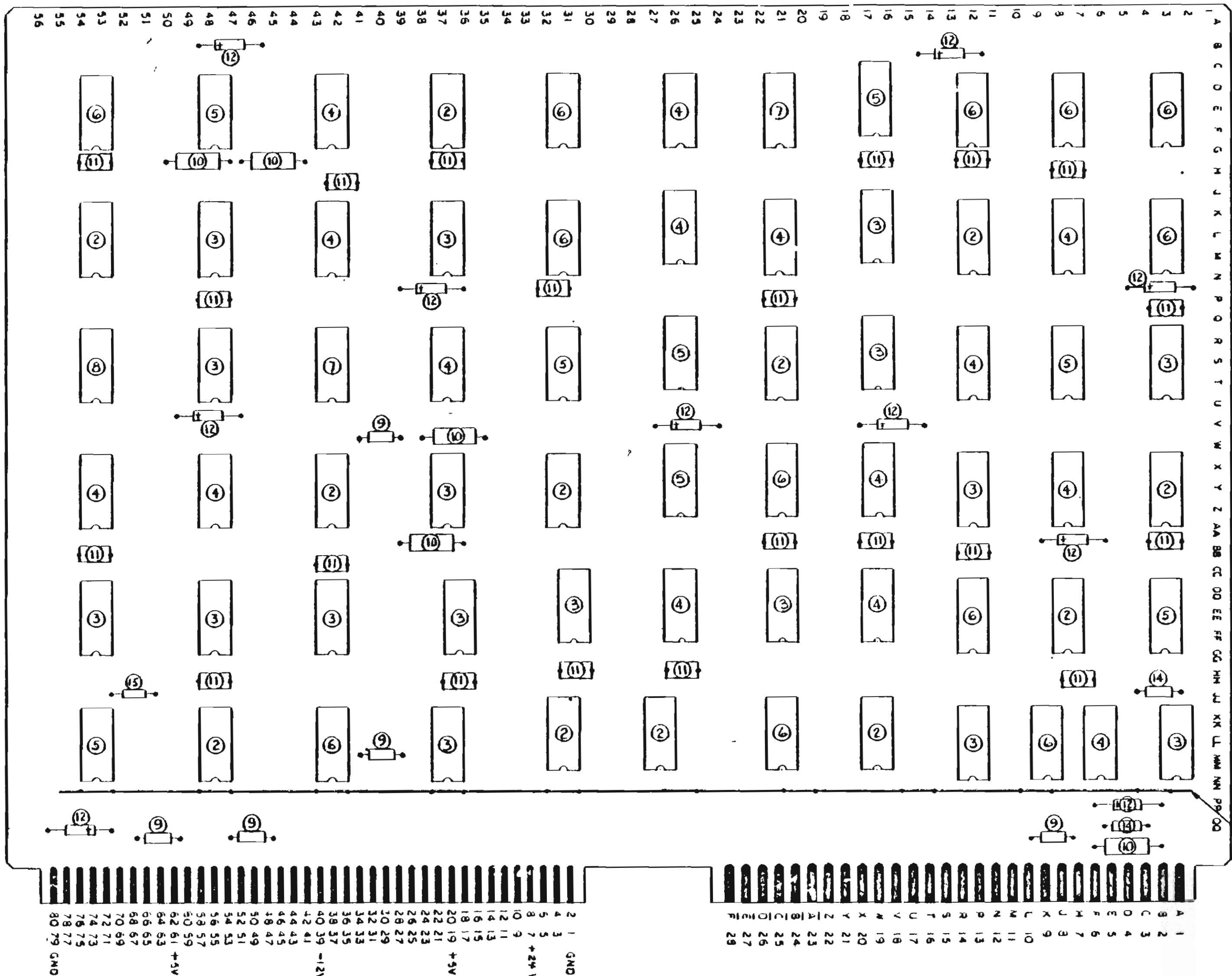
LOGIC DIAGRAM CHART

DWG. NO.	REV
7647	A
7648	A
7649	E
7650	A
7651	B
7652	E
7653	A
7654	A
7655	B
7656	G
7657	A

SPARE GATES

TYPE	LOC	OUTPUT PIN NO.
DUAL CLK FF	E21	8,9
4/2 INPUTS	LL6	6,8,11

FOR LIST OF PARTS SEE DETACHED PARTS LIST



DR80-B-63

COMPONENT ASSEMBLY KEYBOARD FUNCTIONS

D 5098

8605

DR80- B- 64

LOGIC/SCHEMATIC CONTENTS

PCB LOC 4

TYPE NUMBER/NAME BASIC 8001, 8010 INTERFACE

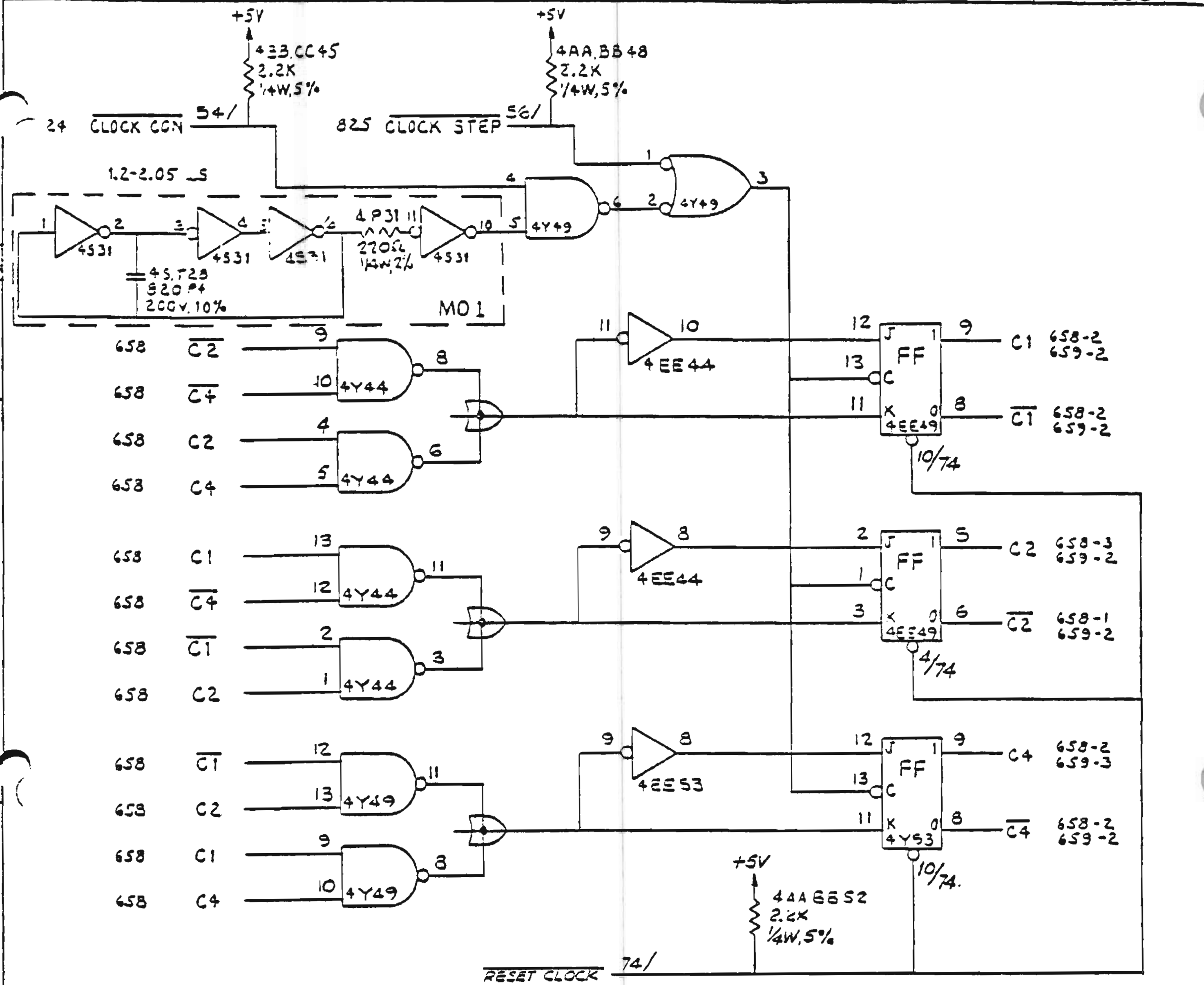
DWG NO	TITLE	PCB/ASM DRAWING DASH NO			
8877	PWB	10	10	10	10
8878	MOTION CONTROL PT-1	10	12	12	13

LOGIC SCHEMATIC	LOGIC DRAWING TITLE SCHEMATIC SHEET NO	LOGIC/SCHEMATIC DRAWING REVISION			
7658	CLOCK OSCILLATOR & COUNTER	E	E	E	E
7659	CLOCK OUTPUT GATING	D	D	E	E
7660	PUNCH SPROCKET	E	E	E	E
7661	CARD TRACKING FF'S	B	B	B	B
7662	STACKER JAM DETECTION	C	C	C	C
7663	MOTOR JAM CHECK	D	D	D	D
7664	GENERAL CLEAR	D	D	D	D
7665	POSITION CONTROL FF'S	E	E	E	E
7666	POSITION PULSE GENERATION	E	E	E	F
7667	POSITION DIFFERENCE	G	G	G	H
7668	POSITION SENSOR	D	D	D	E
7669	POSITION MOTOR CONTROL	F	F	F	F
8878	Sheet 7	M	R	R	R
"	Sheet 8	K	K	K	K
	Sheet 12	/	/	/	S

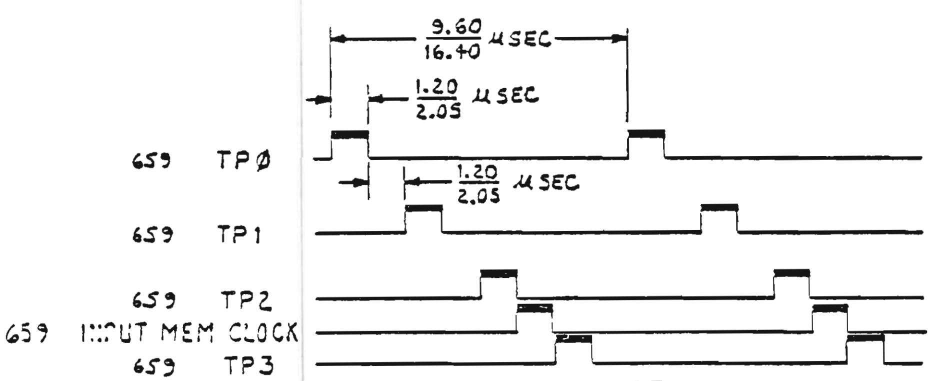
REVISION OF CONTENTS	N	R	X	
DATE OF ECN	26 JUL 74	8 MAY 75	(5108-06)	
APPROVAL				
ENGINEERING CHANGE NOTICE	3186-2C	4319-1C	4536-1P	

DR80-B-65

DWG NO 8878 REV — SHT — OF



	C1	C2	C4
TPØ	0	0	0
	1	0	0
TP1	1	1	0
	0	1	0
TP2	0	1	1
	1	1	1
TP3	1	0	1
	0	0	1



CLOCK OUTPUT TIMING

DR80-B-66

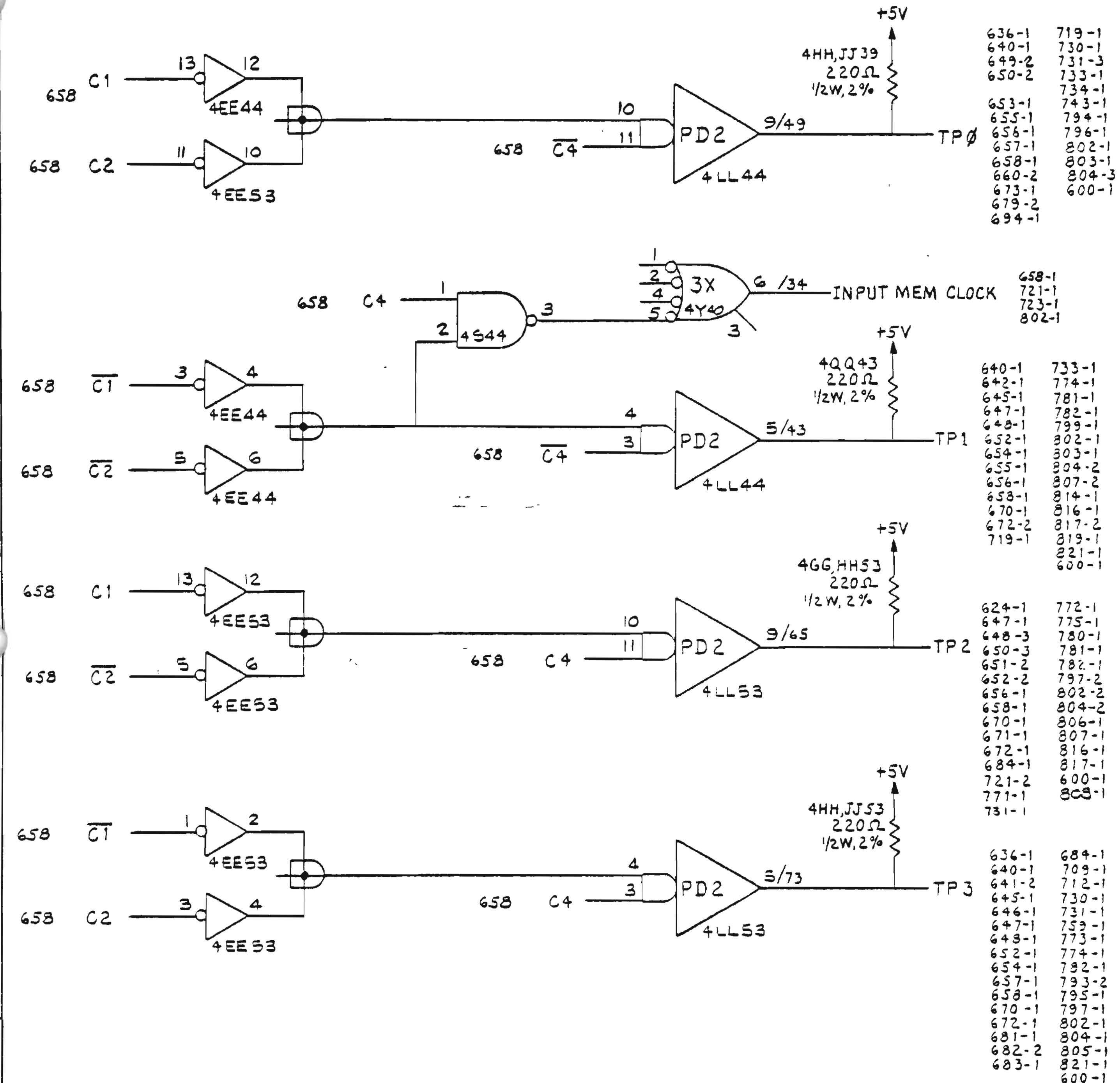
CLOCK OSCILLATOR & COUNTER

REVISIONS

COMPANY CONFIDENTIAL INFORMATION

DWG NO. C 7658

C 7659



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
E	5108-06	5107-05	4336-1P	
D	8878-06	8877-06	2681-2P	
C	8878-06	8877-06	1914-6C	
B	8878-00	8877-00	1900-2P	
A	8878-00	8877-00	1684-2C	
–	5100-01	5099-01	1421-2C	

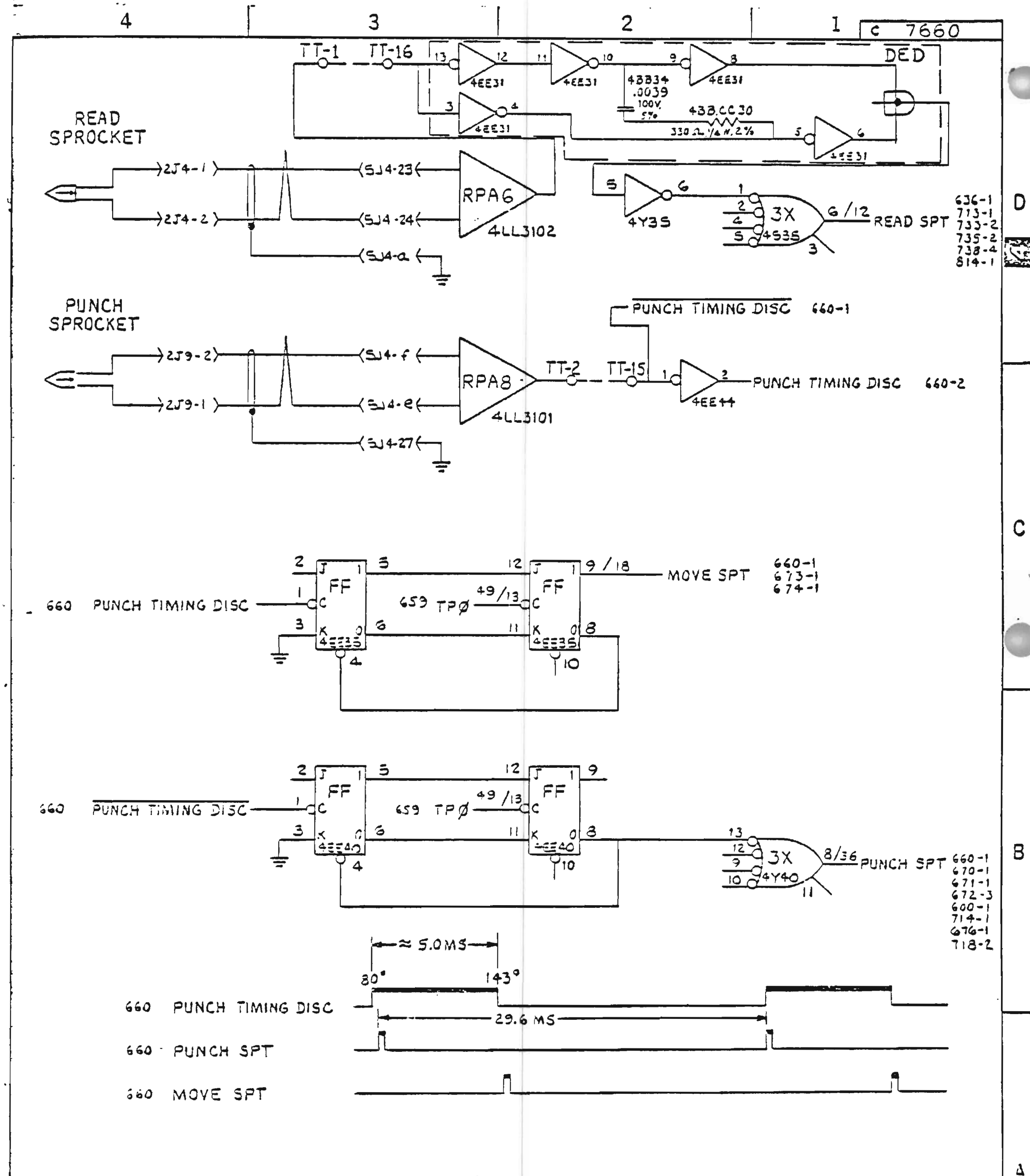
REVISIONS

DR80 - B - 67

DRN J. ALESI 4/18/72 | ENG | CHK | MFG

TITLE: CLOCK OUTPUT GATING

DWG NO. C 7659 | DASH NO. -00 | SH 1 OF 1



REV	COMP ASM	ARTWORK	CON	APPVD-DATE
C	8878-06	8877-06	1914-6C	
B	8878-00	8877-00	2060-1C	
Δ	8878-00	8877-00	1486-3C	
-	5100-01	5099-01	1421-2C	

REVISIONS

DR80-B-68

DWN F. ALESI 9-5-72

PUNCH SPROCKET

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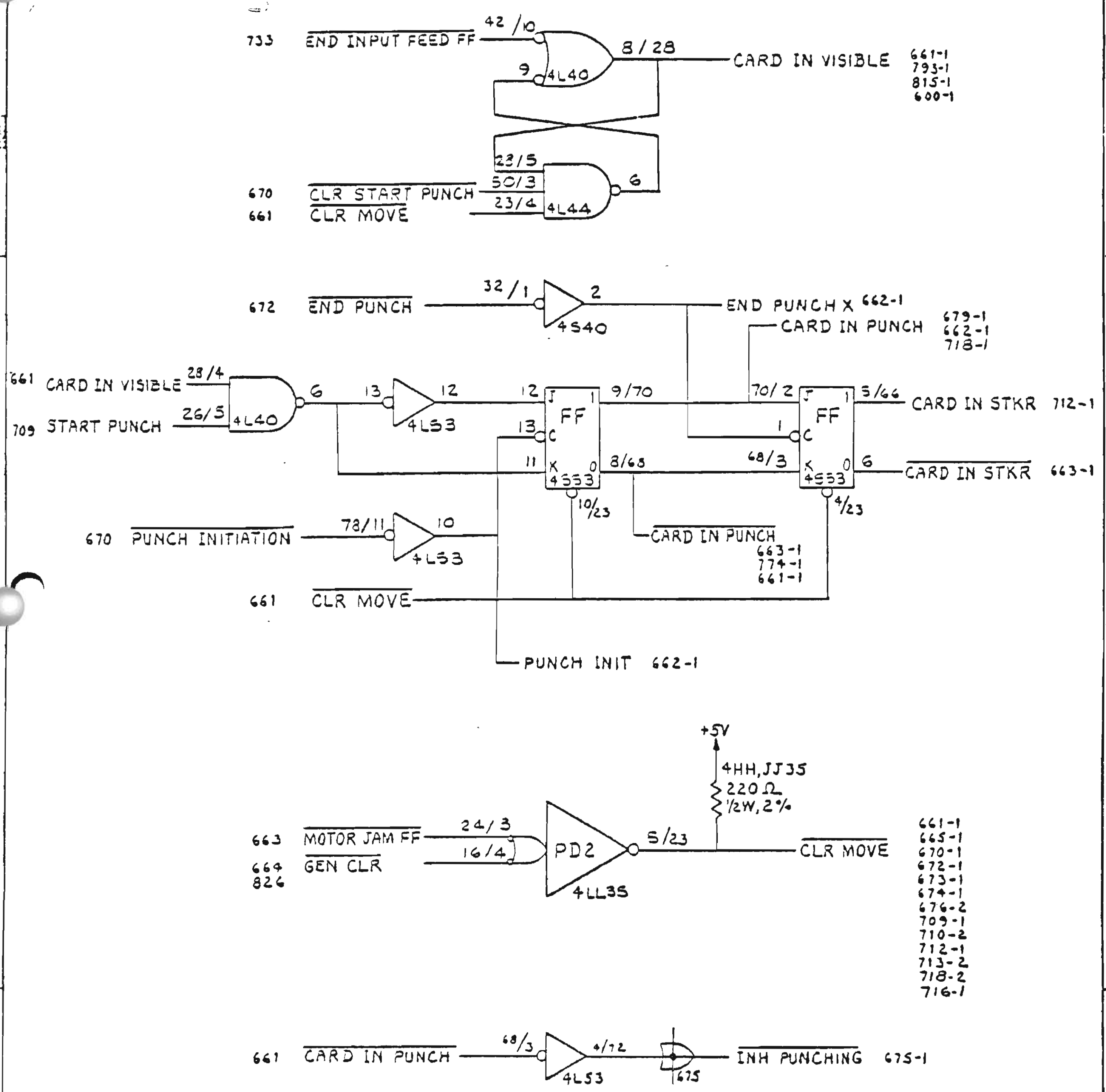
DWG NO. C 7660

DASH NO. -00



REV	COMP ASM	ARTWORK	ECN	APPRO-DATE
B	5373-69	5377-69	2342-55	
A	5379-00	5377-00	1824-3C	
-	5100-01	5099-01	1421-2C	

REVISIONS

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DR80-B-69

DRN F. ALESI 4/17/72

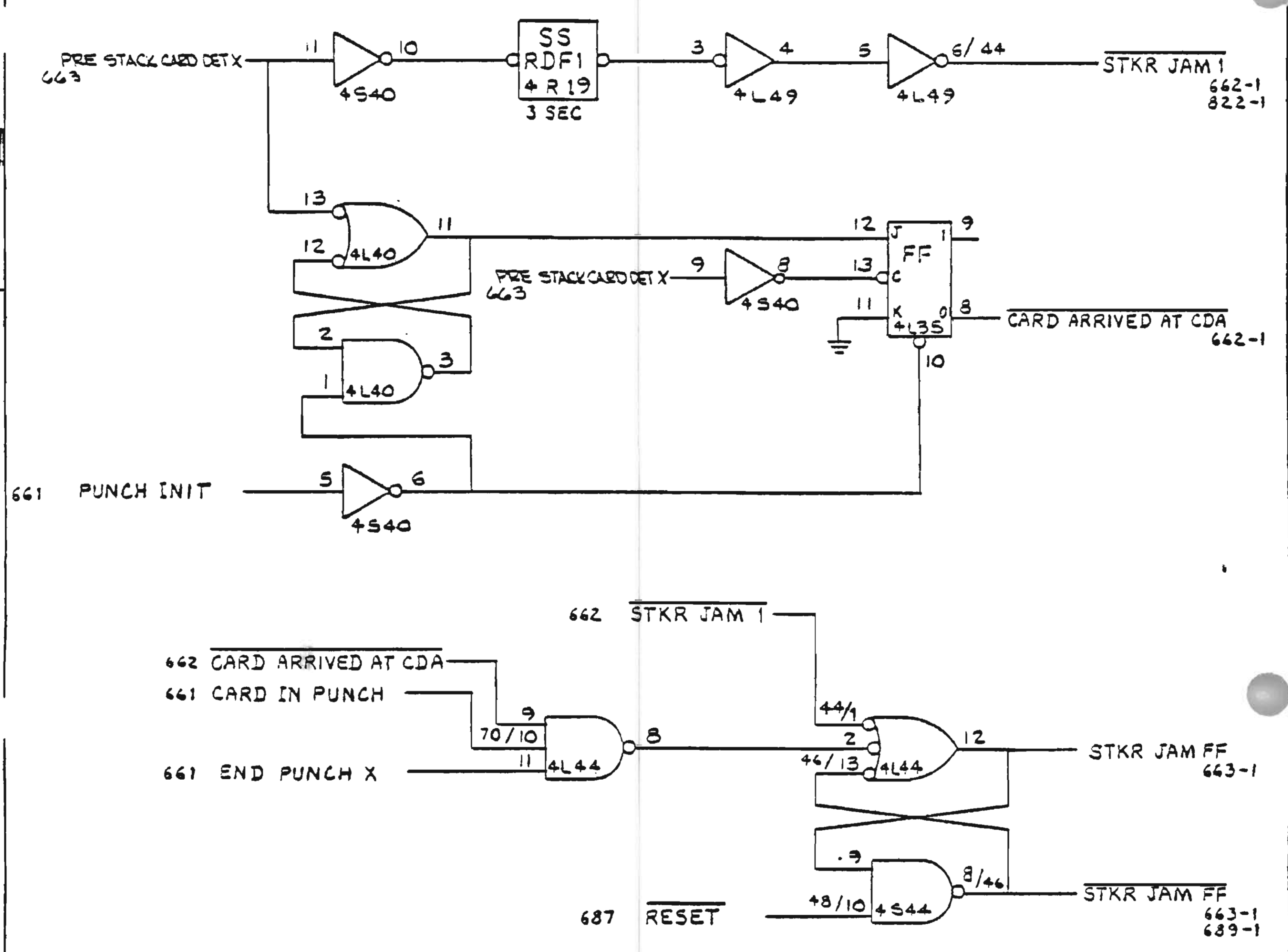
CHK 5-13-72

TITLE CARD TRACKING FLIP-FLOPS

DWG NO. C 7661

DASH NO. -00

SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	8878-06	8877-06	1914-6C	
B	8878-02	8877-00	1914-2C	
A	8878-00	8877-00	1684-3C	
-	5100-01	5099-01	1421-2C	

REVISIONS

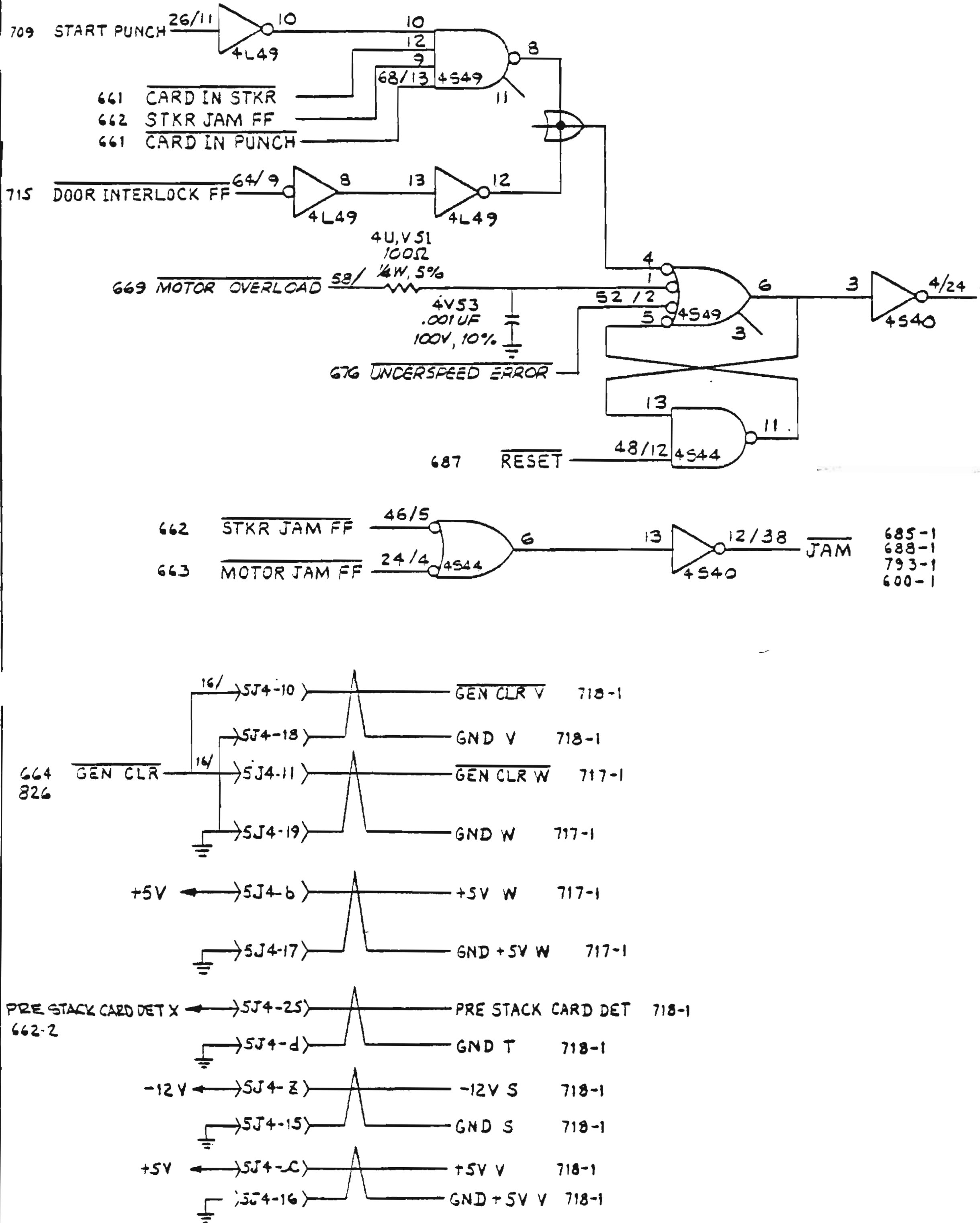
DR80-B-70

DRN T. ALESI 4/17/72

CHK 5/13/72

TITLE STACKER JAM DETECTION

DWG NO. C 7662 DASH NO -00 SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
D	8878-06	8877-06	1914-6	[illegible]
C	8878-04	8877-00	1914-4P	[illegible]
B	8878-02	8877-00	1914-2P	[illegible]
A	8878-00	8877-00	1684-3C	[illegible]
-	5100-01	5099-01	1421-2C	[illegible]

REVISIONS

DR80-B-71

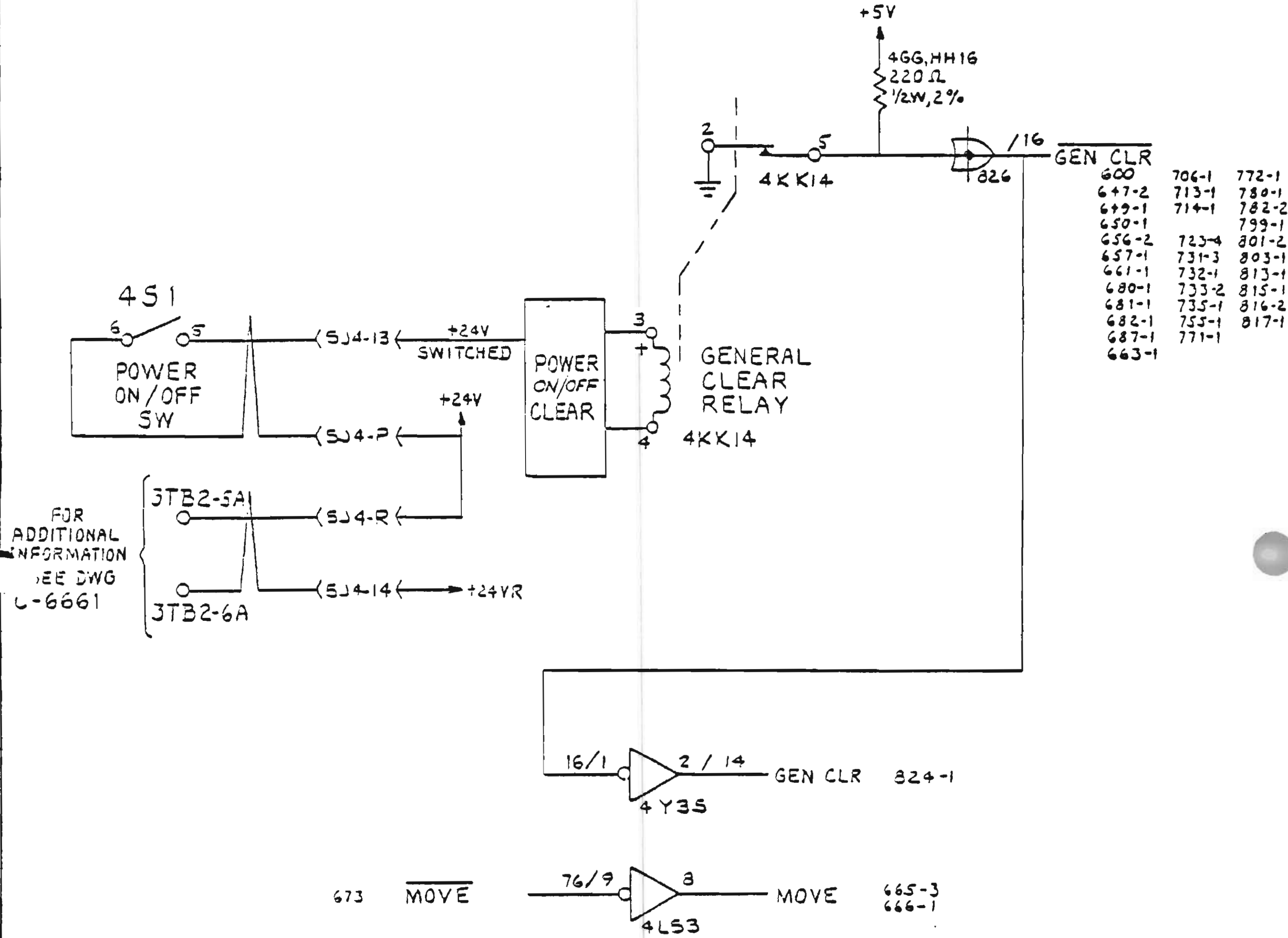
DRN J. ALESI 4/15/72 ENG [illegible]

CHK [illegible] 5/13/72 MFG

TITLE MOTOR JAM CHECK

DWG NO. C 7663 DASH NO. -00 SH 1 OF 1

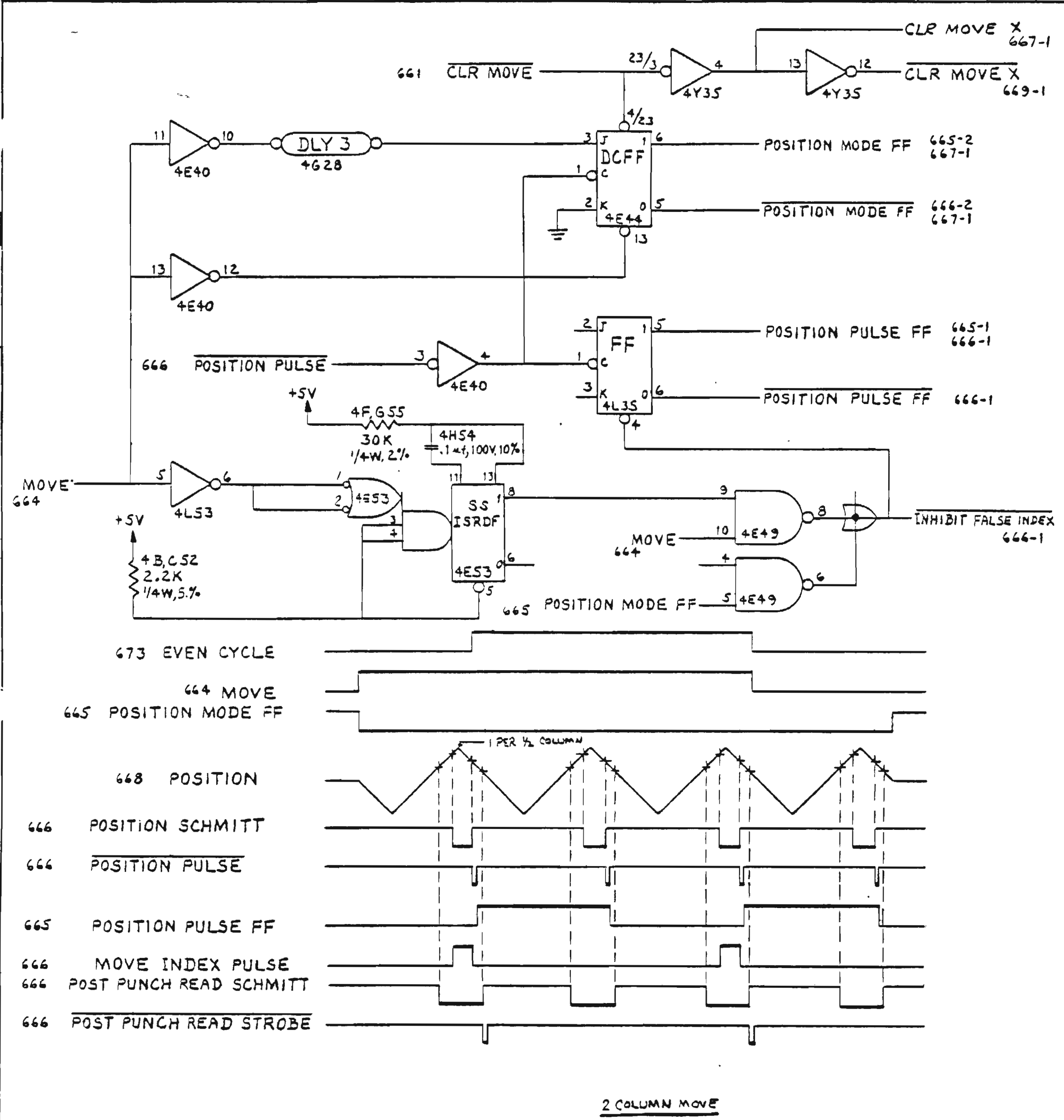
C 7664



DR80-B-72

GENERAL CLEAR

DWG NO. C 7664



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	8878-03	8877-00	[illegible]	[illegible]
B	8878-02	8877-00	1914-2P	[illegible]
A	3878-00	8877-00	1684-3C	[illegible]
-	5100-01	5099-01	1421-2C	[illegible]
E	8878-06	8877-06	1914-6C	[illegible]
D	8878-05	8877-00	1914-5P	[illegible]

REVISIONS

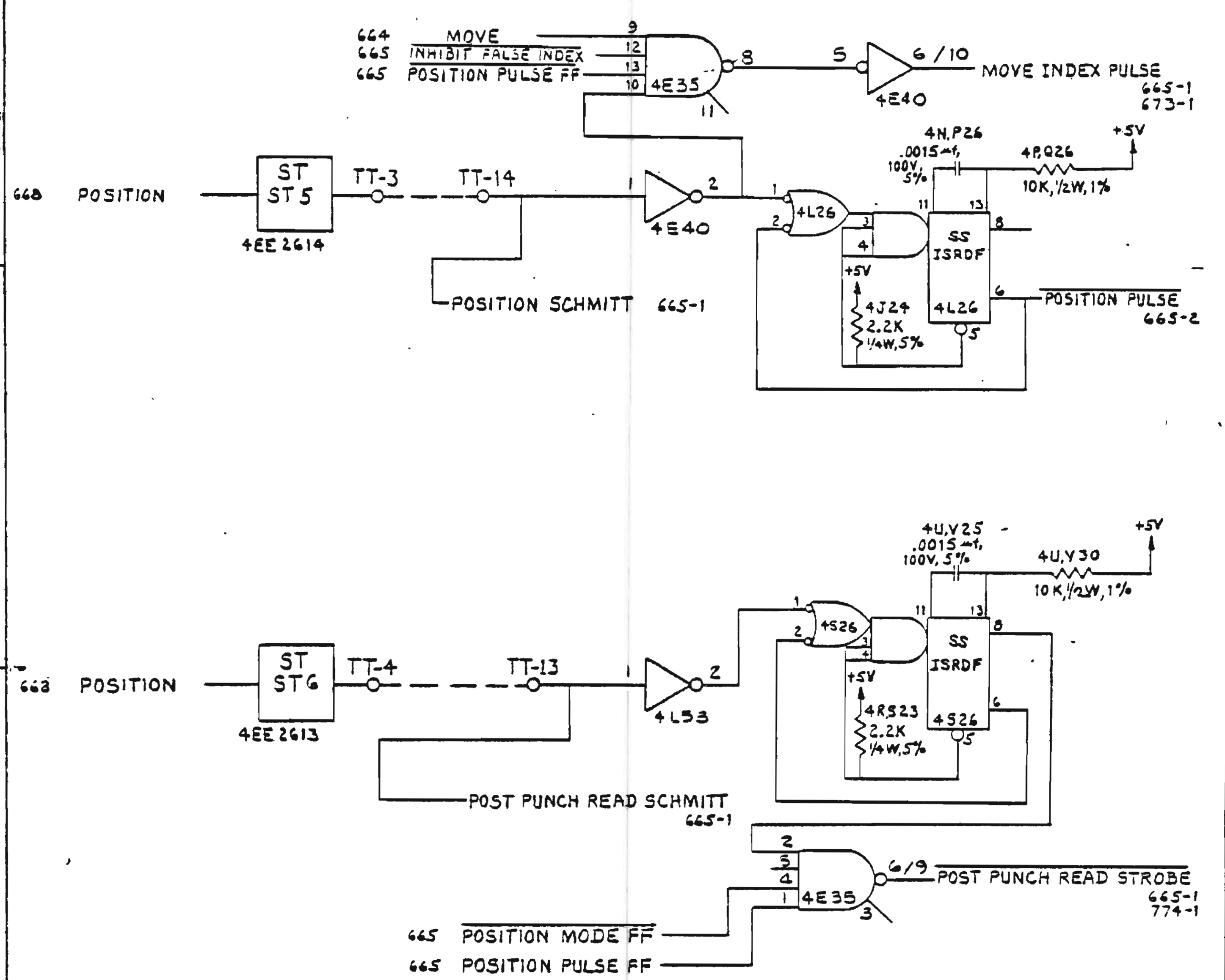
DR80-B-73

DRN J. ALESI 10/2/72 — ENG [illegible]
CHK C. SCAFIDI 5/3/.. — MFG [illegible]

TITLE POSITION CONTROL FLIP-FLOPS

DWG NO. C 7665 — DASH NO. -00 — SH 1 OF 1

C 7666



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
F	8878-13	8877-10	1455-1C	
E	8878-09	8877-09	2342-9C	
D	8878-06	8877-06	1914-6C	
C	8878-05	8877-00	1914-5P	3-23-73
B	8878-03	8877-00	1914-3P REV A	
A	8878-00	8877-00	684-2C	
-	5100-01	5099-01	1421-2C	

REVISIONS

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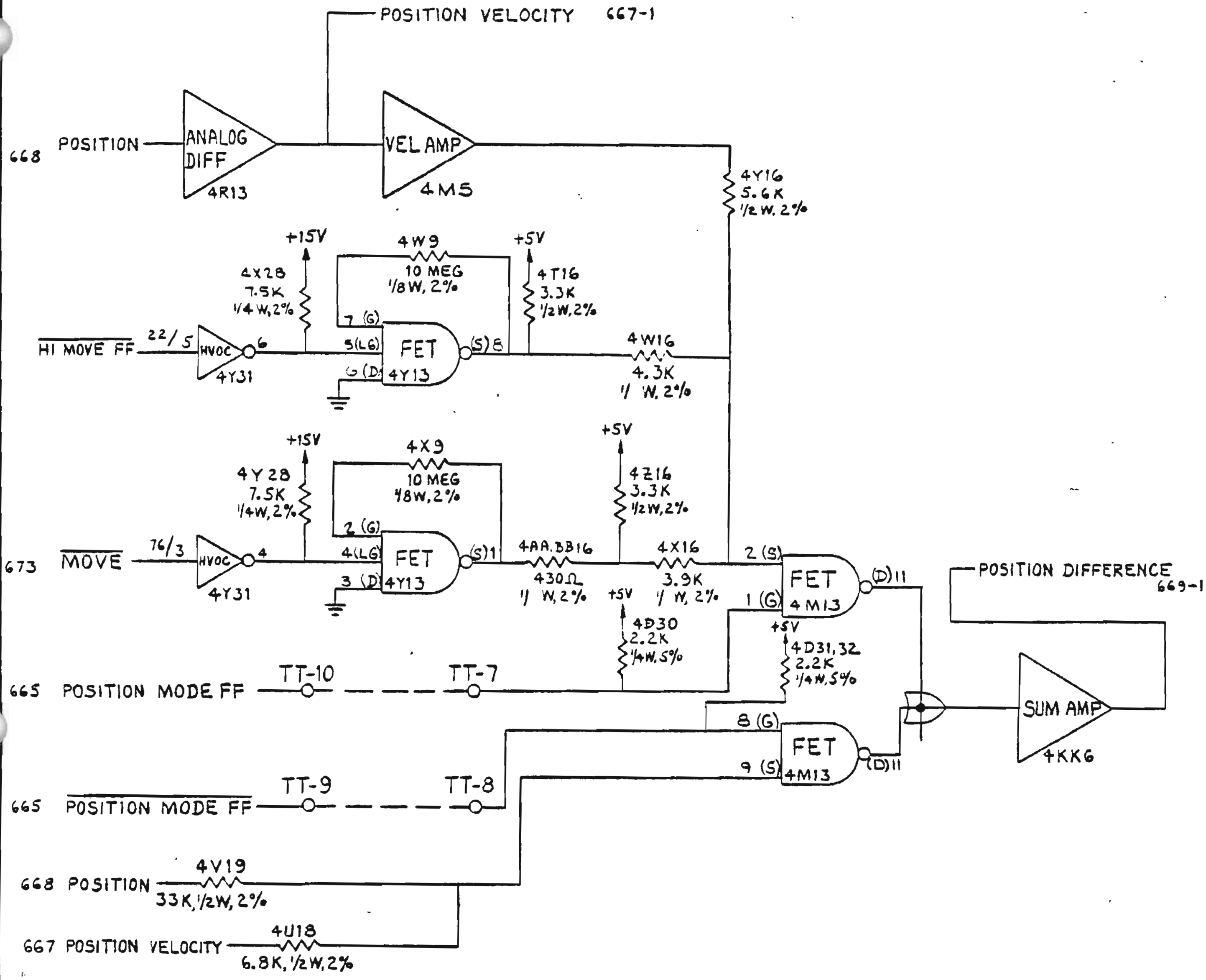
DR80-B-74

DRN J. ALESI 4/17/72

TITLE POSITION PULSE GENERATION

DWG NO. C 7666 DASH NO. -00 SH 1 OF 1

C 7667



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
H	8878-13	8877-10	4455-1C	[illegible]
G	8878-09	8877-09	2342-5C	
F	8878-06	8877-06	1914-6C	[illegible]
E	8878-05	8877-00	1914-5P	[illegible]
D	8876-03	8877-00	1914-3P	[illegible]
C	8878-02	8877-00	1914-2P	[illegible] 3-28-73
B	8878-01	8877-00	1914-1P	[illegible]
A	8878-00	8877-00	1684-2C	[illegible]
–	5100-01	5099-01	1421-2C	[illegible]

REVISIONS

DR 80-B-75

DRN J. ALESI 4/17/72	ENG [illegible]
CHK [illegible] 5-13-72	MFG [illegible]

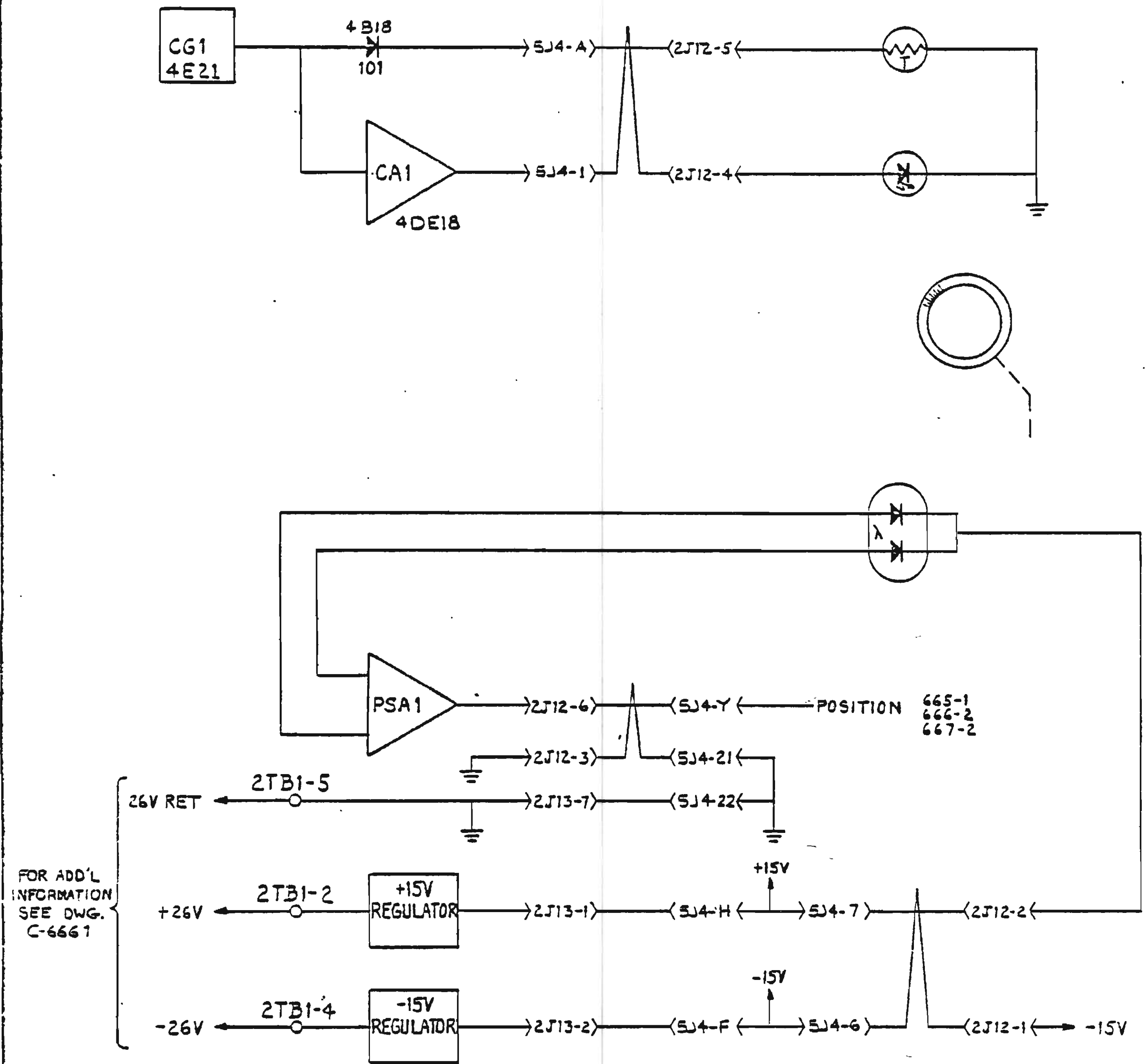
TITLE POSITION DIFFERENCE

DWG NO. C 7667

DASH NO. -00

SH 1 OF 1

C 7668



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
E	8879-13	8877-10	4455-1C	[illegible]
D	12878-06	8877-06	1914-6C	[illegible]
C	8878-05	8877-00	1914-5P	[illegible]
B	8878-03	8877-00	1914-3P	[illegible]
A	8879-00	8877-00	1684-2C	[illegible]
-	5100-01	5099-01	1421-2C	[illegible]

REVISIONS

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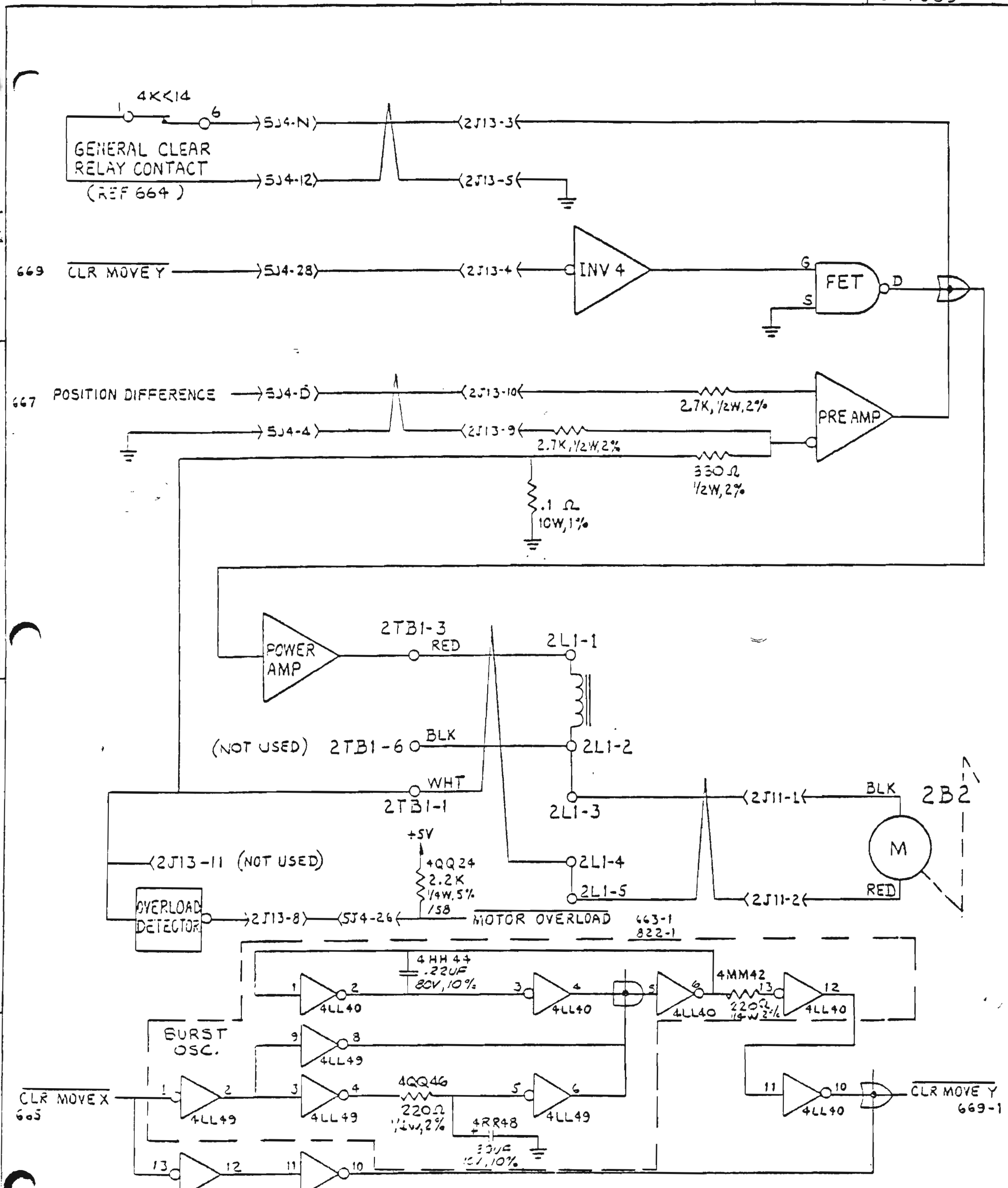
DR80-B-76

DRN J. ALESI 4/17/72 ENG [illegible]
CHK C. [illegible] 5-13-72 MFG [illegible]

TITLE POSITION SENSOR

DWG NO. C 7668 DASH NO. -00 SH 1 OF 1



REV	COMP REM	ARTWORK	ECN	APPRO DATE
D	5575-07	5577-03	2342-1	
C	5578-03	5577-03	1914-0C	
B	5575-04	5577-03	1914-20	
A				
—	5199-01	5099-01	1421-20	

REVISIONS

F	5878-03	5377-03	2342-3C	

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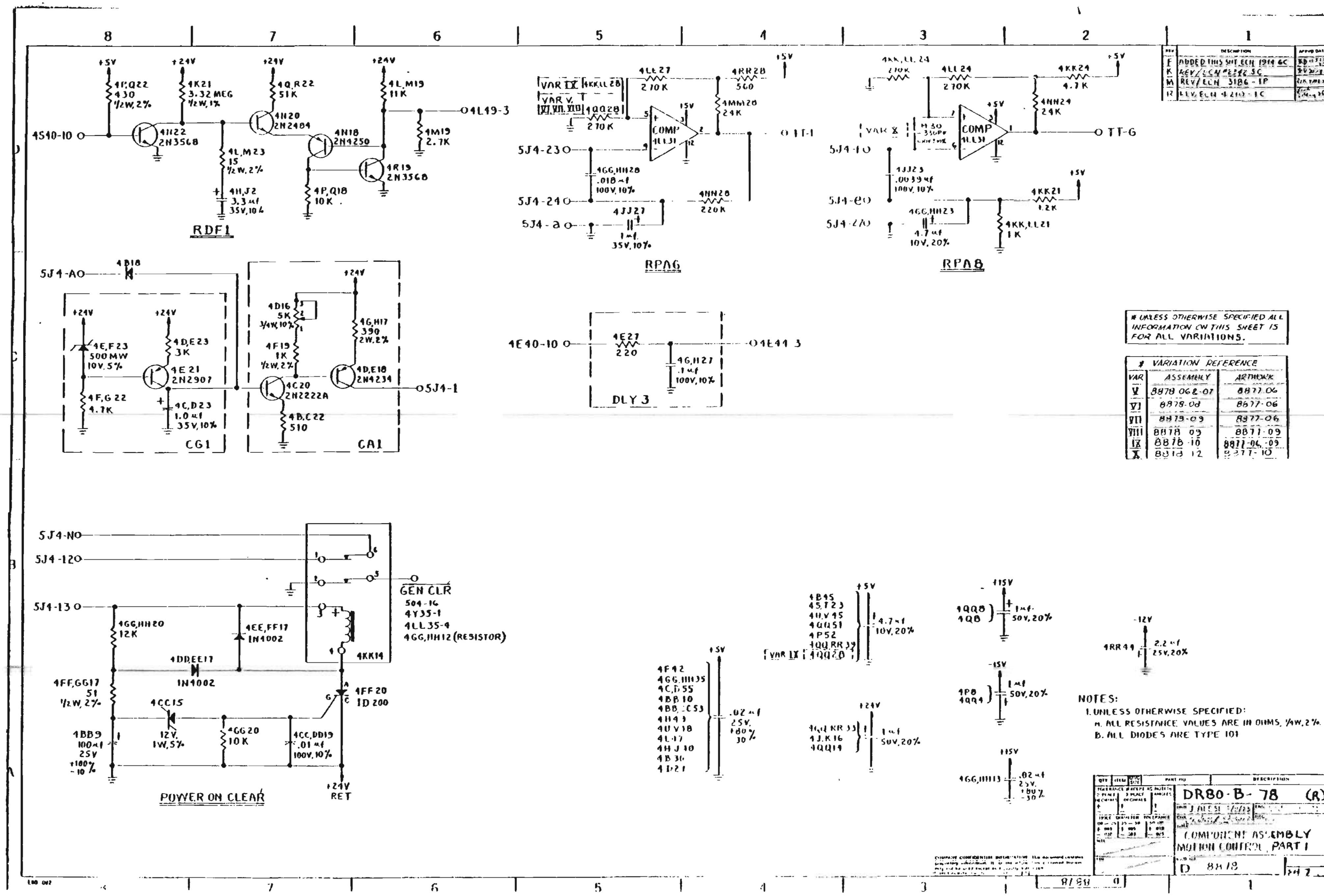
DR80 - B - 77

DRN J.A. ESI 4/17/72 ENG
CHK 5-1-72 MFG

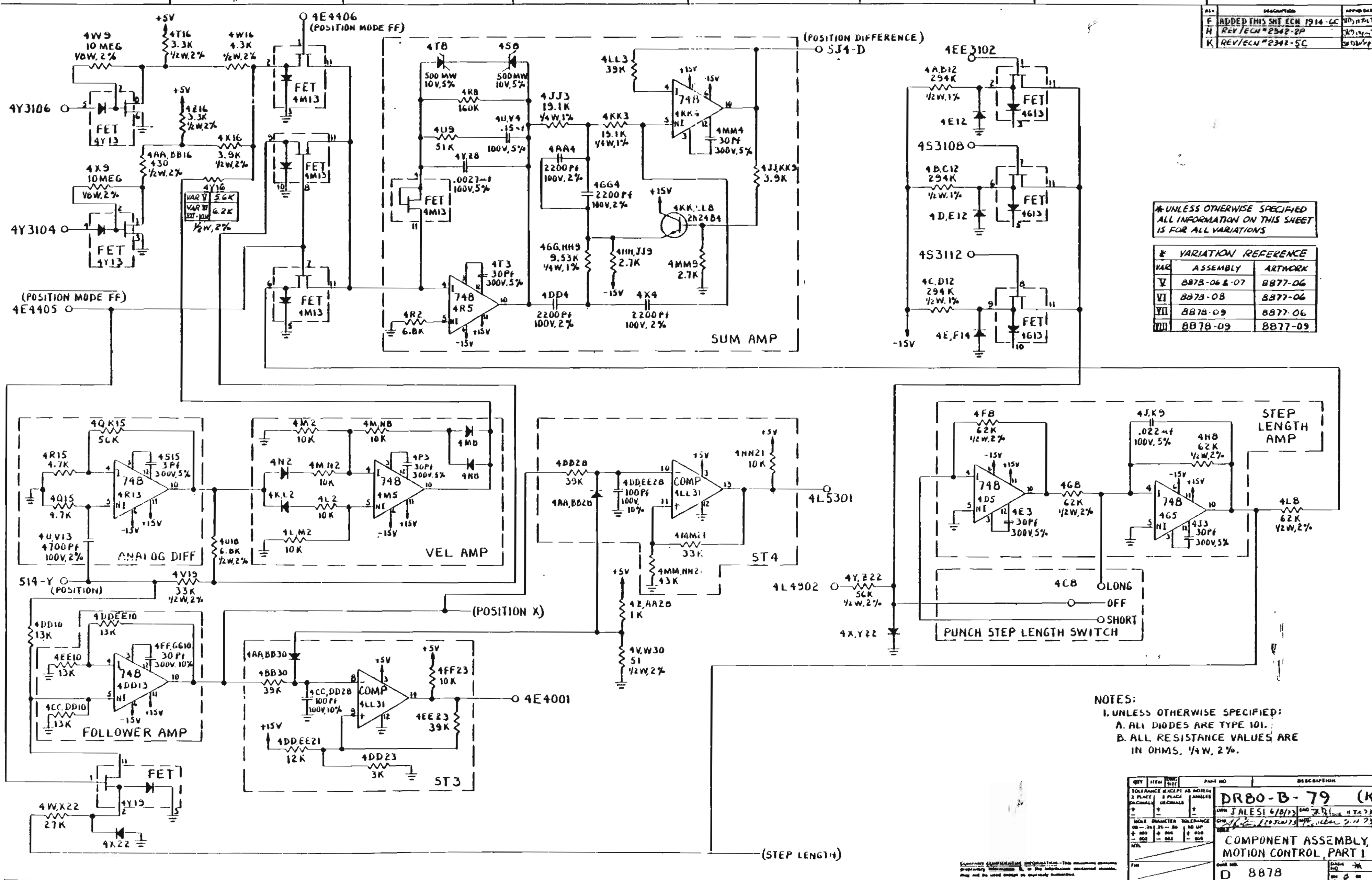
TITLE POSITION MOTOR CONTROL

DWG NO. C 7669 DASH NO. -00



RDF1
RPAG
RPA8
CG1
CA1
DLY 3
POWER ON CLEAR
GEN CLR
504-16
4Y35-1
4LL35-4
4GG,HH12 (RESISTOR)
4S40-10
4L49-3
5J4-AO
5J4-1
5J4-NO
5J4-120
5J4-130
4E40-10
4E41 3
5J4-230
5J4-240
TT-1
TT-G
4H22 2N3568
4N20 2N2484
4N18 2N4250
4R19 2N3568
4E21 2N2907
4C20 2N2222A
4D,E18 2N4234
COMP 4LL31
+24V RET
ADDED THIS SHT ECN 1914 6C
REV/ECN 2242 3C
REV/ECN 3186-1P
REV ECN 4210-1C
* UNLESS OTHERWISE SPECIFIED ALL INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS.
* VARIATION REFERENCE
VAR | ASSEMBLY | ARTWORK
V | 8878 06&-07 | 8877-06
VI | 8878-08 | 8877-06
VII | 8879-09 | 8877-06
VIII | 8878 09 | 8877-09
IX | 8878-10 | 8877-06,-09
X | 8878 12 | 8877-10
NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.
B. ALL DIODES ARE TYPE 101
DR80-B-78 (R)
COMPONENT ASSEMBLY
MOTION CONTROL, PART I
D 8878

REV	DESCRIPTION	APPROVED DATE
F	ADDED THIS SHT ECN 1914-CC	
H	REV/ECN #2342-2P	
K	REV/ECN #2342-5C	



* UNLESS OTHERWISE SPECIFIED ALL INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS

* VAR	VARIATION REFERENCE ASSEMBLY	ARTWORK
V	8878-06 & -07	8877-06
VI	8878-08	8877-06
VII	8878-09	8877-06
VIII	8878-09	8877-09

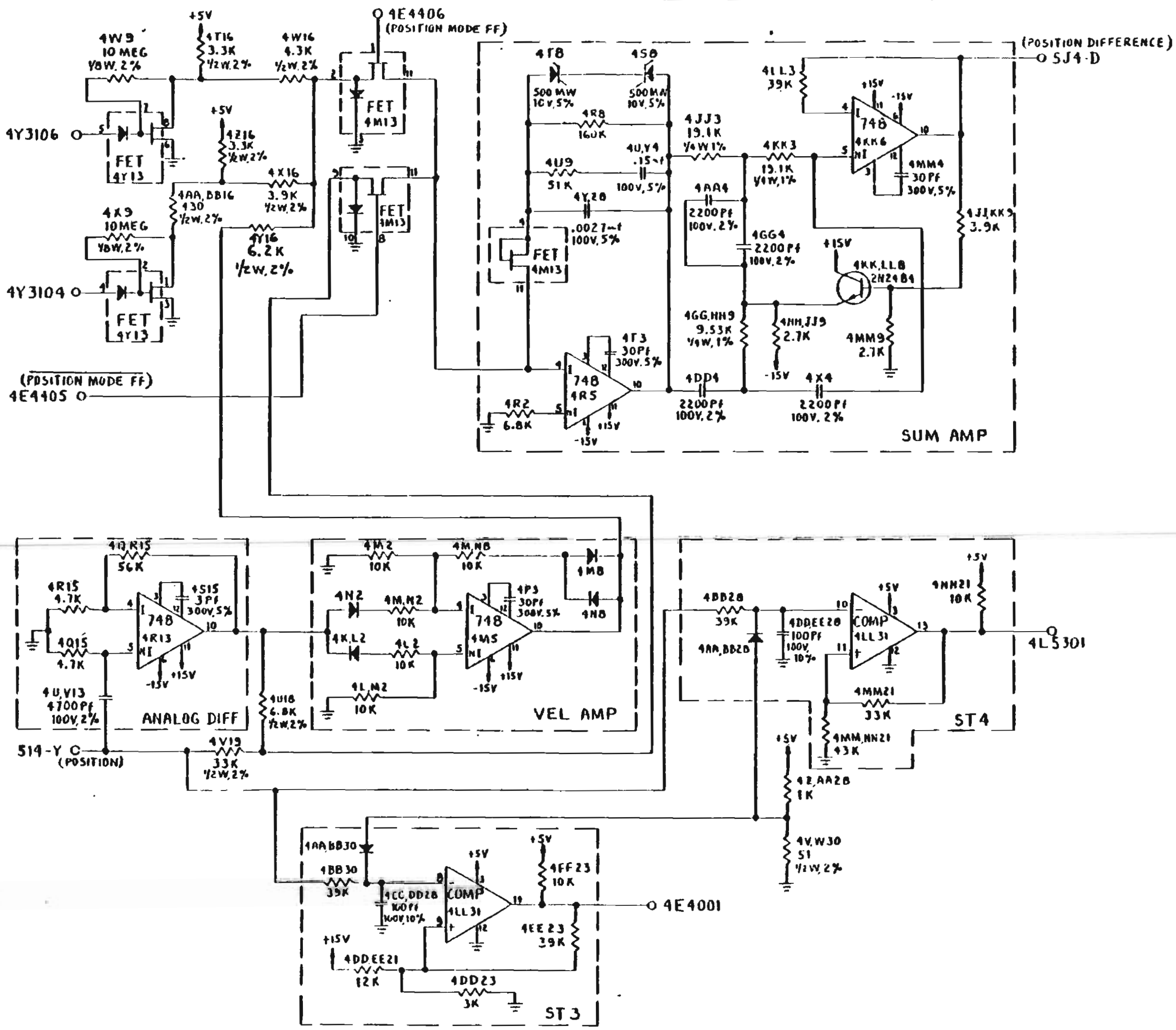
NOTES:
1. UNLESS OTHERWISE SPECIFIED:
   A. ALL DIODES ARE TYPE 101.
   B. ALL RESISTANCE VALUES ARE IN OHMS, 1/4 W, 2%.

DR80-B-79 (K)

DRN J ALESI 6/8/73

COMPONENT ASSEMBLY, MOTION CONTROL, PART 1

D 8878



THIS SHEET IS FOR 8878-13 ASSEMBLY
ON 8877-10 BARE BOARD ONLY.

NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. ALL DIODES ARE TYPE 101.
B. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.

REV	DESCRIPTION	APPROVED
9	ADDED THIS SHT ECN 4455-1C	

DECISION DATA COMPUTER CORP.

COMPONENT ASSEMBLY, MOTION CONTROL, PART 1

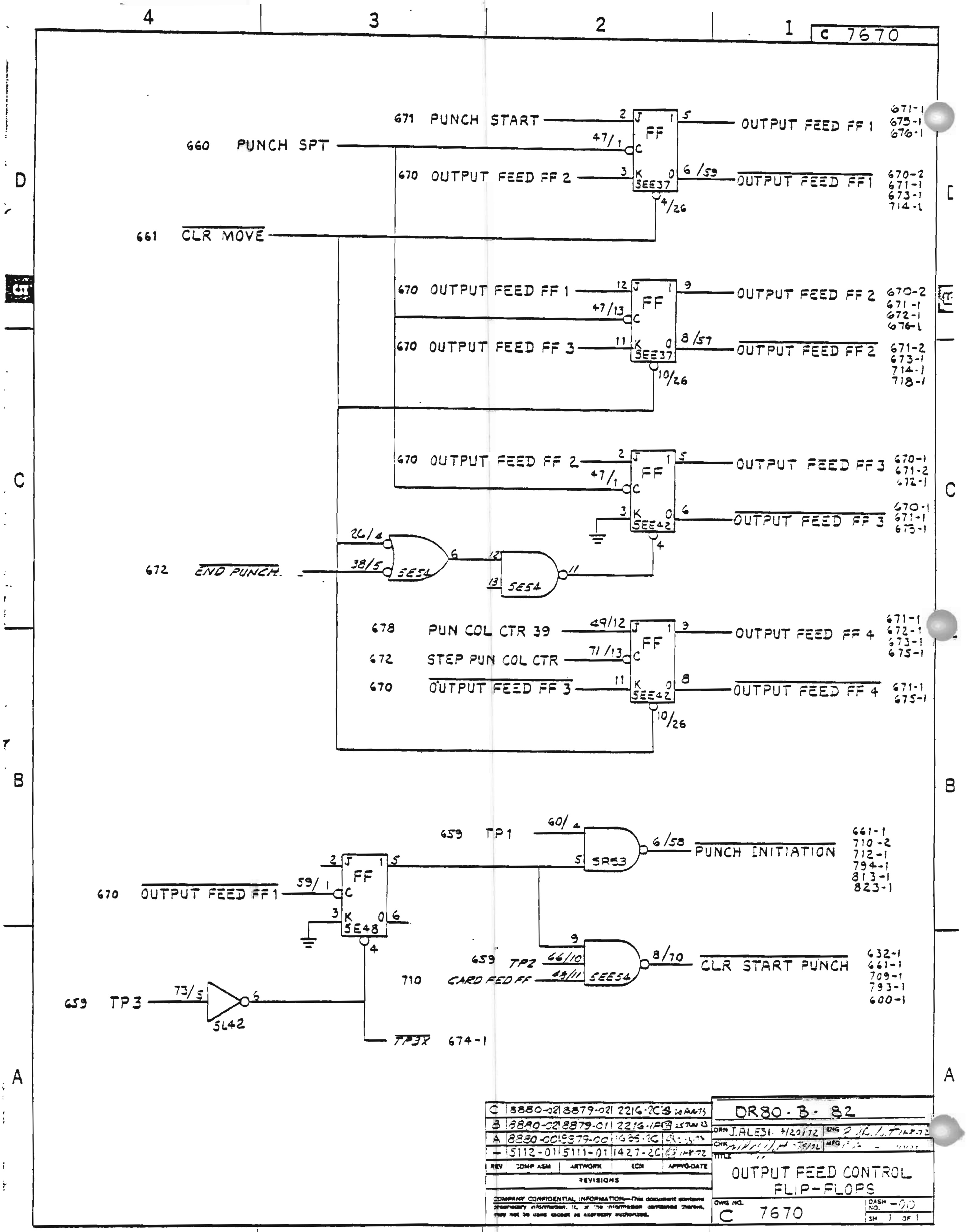
D 8819 -13

DR80-B-80

LOGIC/SCHEMATIC INDEX

MANUAL	TYPE/FEATURE		
7545	8001/8010 DR 80 Col	PWB	8879-03
902193	8010-11 DR 360	ASM	8880-03

DOCUMENT	SH	RV	DESCRIPTION LOCATION: 5
			PUNCH CONTROL
7670		-C	Output Feed Control Flip Flops
7671	01	-F	Punch Start Control
7671	02	-F	Punch Start Control
7672		-C	Punch Stepping Control
7673		-C	Servo Move Control
7674		-C	Servo Hi-speed Control
7675		-C	Blank Data Detection
7676		-C	Punch Column Counter
7677		-B	Punch Column Counter, Partial Decodes
7678		-B	Punch Column Counter Decodes
7679		-C	Punch Check/Print Control
8880	04	-H	Schematic - Punch Control


C 7670
671 PUNCH START
660 PUNCH SPT
670 OUTPUT FEED FF 2
SEE37
OUTPUT FEED FF 1
671-1
675-1
676-1
OUTPUT FEED FF1
670-2
671-1
673-1
714-1
661 CLR MOVE
670 OUTPUT FEED FF 1
670 OUTPUT FEED FF 3
OUTPUT FEED FF 2
670-2
671-1
672-1
676-1
OUTPUT FEED FF 2
671-2
673-1
714-1
718-1
670 OUTPUT FEED FF 2
SEE42
OUTPUT FEED FF 3
670-1
671-2
672-1
OUTPUT FEED FF 3
670-1
671-1
673-1
672 END PUNCH
SE54
678 PUN COL CTR 39
672 STEP PUN COL CTR
670 OUTPUT FEED FF 3
OUTPUT FEED FF 4
671-1
672-1
673-1
675-1
OUTPUT FEED FF 4
671-1
675-1
659 TP1
SR53
PUNCH INITIATION
661-1
710-2
712-1
794-1
813-1
823-1
670 OUTPUT FEED FF1
SE48
659 TP2
710 CARD FED FF
SEE54
CLR START PUNCH
632-1
661-1
709-1
793-1
600-1
659 TP3
5L42
TP3X 674-1
DR80-B-82
DRN J. ALESI 4/20/72
REV | COMP ASM | ARTWORK | ECN | APPVD-DATE
C | 8880-02 | 8879-02 | 2216-2C
B | 8880-02 | 8879-01 | 2216-1P
A | 8880-00 | 8879-00 | 1635-2C
— | 5112-01 | 5111-01 | 1427-2C
REVISIONS
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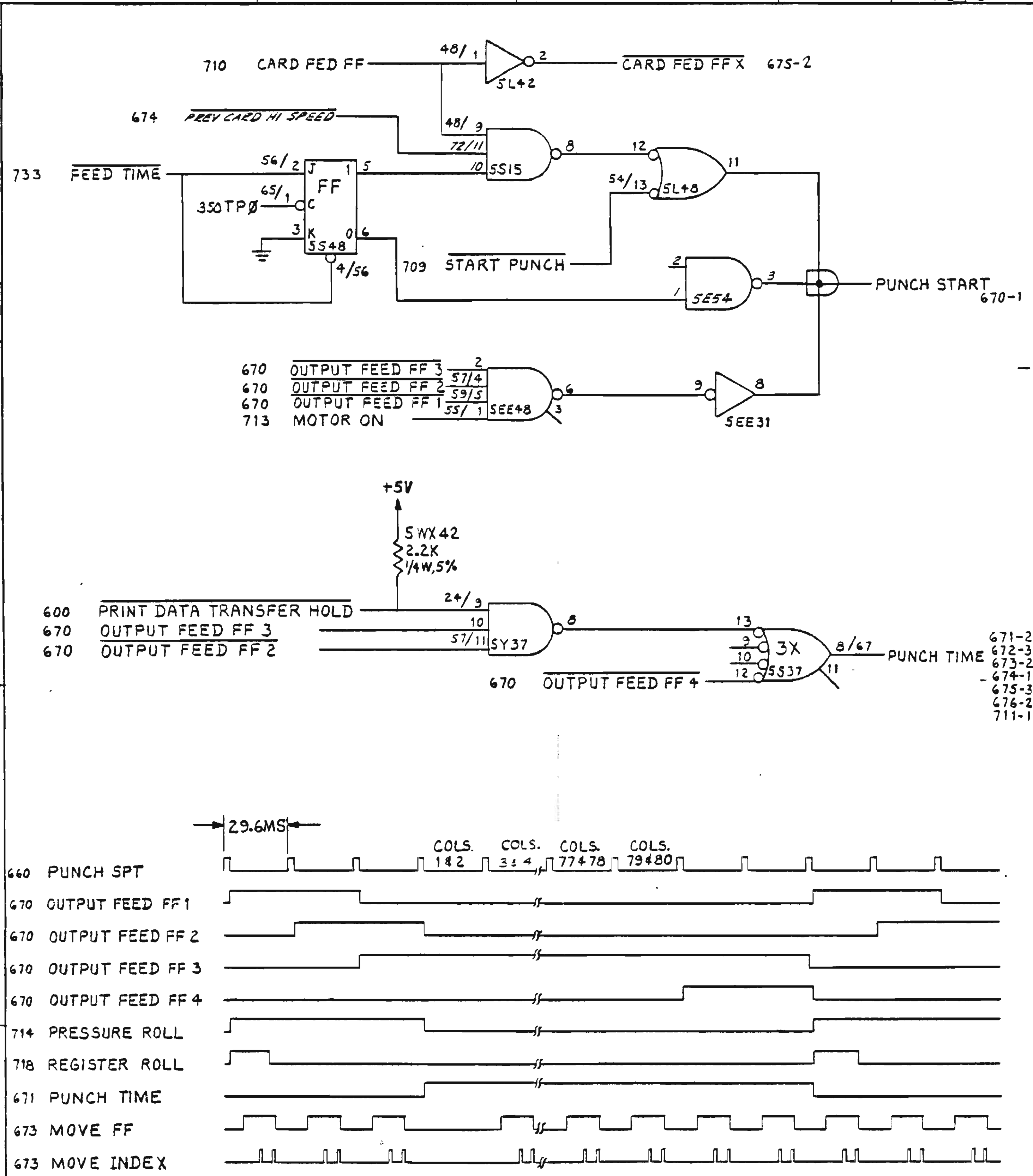
TITLE OUTPUT FEED CONTROL FLIP-FLOPS
DWG NO. C 7670
SH 1 OF 1



C 7671

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
D	8880-02	8879-01	2216-1P	
C	8880-01	8879-00	1963-1P	
B	8880-00	8879-00		
A	8880-00	8879-00	1685-2C	
—	5112-01	5111-01	1427-2C	
REVISIONS				

F	8880-02	8879-02	5664-1C	
E	8880-02	8879-02	2216-2C	

SHEET	REV	F	F
INDEX	SH	1	2

DR80-B 83

DRN J. ALESI 4/20/72

CHK 5/9/72

ENG

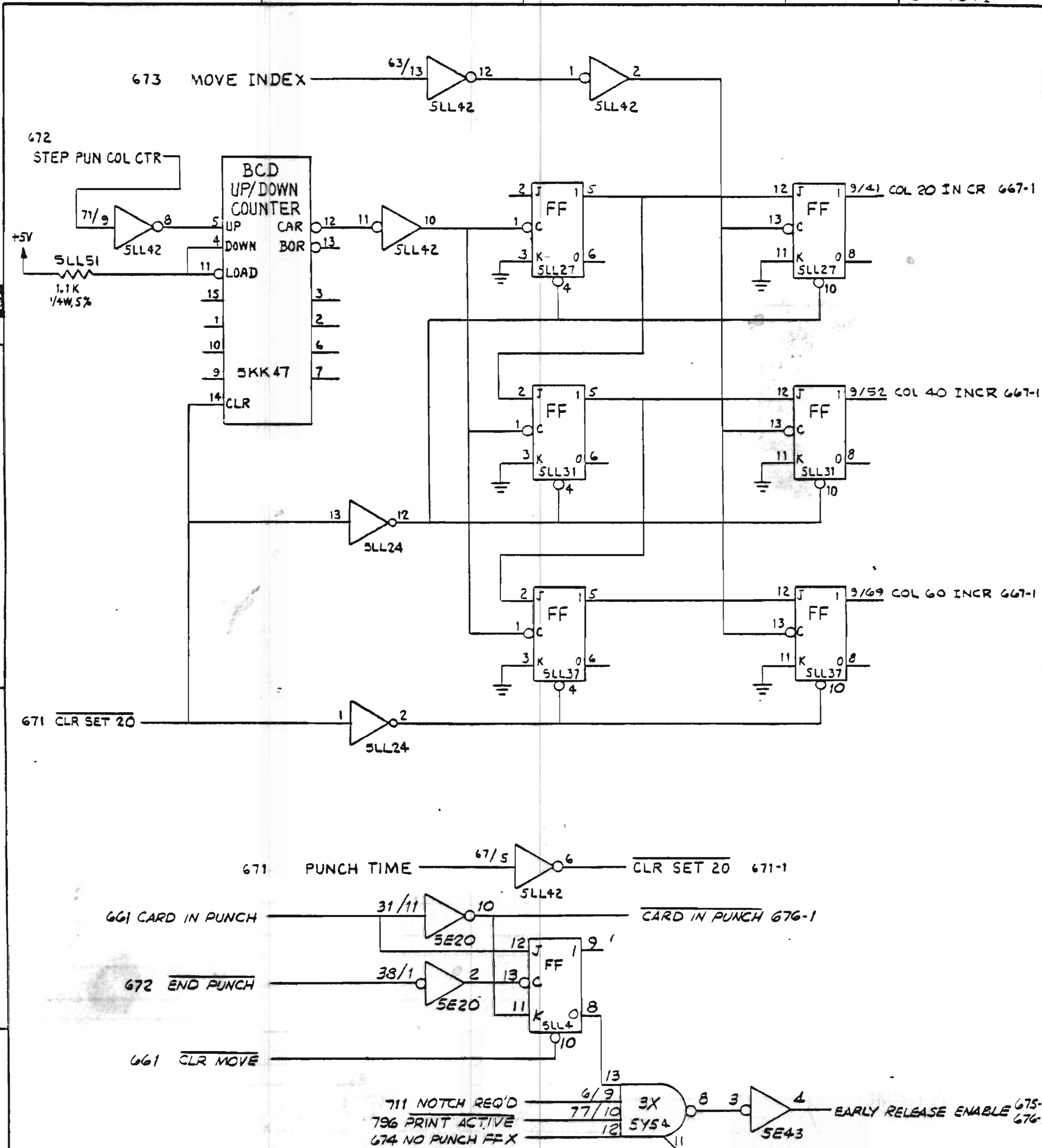
MFG

TITLE PUNCH START CONTROL

DWG NO. C 7671

DASH NO. -00

SH 1 OF 2



C	8880-01	8879-00	1963-1P	[illegible]
B	8880-00	8879-00	1919-1C	[illegible]
A	8880-00	8879-00	1635-2C	[illegible]
-	5112-01	5111-01	1427-2C	
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

F	8880-02	8879-02	5664-1C	[illegible]
E	8880-02	8879-02	2216-2C	[illegible]
D	8880-02	8879-01	2216-1P	[illegible]

DR80-B-84

DRN J. ALESI 2/6/73 ENG [illegible]

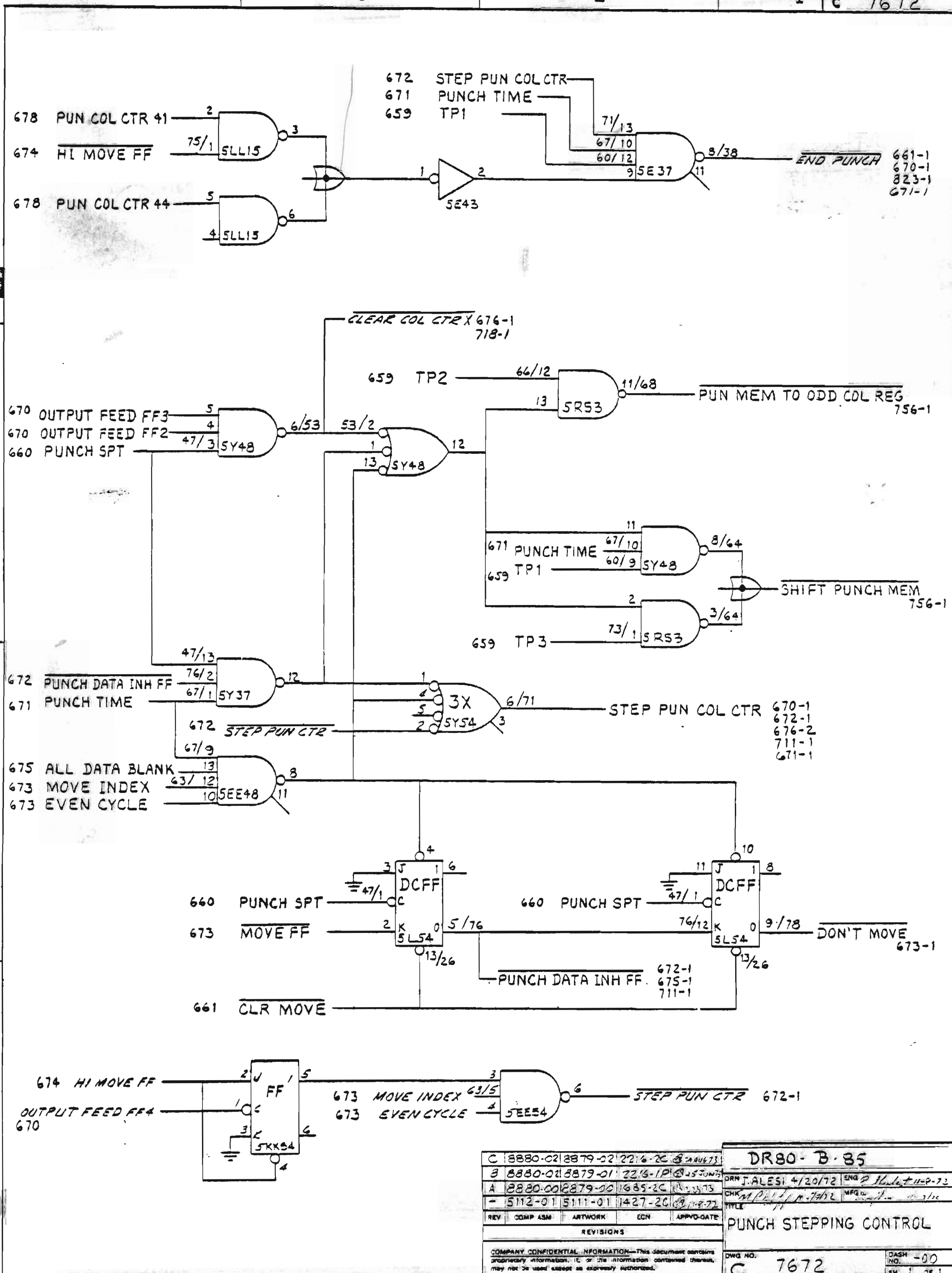
CHK MFG

TITLE PUNCH START CONTROL

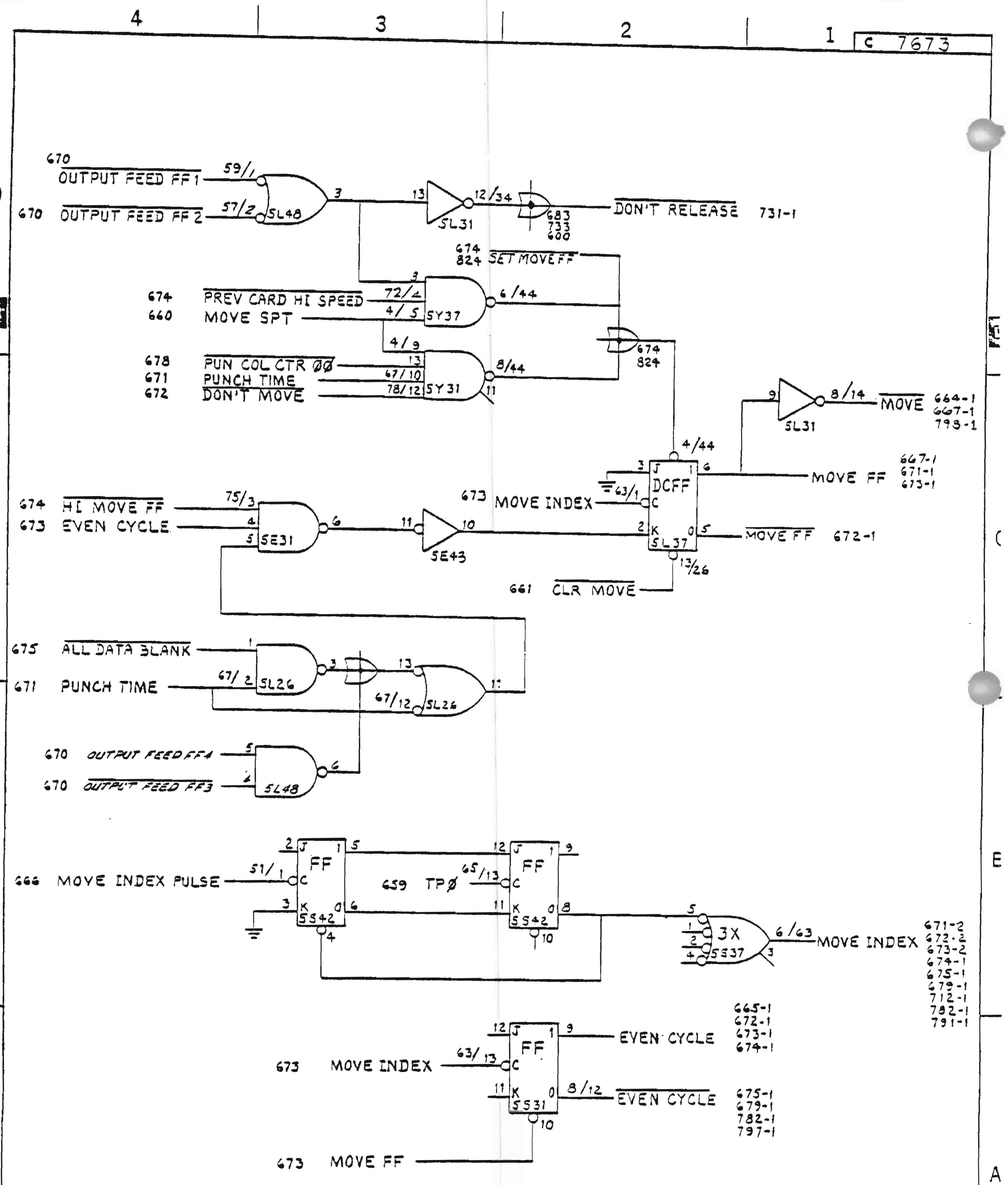
DWG NO. C 7671 DASH NO. -00

SH 2 OF

E79-082



4
3
2
1
C 7672
D
C
B
A
E
A
672 STEP PUN COL CTR
671 PUNCH TIME
659 TP1
678 PUN COL CTR 41
674 HI MOVE FF
678 PUN COL CTR 44
5LL15
5E43
5E37
END PUNCH
661-1
670-1
823-1
671-1
CLEAR COL CTR X 676-1
718-1
659 TP2
5RS3
PUN MEM TO ODD COL REG
756-1
670 OUTPUT FEED FF3
670 OUTPUT FEED FF2
660 PUNCH SPT
5Y48
671 PUNCH TIME
659 TP1
SHIFT PUNCH MEM
756-1
659 TP3
5RS3
672 PUNCH DATA INH FF
671 PUNCH TIME
5Y37
672 STEP PUN CTR
3X
5Y54
STEP PUN COL CTR
670-1
672-1
676-2
711-1
671-1
675 ALL DATA BLANK
673 MOVE INDEX
673 EVEN CYCLE
5EE48
DCFF
660 PUNCH SPT
673 MOVE FF
5L54
660 PUNCH SPT
DCFF
5L54
DON'T MOVE
673-1
PUNCH DATA INH FF
672-1
675-1
711-1
661 CLR MOVE
674 HI MOVE FF
OUTPUT FEED FF4
670
FF
5KK54
673 MOVE INDEX
673 EVEN CYCLE
5EE54
STEP PUN CTR
672-1
C 8880-02 8879-02 2216-2C
B 8880-02 8879-01 2216-1P
A 8880-00 8879-00 1685-2C
- 5112-01 5111-01 1427-2C
REV COMP ASM ARTWORK ECN APPVD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION—This document contains proprietary information. It, or the information contained therein, may not be used except as expressly authorized.
DR80 - B - 85
DRN J. ALESI 4/20/72
CHK
ENG
MFG
TITLE PUNCH STEPPING CONTROL
DWG NO. C 7672
DASH NO. -00
SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	8880-018879-02	2216-2C		4 AUG 73
B	8880-018879-01	2216-1P		15 JUN 73
A	8880-018879-00	1685-2C		2-14-73
–	5112-01	5111-01	1427-2C	1-12-72

REVISIONS

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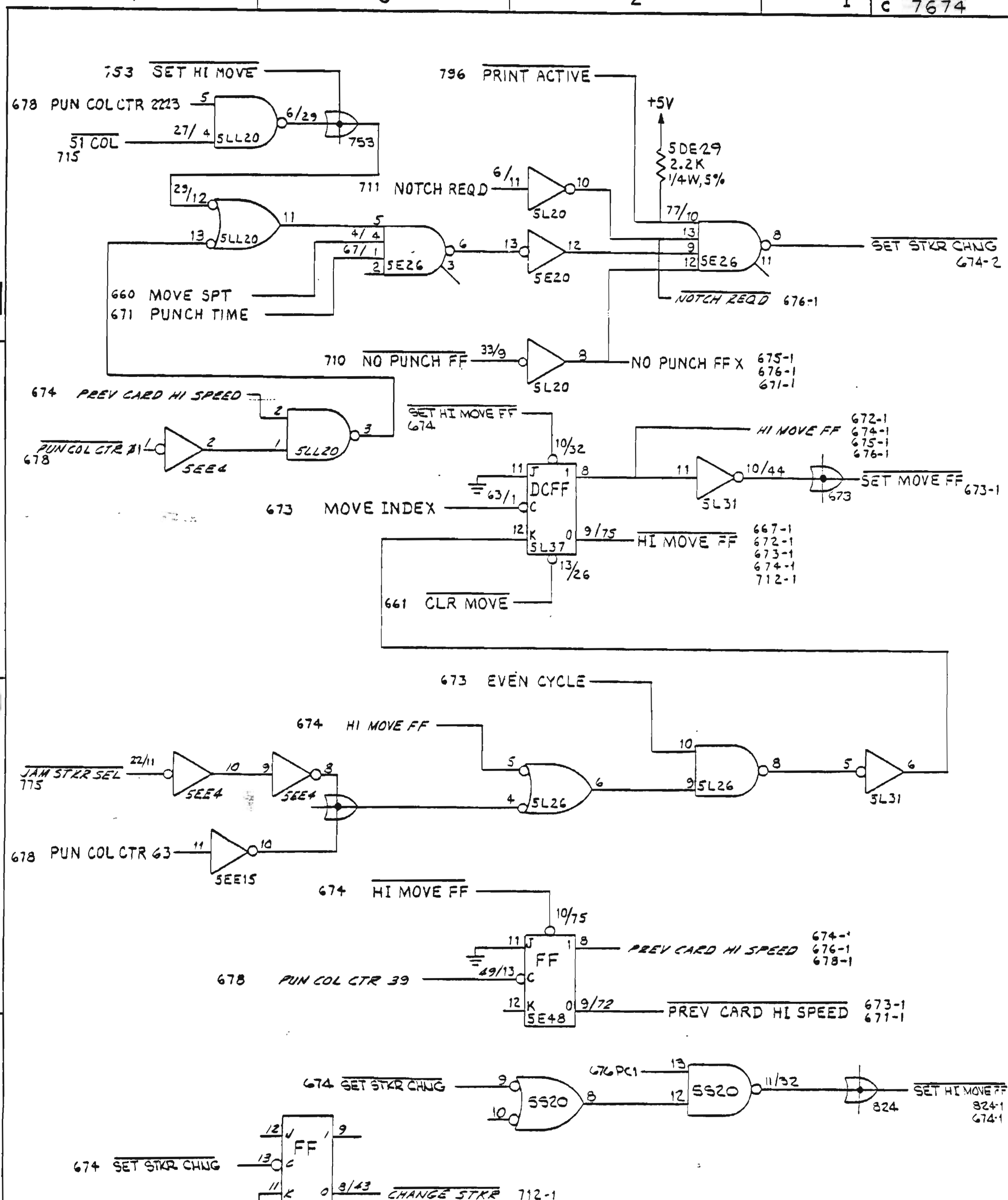
DR80-B-86

DRN J. ALESI 4/20/72

TITLE SERVO MOVE CONTROL

DWG NO. C 7673 DASH NO -00 SH 1 OF 1

C 7674



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	8880-02	8879-02	2216-2C	
B	8880-02	8879-01	2216-1P	
A	8880-00	8879-00	1685-2C	
-	5112-01	5111-01	1427-2C	

REVISIONS

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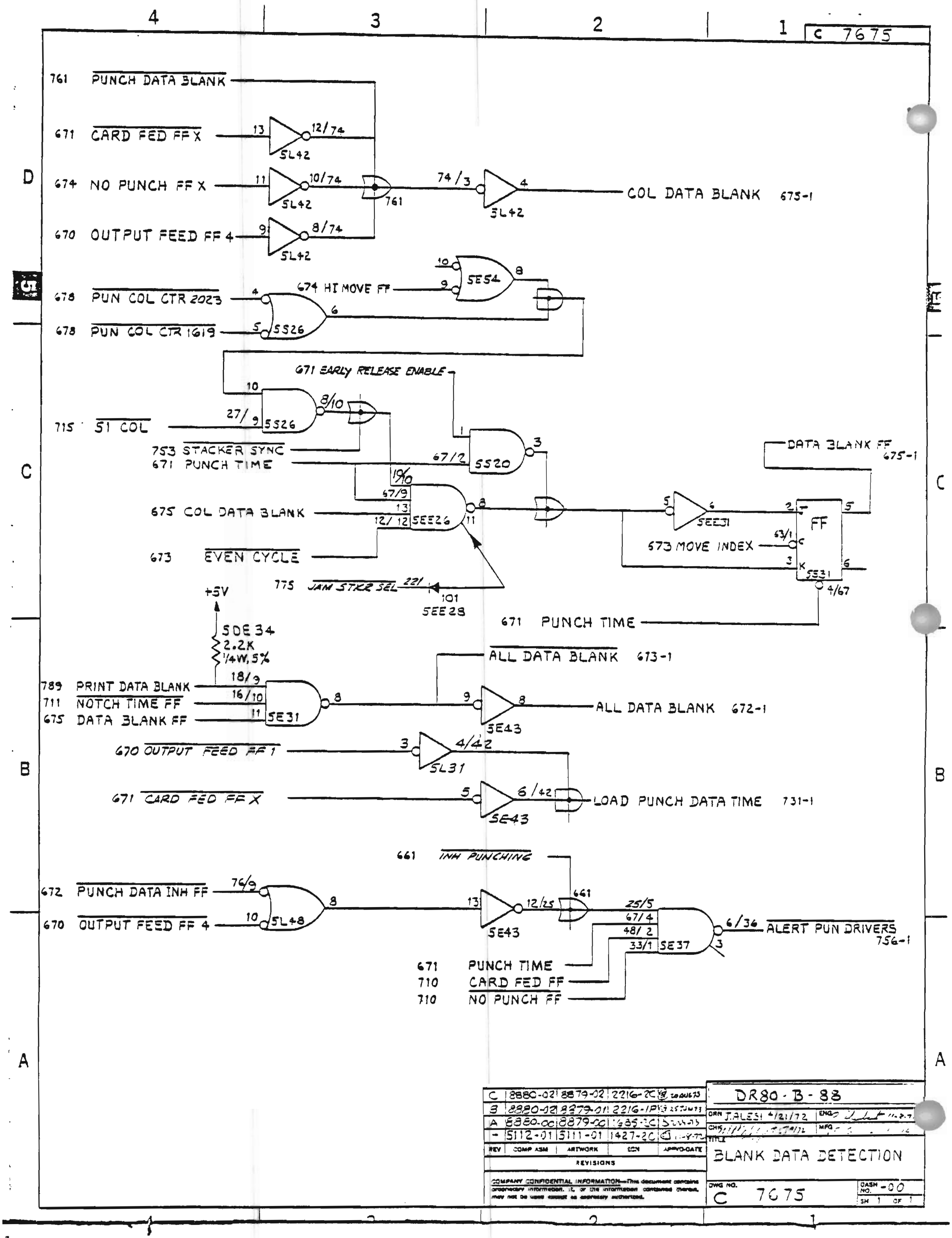
DR80-B-87

DRN J. ALESI 4/21/72

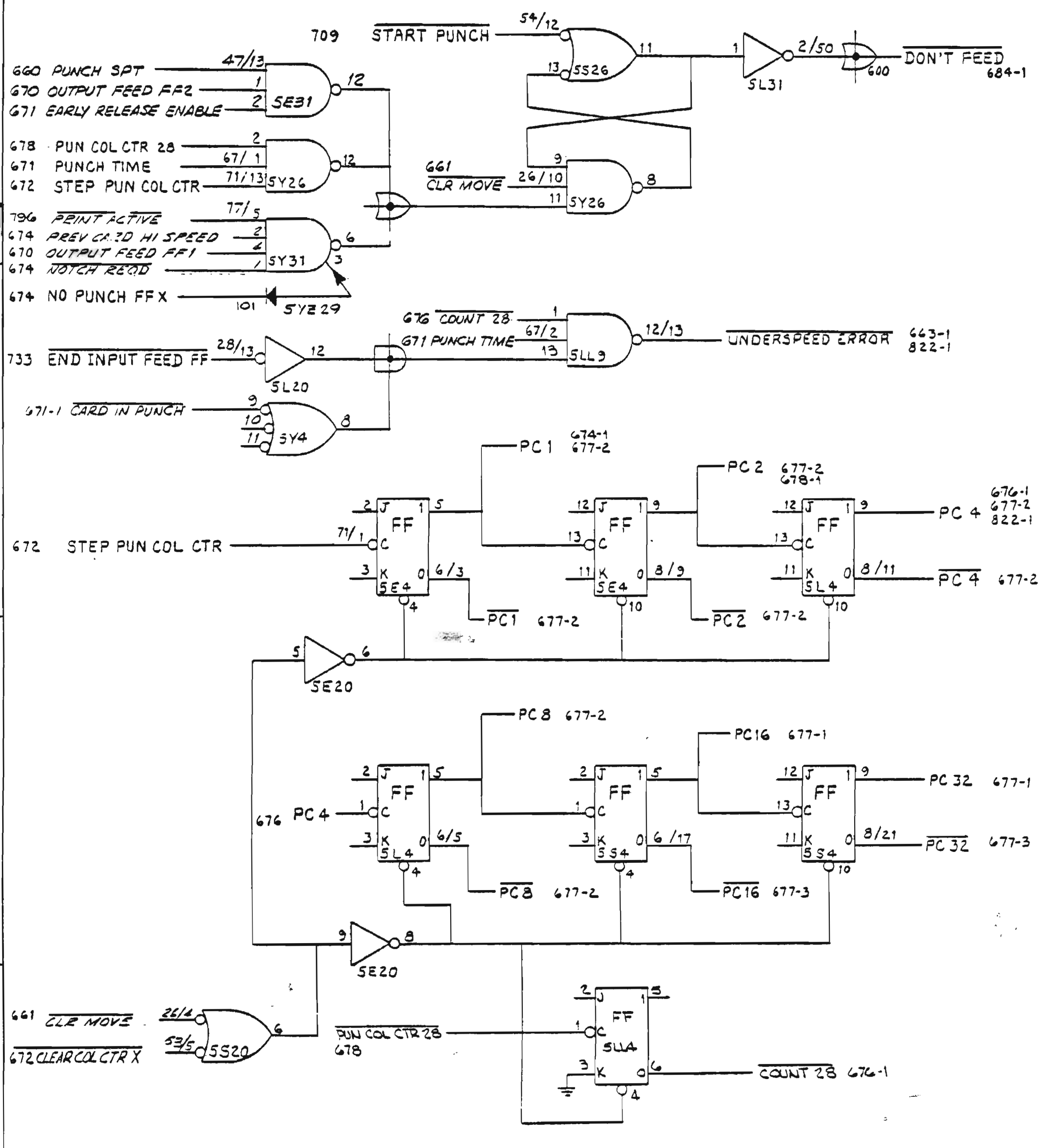
TITLE SERVO HI SPEED CONTROL

DWG NO. C 7674 DASH NO. -00 SH 1 OF 1



C 7675
761 PUNCH DATA BLANK
671 CARD FED FF X
674 NO PUNCH FF X
670 OUTPUT FEED FF 4
5L42
761
COL DATA BLANK 675-1
674 HI MOVE FF
5E54
678 PUN COL CTR 2023
678 PUN COL CTR 1619
5S26
671 EARLY RELEASE ENABLE
715 51 COL
753 STACKER SYNC
671 PUNCH TIME
5S20
675 COL DATA BLANK
5EE26
673 EVEN CYCLE
775 JAM STKR SEL
5EE28
5EE31
DATA BLANK FF 675-1
FF
573 MOVE INDEX
5S31
671 PUNCH TIME
+5V
5DE34
2.2K
1/4W, 5%
789 PRINT DATA BLANK
711 NOTCH TIME FF
675 DATA BLANK FF
5E31
ALL DATA BLANK 673-1
5E43
ALL DATA BLANK 672-1
670 OUTPUT FEED FF 1
5L31
671 CARD FED FF X
LOAD PUNCH DATA TIME 731-1
661 INH PUNCHING
672 PUNCH DATA INH FF
670 OUTPUT FEED FF 4
5L48
5E37
ALERT PUN DRIVERS 756-1
671 PUNCH TIME
710 CARD FED FF
710 NO PUNCH FF
DR80-B-88
J. ALESI 4/21/72
BLANK DATA DETECTION
REVISIONS
COMPANY CONFIDENTIAL INFORMATION—This document contains proprietary information. It, or the information contained therein, may not be used except as expressly authorized.
DWG NO. C 7675
DASH NO. -00
SH 1 OF 1

C 7676



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	8880-03	8879-02	2216-2C	10-26-73
B	8880-02	8879-01	2216-1P	25 Jul 73
A	8880-00	8879-00	1635-2C	1-23-73
—	5112-01	5111-01	1427-2C	11-8-72

REVISIONS

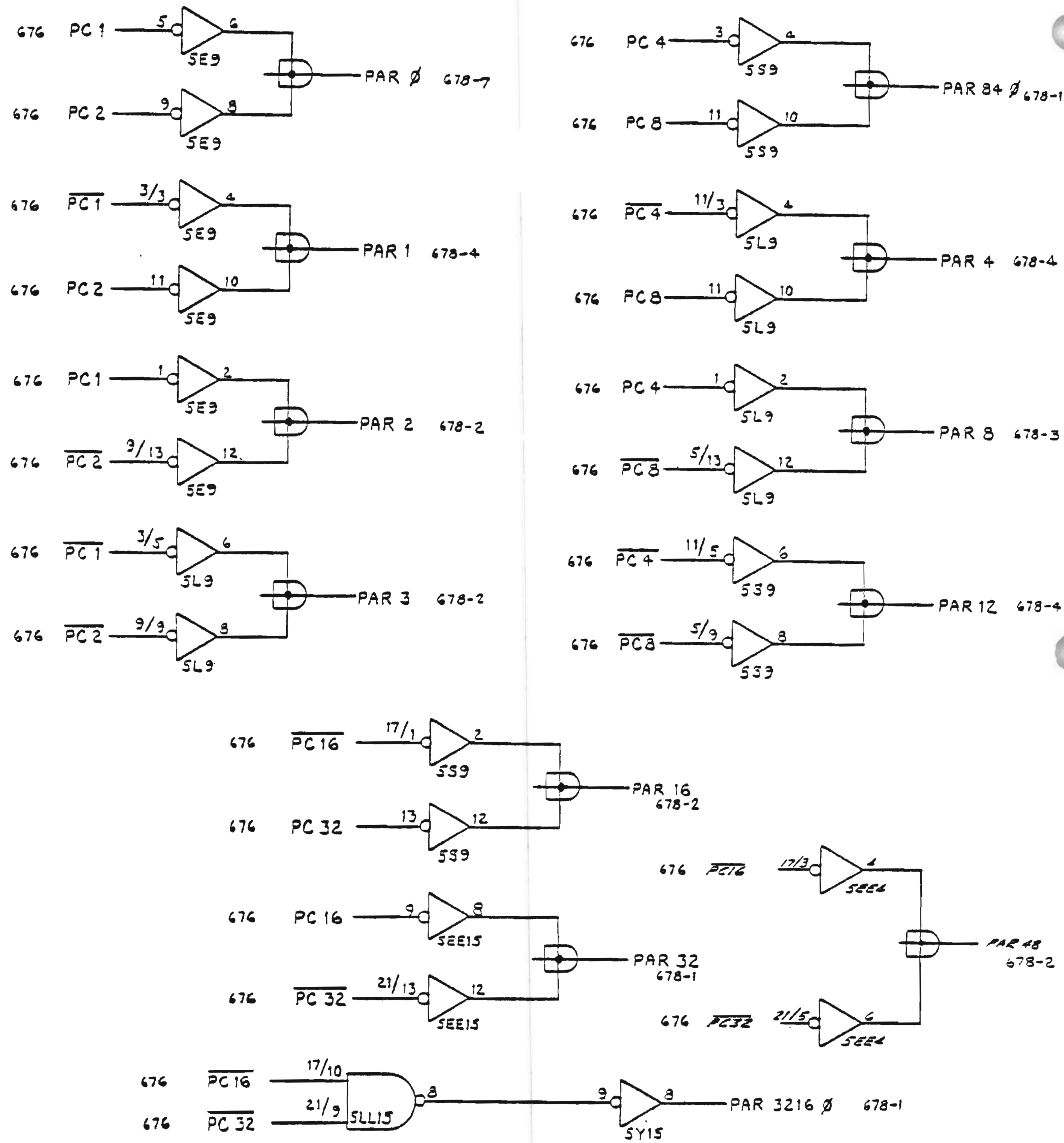
DR80-B-89

DRN J. ALESI 4/21/72 ENG 11-8-72
CHK 5/4/72 MFG

TITLE PUNCH COLUMN COUNTER

DWG NO. C 7676 DASH NO. -00 SH 1 OF 1

C 7677



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	8880-00	8879-00	1685-2C	2-13-73
A	5112-02	5111-01	1685-1P	11-4-72
-	5112-01	5111-01	1427-2C	11-8-72

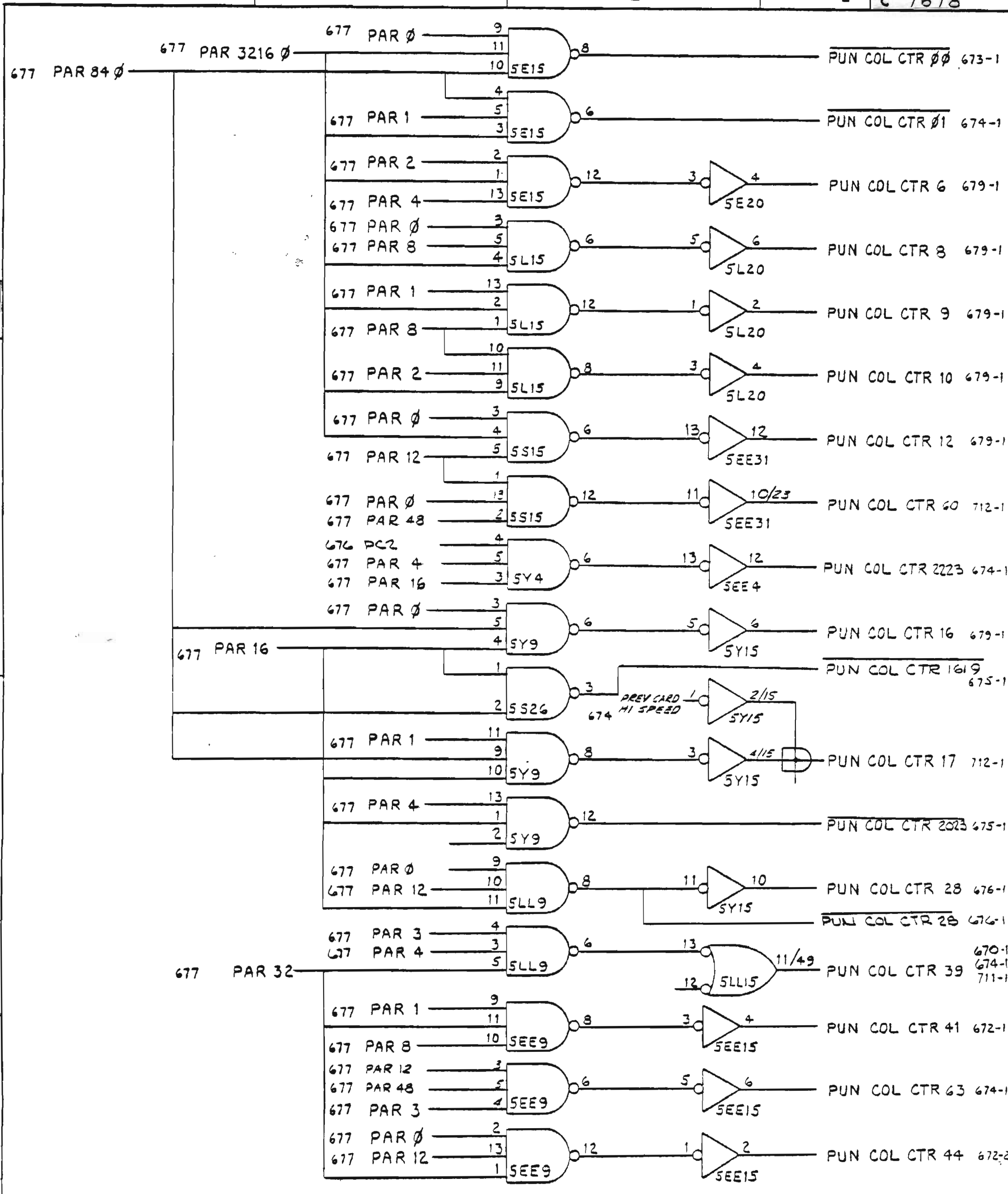
REVISIONS

DR80-B-90

DRN J. ALESI 4/21/72

PUNCH COLUMN COUNTER PARTIAL DECODES

DWG NO. C 7677 DASH NO. -00 SH 1 OF 1



DR80 - B - 91

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	8880-00	8879-00	1685-2C	7-23-73
A	5112-02	5111-01	1685-1P	
-	5112-01	5111-01	1427-2C	

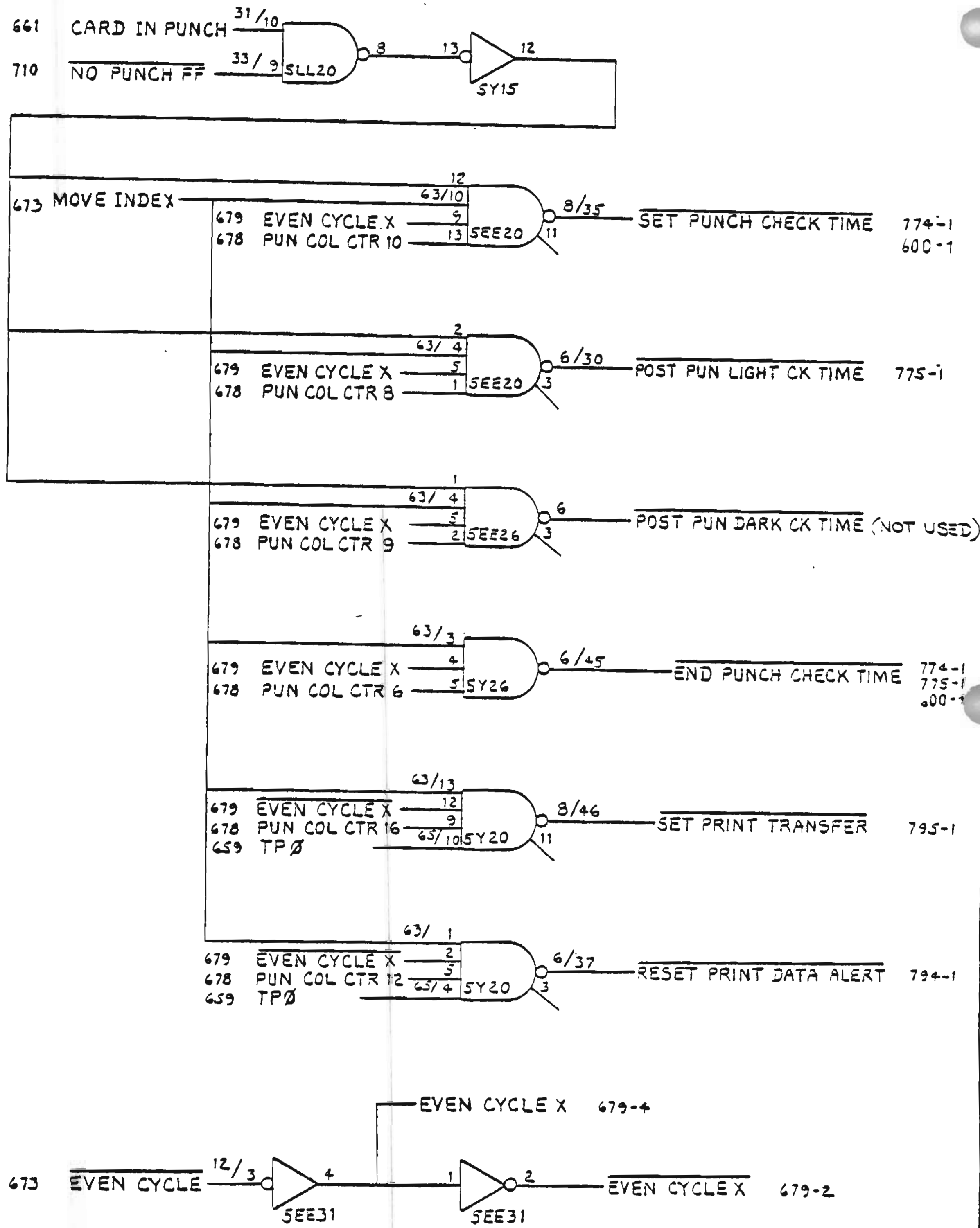
REVISIONS

DRN J. ALESI 4/22/72

TITLE PUNCH COLUMN COUNTER DECODES

DWG NO. C 7678 DASH NO. -00 SH 1 OF 1

C 7679



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	8880-03	8879-03	3739-1C	[illegible]
3	8880-00	8879-00	1683-2C	[illegible]
A	5112-02	5111-01	1665-1P	[illegible]
—	5112-01	5111-01	1427-2C	8/11-[illegible]

REVISIONS

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DR 80 - B - 92

DRN J. ALESI 4/22/72

TITLE PUNCH CHECK AND PRINT CONTROL

DWG NO. C 7679 DASH NO. -00 SH 1 OF 1

REV	DESCRIPTION	APPVD DATE
G	ADDED THIS SHT/ECN 2520-2C	10-1-73
H	REVISED PER ECN 2759-1C	10-13-73

*UNLESS OTHERWISE SPECIFIED:
ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR VARIATIONS LISTED; OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 8880-03 ON ARTWORK 8879-03

*VARIATION REFERENCE

VAR	ASSEMBLY	ARTWORK
I	8880-00-01	8879-00
II	8880-01-02	8879-01
III	8880-02-03	8879-02
IV	8880-03	8879-03

VAR I, II: 5V3, 5V14, 5V36, 5V48, 5PP,QQ34, 5PP,QQ50, 5B15, 5A,B41, 5V25
VAR III, IV: 5B5, 5B25, 5B53, 5V4, 5V15, 5V37, 5U,V48, 5PP34, 5QQ50

+5V — 4.7 µf 10V, 20%

VAR I, II: 5BB26, 5H31, 5BB53, 5H53, 5HH37, 5H42, 5BB42, 5P48, 5HH48, 5H37, 5HH,JJ4
5P4, 5H9, 5BB9, 5P15, 5HH15, 5H20, 5BB20, 5P26
VAR III, IV: 5H15, 5H43, 5G,H54, 5H,P48, 5BB9, 5BB20, 5AA,BB42, 5U53, 5HH4, 5GG,HH48, 5GG54

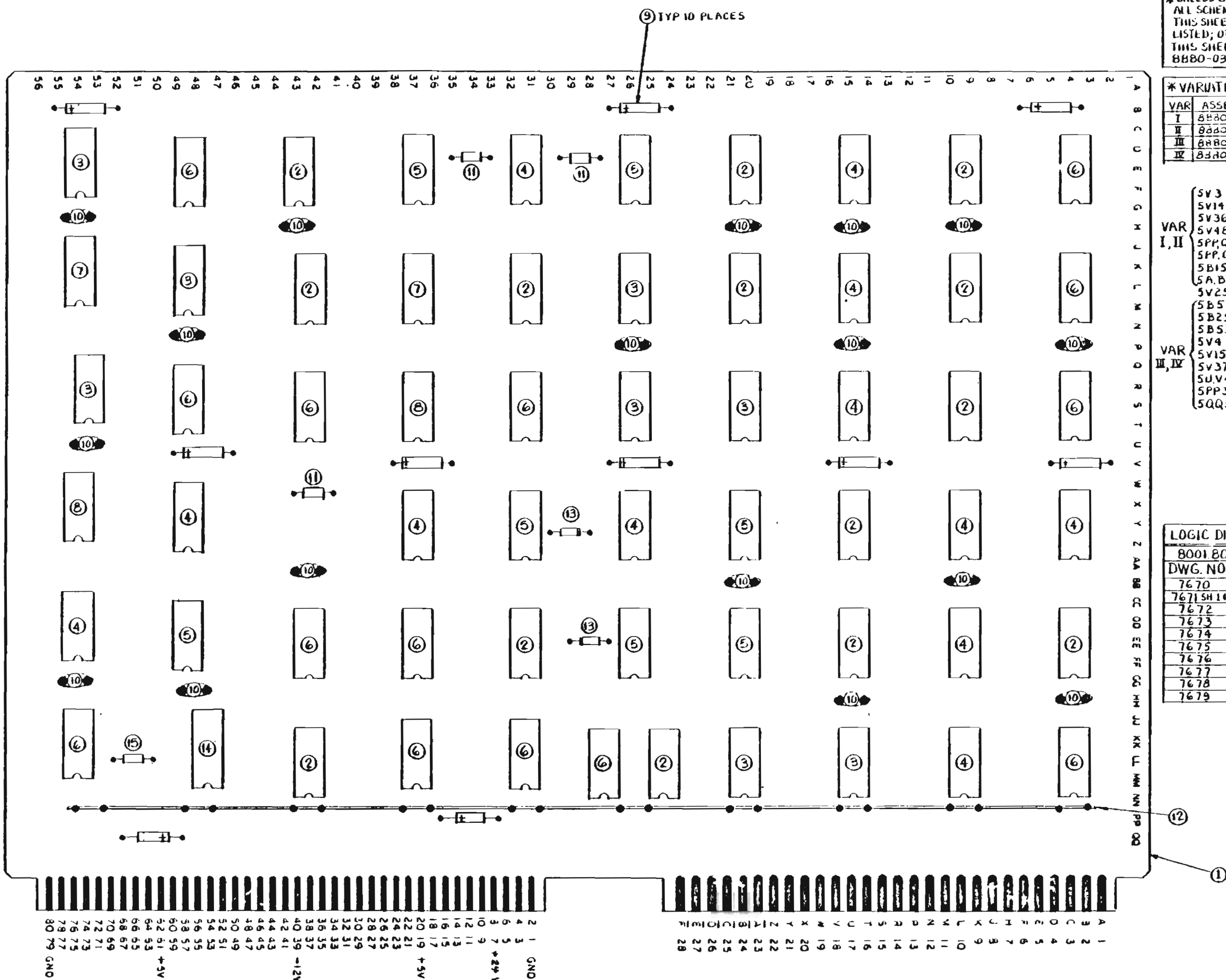
+5V — .02 µf 25V, +80%, -30%

LOGIC DIAGRAM CHART

8001 8010 DWG. NO.	REV	8035 8045 DWG. NO.	REV
7670	C	9283	-
7671 SH 1&2	E	9284	-
7672	C	9285	-
7673	C	9286	-
7674	C	9287	-
7675	C	9288	-
7676	C	9289	-
7677	B	9290	-
7678	B	9291	-
7679	B	9292	-
		9293	-

SPARE GATES

TYPE	LOC	OUTPUT PIN NO.
FLIP-FLOP	KK54	5, 6
6/1 INPUT	LL42	4
FLIP-FLOP	LL53	8,9
3/3 INPUT	EE54	12
6/1 INPUT	LL24	4,6,8,10
4/2 INPUT	R53	8
3/3 INPUT	Y4	12



FOR LIST OF PARTS SEE DETACHED PARTS LIST

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DR80-B-93

COMPONENT ASSEMBLY
PUNCH CONTROL

DWG NO. 8880 SH 4 OF

8880 D

DR80- B- 94

LOGIC/SCHEMATIC CONTENTS

PCB LOC 6

TYPE NUMBER/NAME BASIC 8001, 8010 INTERFACE

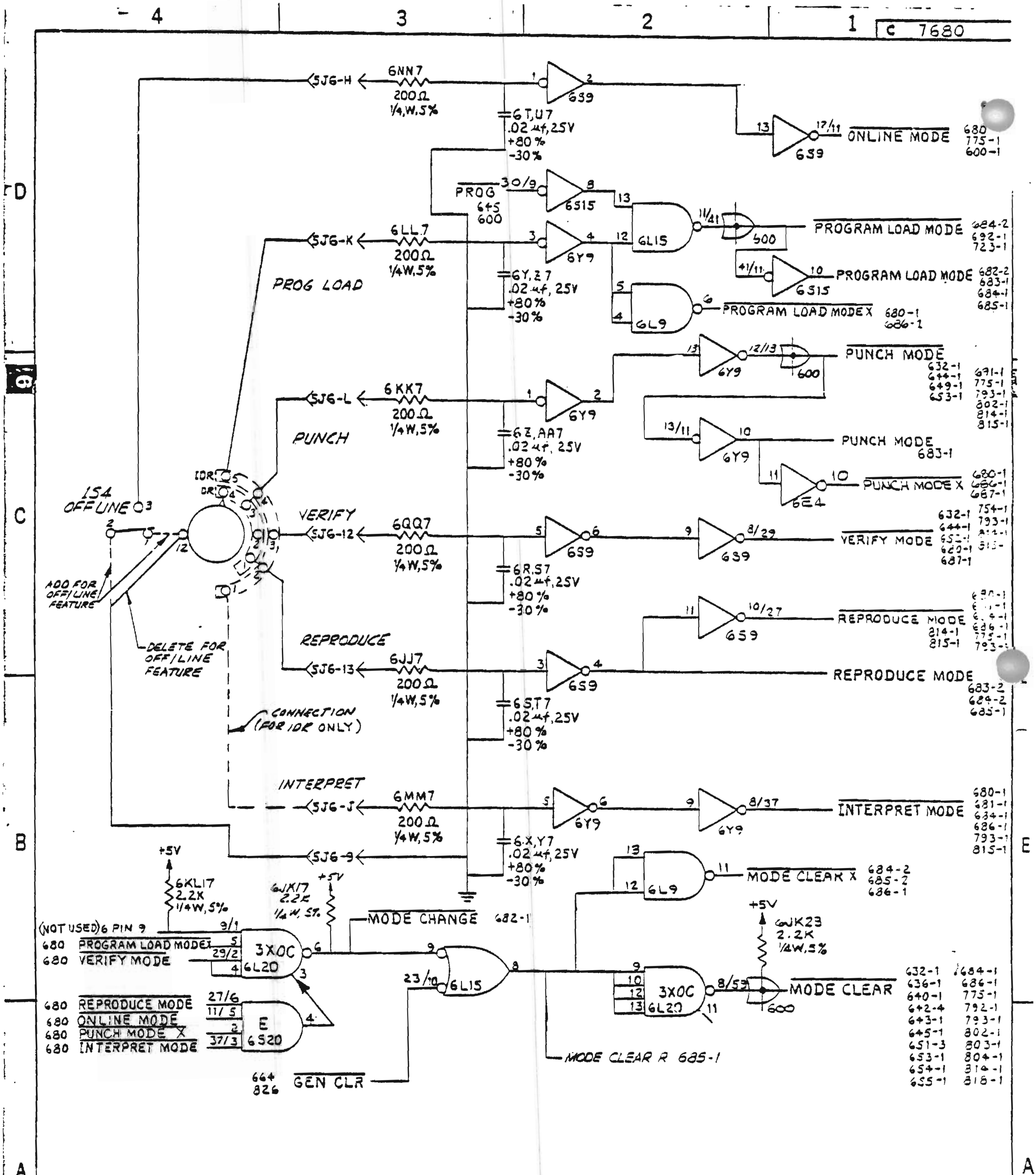
DWG NO	TITLE	PCB/ASM DRAWING DASH NO
3281	PWB	12
3282	MODE CONTROL	12

LOGIC SCHEMATIC	LOGIC DRAWING TITLE SCHEMATIC SHEET NO	LOGIC/SCHEMATIC DRAWING REVISION
7680	MODE CONTROL SWITCH	C
7681	START STOP SWITCHES	B
7682	RUN OUT	A
7683	START OUTPUT FEED GATES	A
7684	START INPUT FEED GATES	B
7685	REPRODUCE SEQUENCE CONTROL FF's	B
7686	STACKER FULL	B
7687	RESET CONTROL	B
7688	STATUS DISPLAY PT-1	B
7689	" " PT-2	B
7690	COLUMN DISPLAY	A
3282	Sheet 8	U

REVISION OF CONTENTS	U
DATE OF ECN	6 DEC 74
APPROVAL	
ENGINEERING CHANGE NOTICE	3913-IC

DR80-B-95

DWG NO 3282 REV — SHT — OF



C	3282-12	3281-12	2438-2C
B	3282-12	3281-11	2438-1A
A	3282-11	3281-09	1781-2P
-	3282-09	3281-09	1665-1C
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

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DR80 · B · 96

DRN 12/7/71

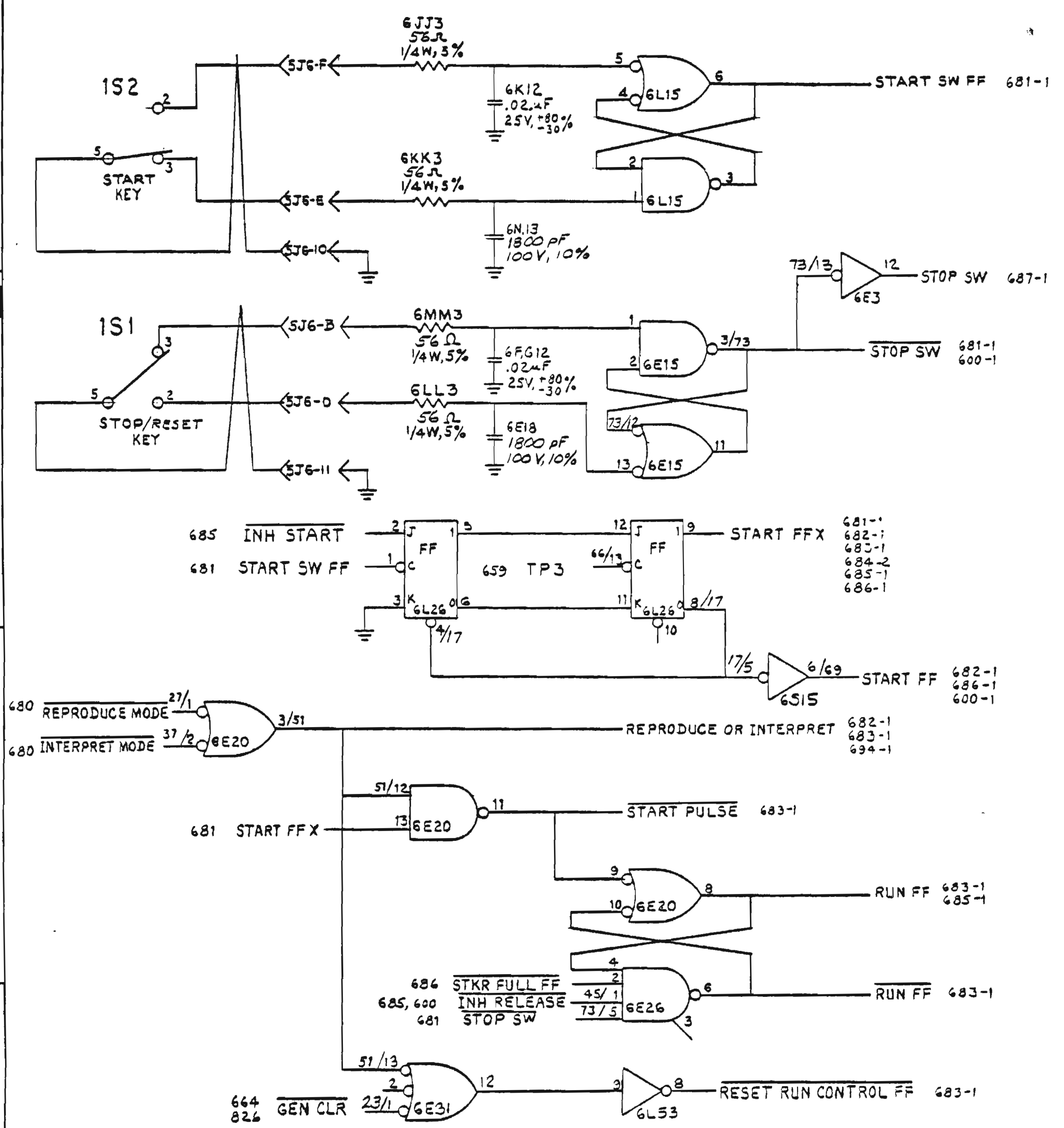
TITLE MODE CONTROL SWITCH

DWG NO. C 7680

DASH NO. 00

SH 1 OF

C 7681



DR80-B-97

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	3282-12	3281-11	2438-1P	
A	3282-10	3281-C9	1731-1P	
-	3282-09	3281-09	1665-1C	

REVISIONS

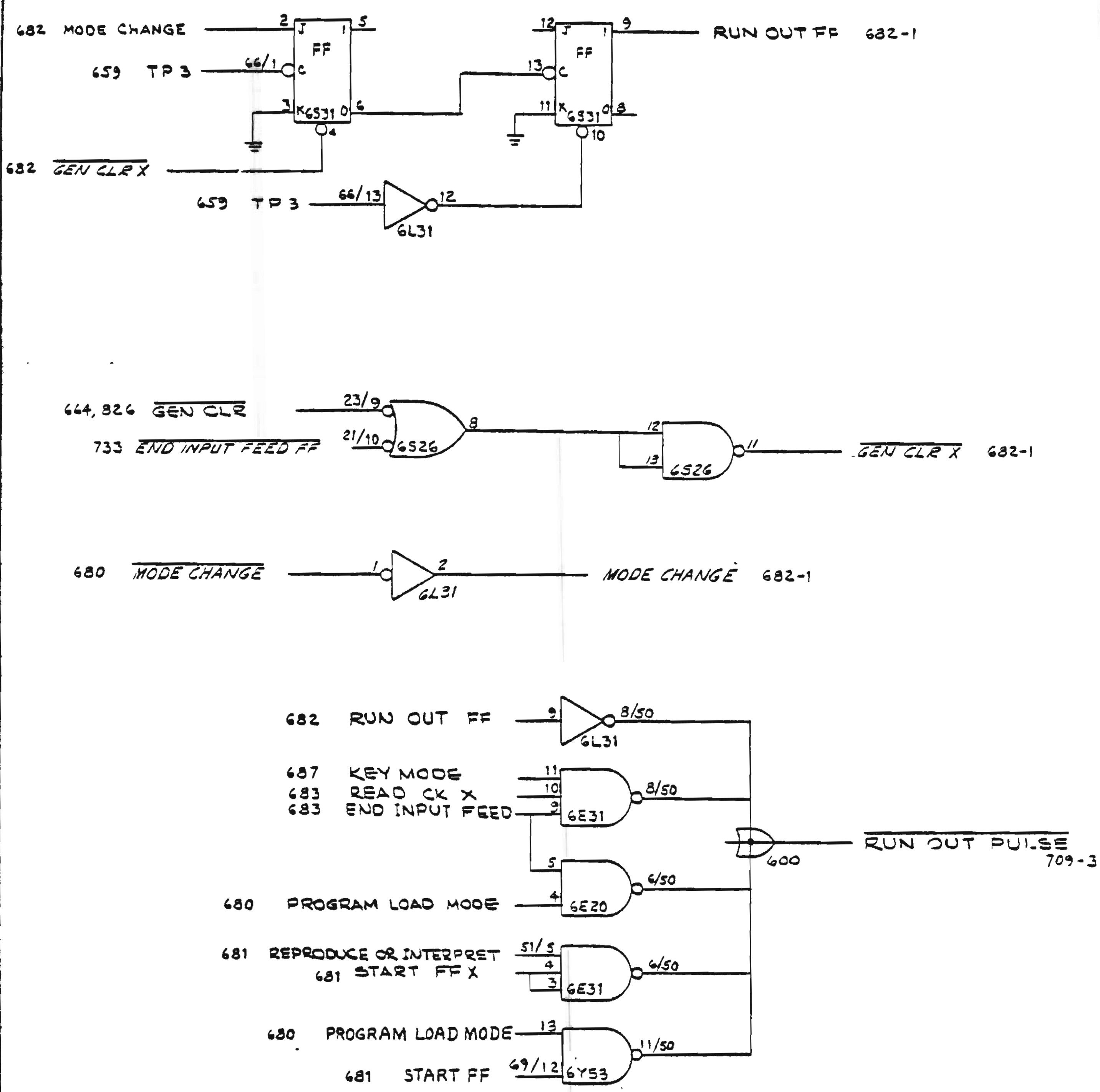
DRN 12/7/71
CHK 11/14/72

TITLE START STOP SWITCHES

DWG NO. C 7681

DASH NO. -00

SH 1 OF 1



C 7682

DR80-B-98

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	3282-12	3281-12	2438-2C	
–	3282-09	3281-09	1665-1C	

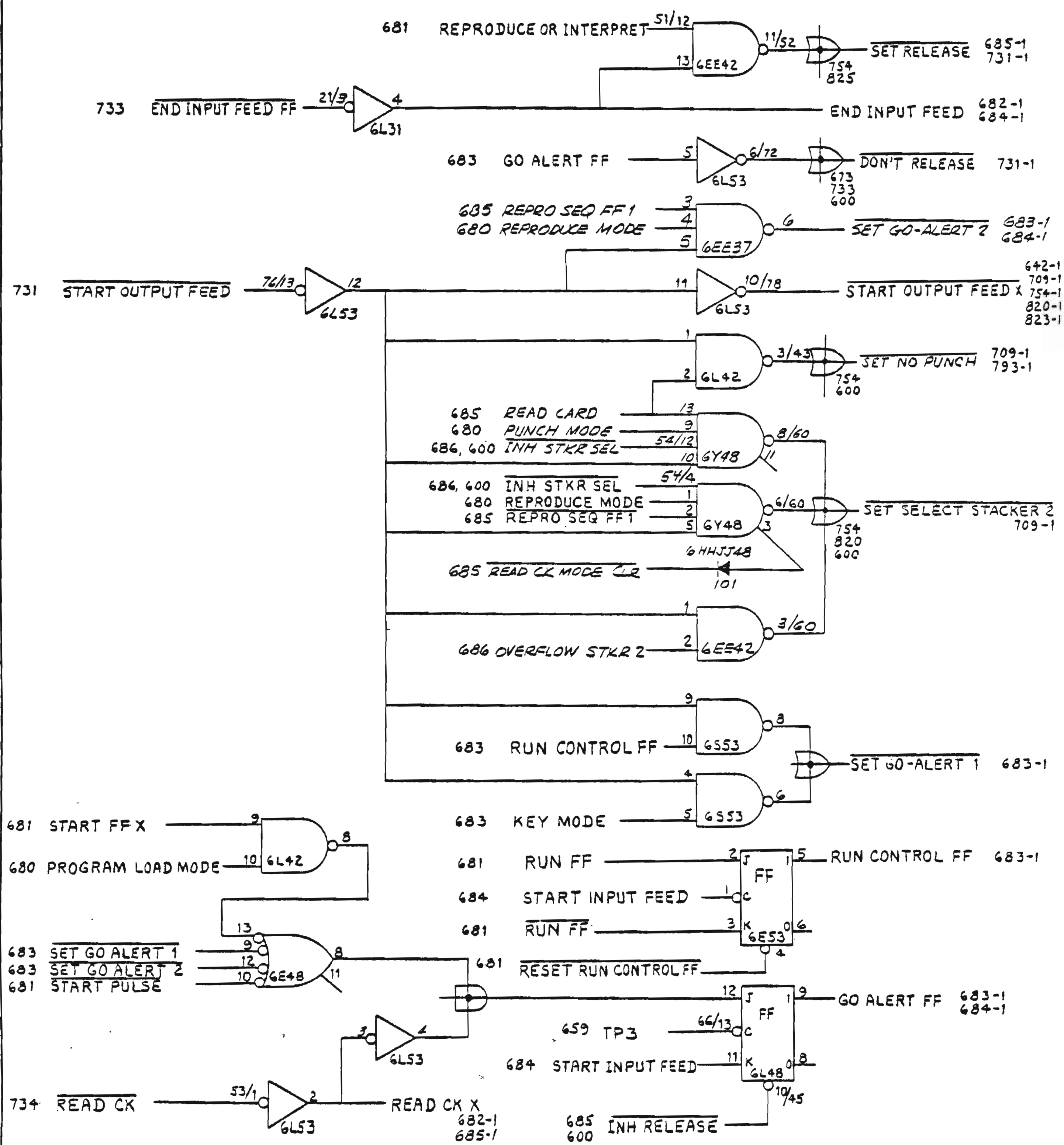
REVISIONS

TITLE: RUN OUT

DWG NO. C 7682

DASH NO 00

SH 1 OF 1



DR80-B-99

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	3282-12	3281-11	2438-1P	10/0ct73
-	3282-09	3281-09	1665-1C	12-5-72

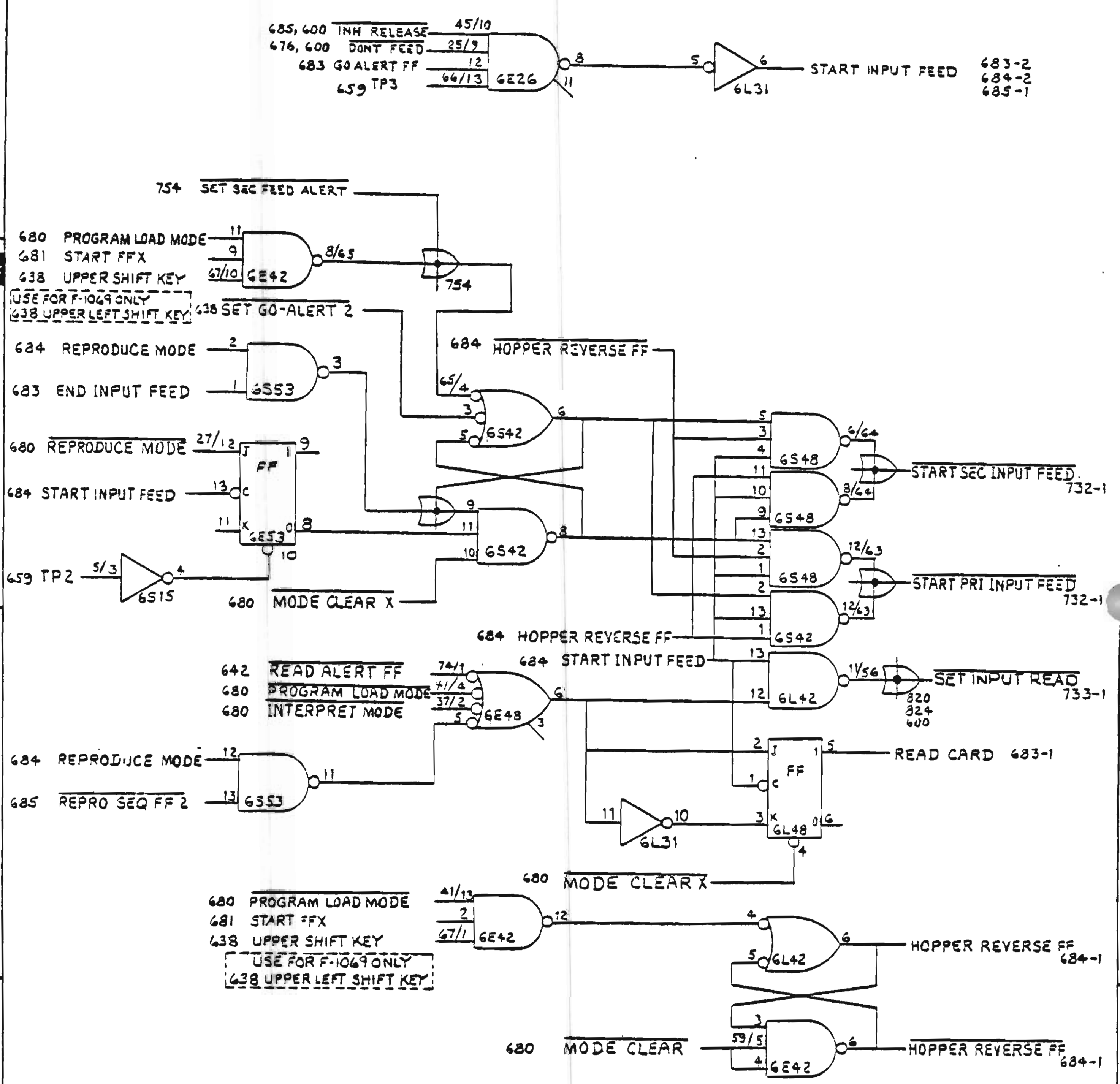
REVISIONS

DRN J. Hudy 12/7/71 ENG E. SHERBERT
CHK L. [illegible] 11/14/72 MFG [illegible]

TITLE START OUTPUT FEED GATES

DWG NO. C 7683 DASH NO. 00 SH 1 OF 1

C 7684



DR80-B-100

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	3282-12	3281-12	3330-1C	25 SEP 74
A	3282-12	3281-12	2438-2C	
—	3282-09	3281-09	1665-1C	17-5-72

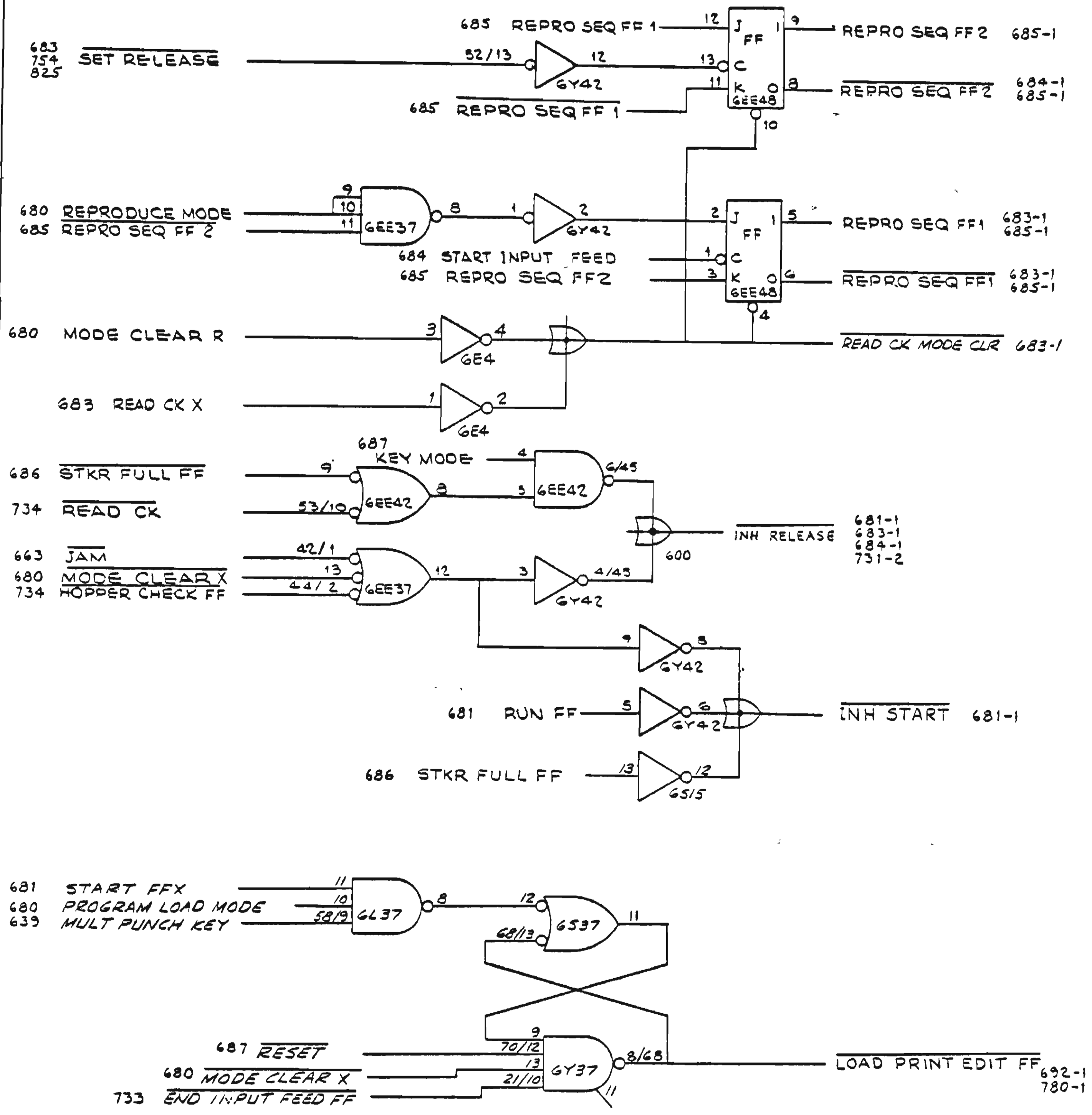
REVISIONS

DRN 2/7/71 ENG
CHK MFG

TITLE START INPUT FEED GATES

DWG NO. C 7684 DASH NO. 00 SH 1 OF 1

C 7685



DR80-B-101

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	3282-12	3281-12	2438-2C	
A	3282-12	3281-11	2438-1P	
-	3282-09	3281-09	1665-1C	12-5-72

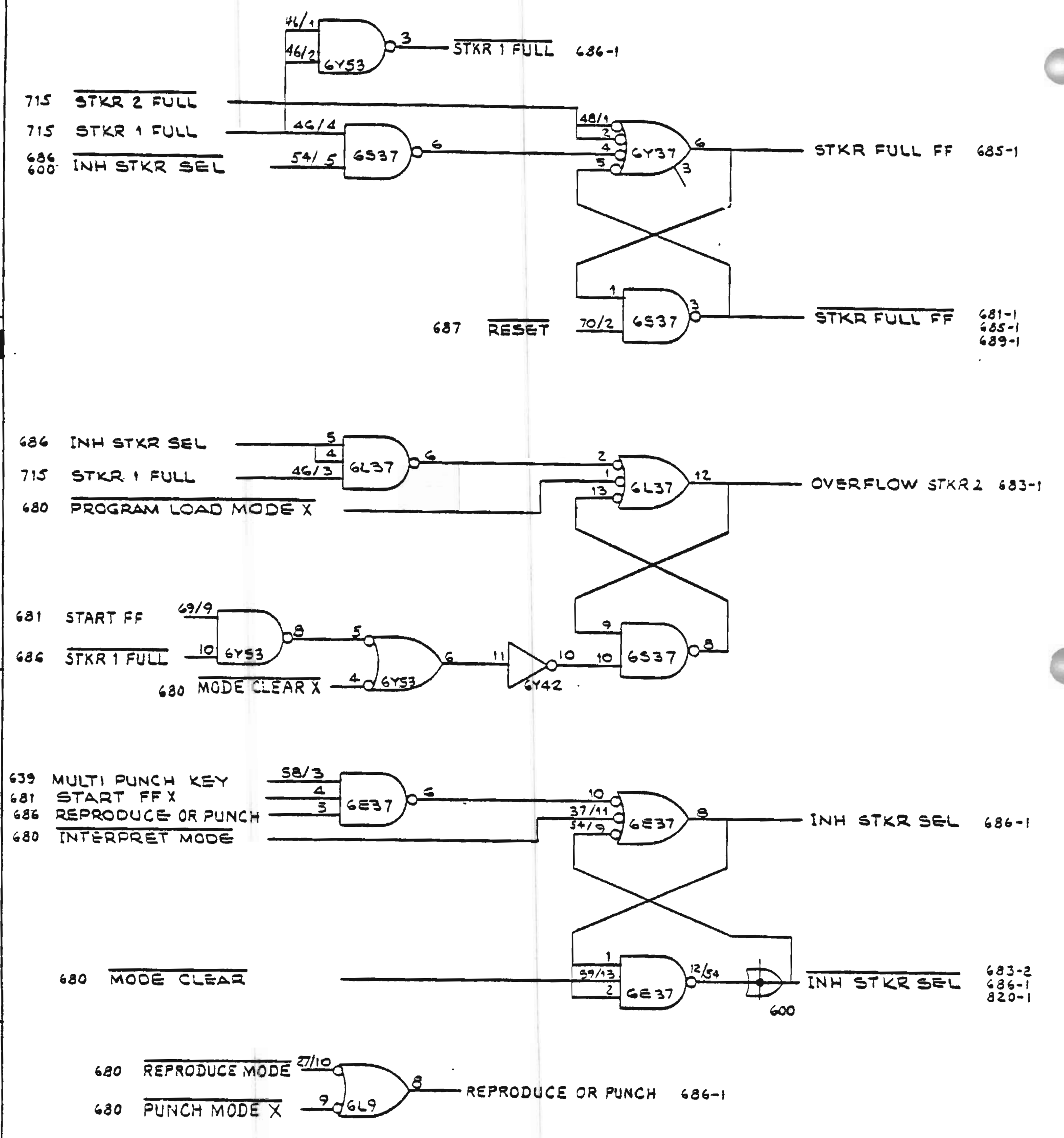
REVISIONS

DRN 12/7/71
CHK 1/14/72
ENG
MFG

TITLE REPRODUCE SEQUENCE CONTROL FLIP-FLOPS

DWG NO. C 7685 DASH NO. 00 SH 1 OF 1

C 7686



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
3	3282-12	3281-12	2438-2C	3
A	3232-11	3231-09	1781-2A	4-73
-	3282-09	3281-09	1665-1C	12-6-72

REVISIONS

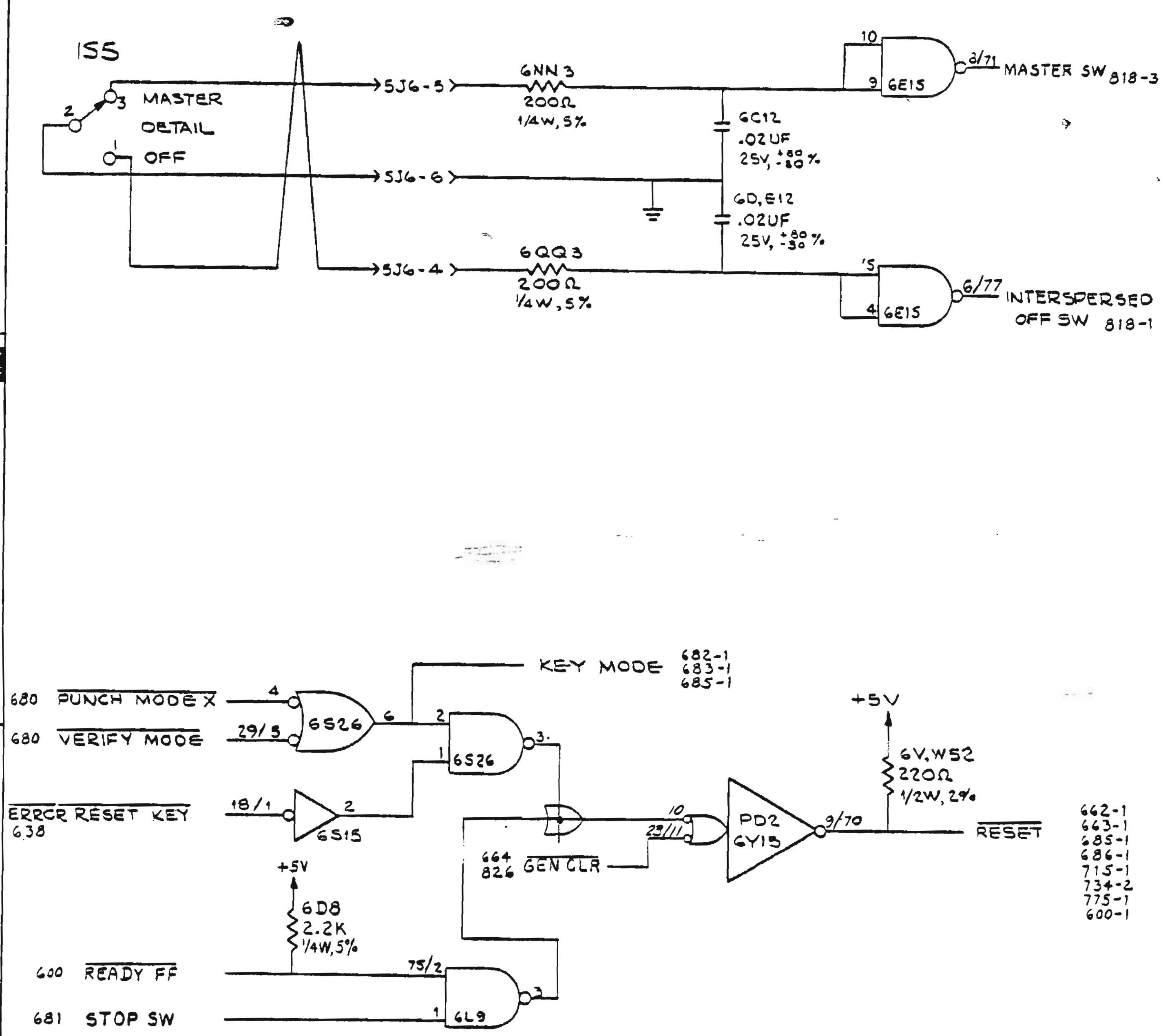
DR80 - B - 102

DRN F. Healy 12/7/71 ENG
CHK MFG

TITLE STACKER FULL

DWG NO. C 7686 DASH NO. 00 SH 1 OF 1

C 7687



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	3282-12	3281-12	2438-2C	
A	3282-11	3281-09	1781-2B	4-4-73
–	3282-09	3281-09	1665-1C	12-5-72

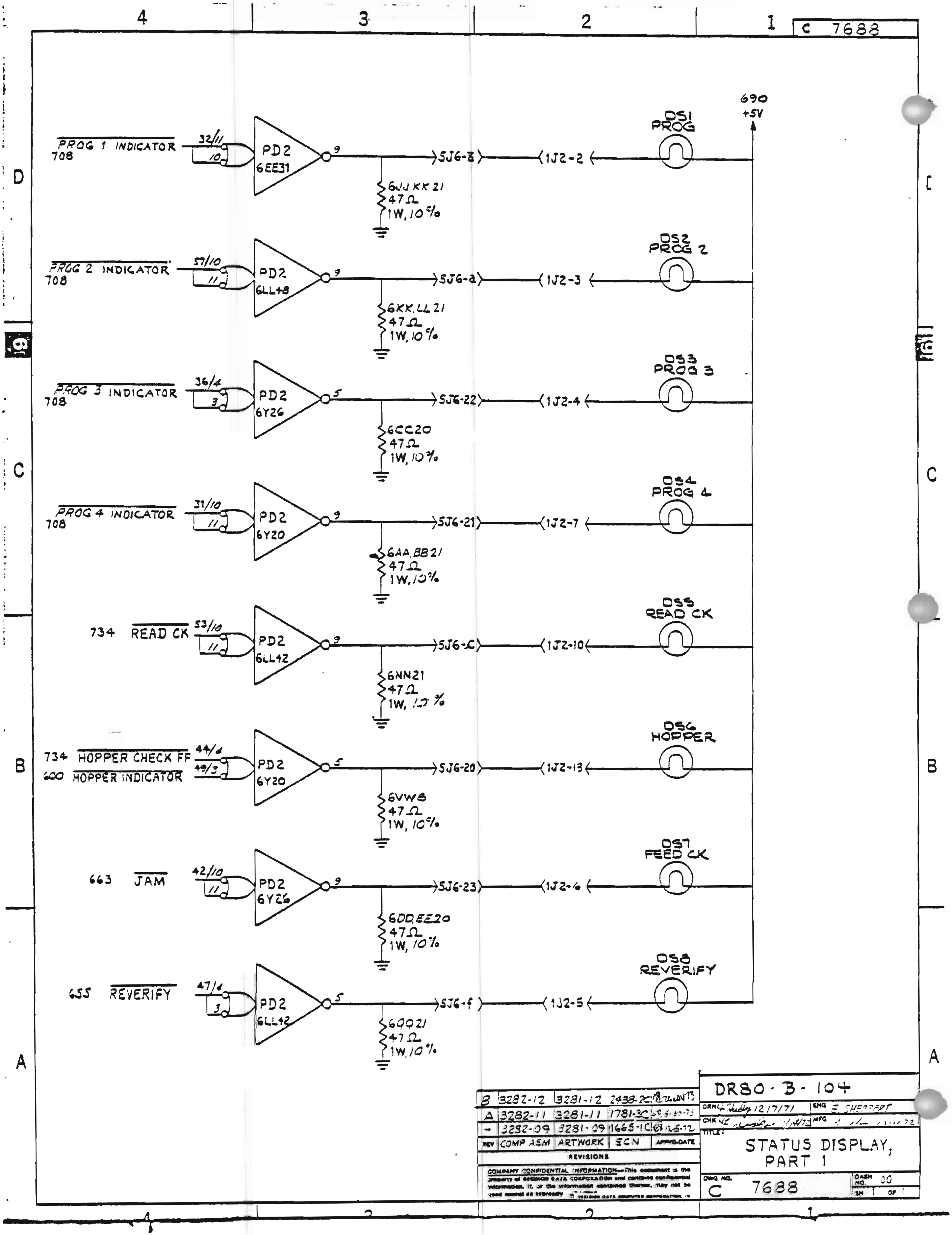
REVISIONS

DR80 - B - 103

DRN J. Hurley 12/7/71 ENG E. Shepeert
CHK

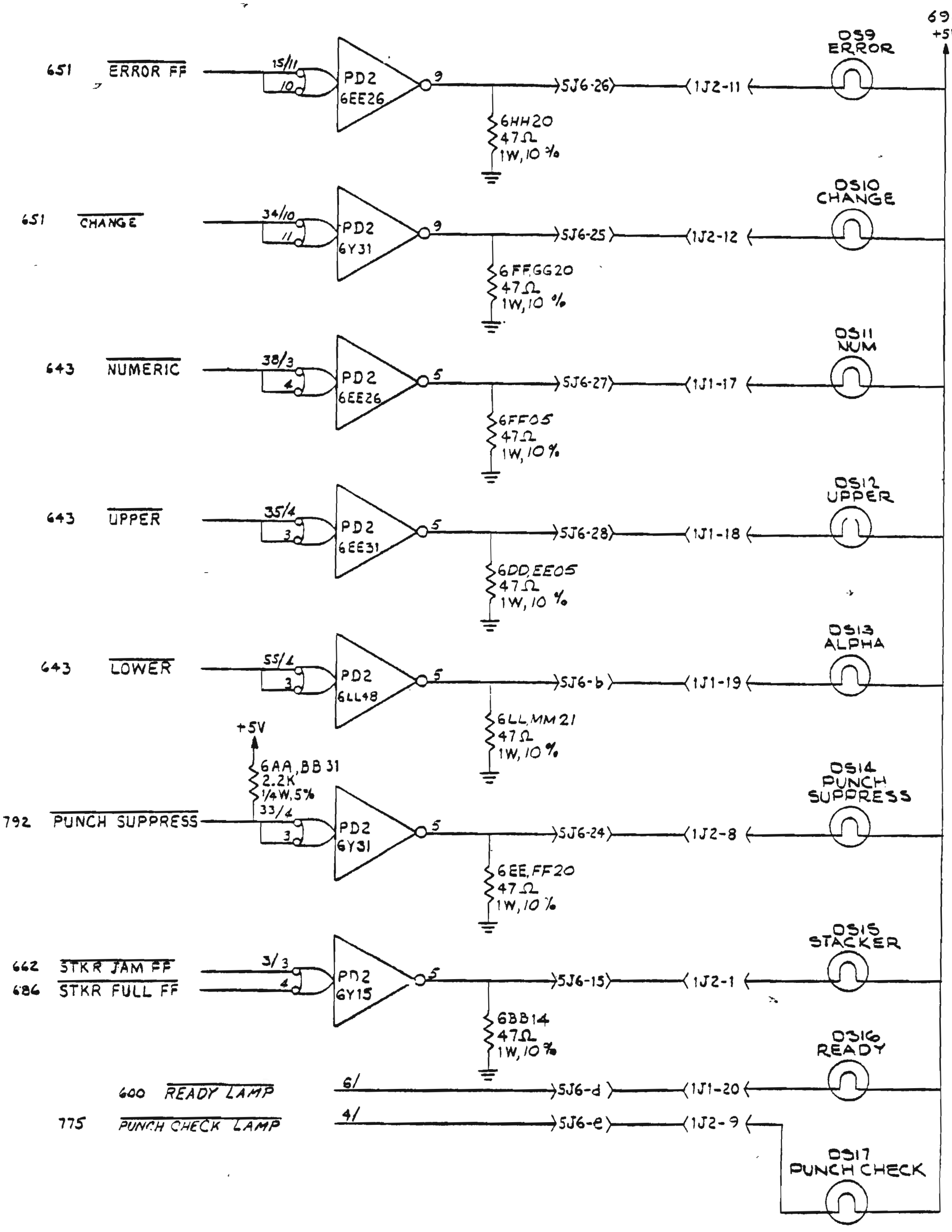
TITLE RESET CONTROL

DWG NO. C 7687 DASH NO. 00 SH 1 OF 1



C 7688
690 +5V
PROG 1 INDICATOR 708
PROG 2 INDICATOR 708
PROG 3 INDICATOR 708
PROG 4 INDICATOR 708
734 READ CK
734 HOPPER CHECK FF
600 HOPPER INDICATOR
663 JAM
655 REVERIFY
PD2 6EE31
PD2 6LL48
PD2 6Y26
PD2 6Y20
PD2 6LL42
PD2 6Y20
PD2 6Y26
PD2 6LL42
6JJ,KK 21 47Ω 1W, 10%
6KK,LL 21 47Ω 1W, 10%
6CC20 47Ω 1W, 10%
6AA,BB 21 47Ω 1W, 10%
6NN21 47Ω 1W, 10%
6VW8 47Ω 1W, 10%
6DD,EE20 47Ω 1W, 10%
6QQ21 47Ω 1W, 10%
5J6-B 1J2-2 DS1 PROG
5J6-a 1J2-3 DS2 PROG 2
5J6-22 1J2-4 DS3 PROG 3
5J6-21 1J2-7 DS4 PROG 4
5J6-C 1J2-10 DS5 READ CK
5J6-20 1J2-13 DS6 HOPPER
5J6-23 1J2-6 DS7 FEED CK
5J6-f 1J2-5 DS8 REVERIFY
DR80 · B · 104
B 3282-12 3281-12 2438-2C
A 3282-11 3281-11 1781-3C
- 3282-09 3281-09 1665-1C
REV COMP ASM ARTWORK ECN APPRO-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION
DRN 12/7/71
ENG
CHK
MFG
TITLE: STATUS DISPLAY, PART 1
DWG NO. C 7688
DASH NO. 00
SH 1 OF 1

C 7689



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	3282-12	3281-12	2438-2C	73
A	3282-11	3281-11	1781-3C	5-30-73
-	3282-09	3281-09	1665-1C	12-5-72

REVISIONS

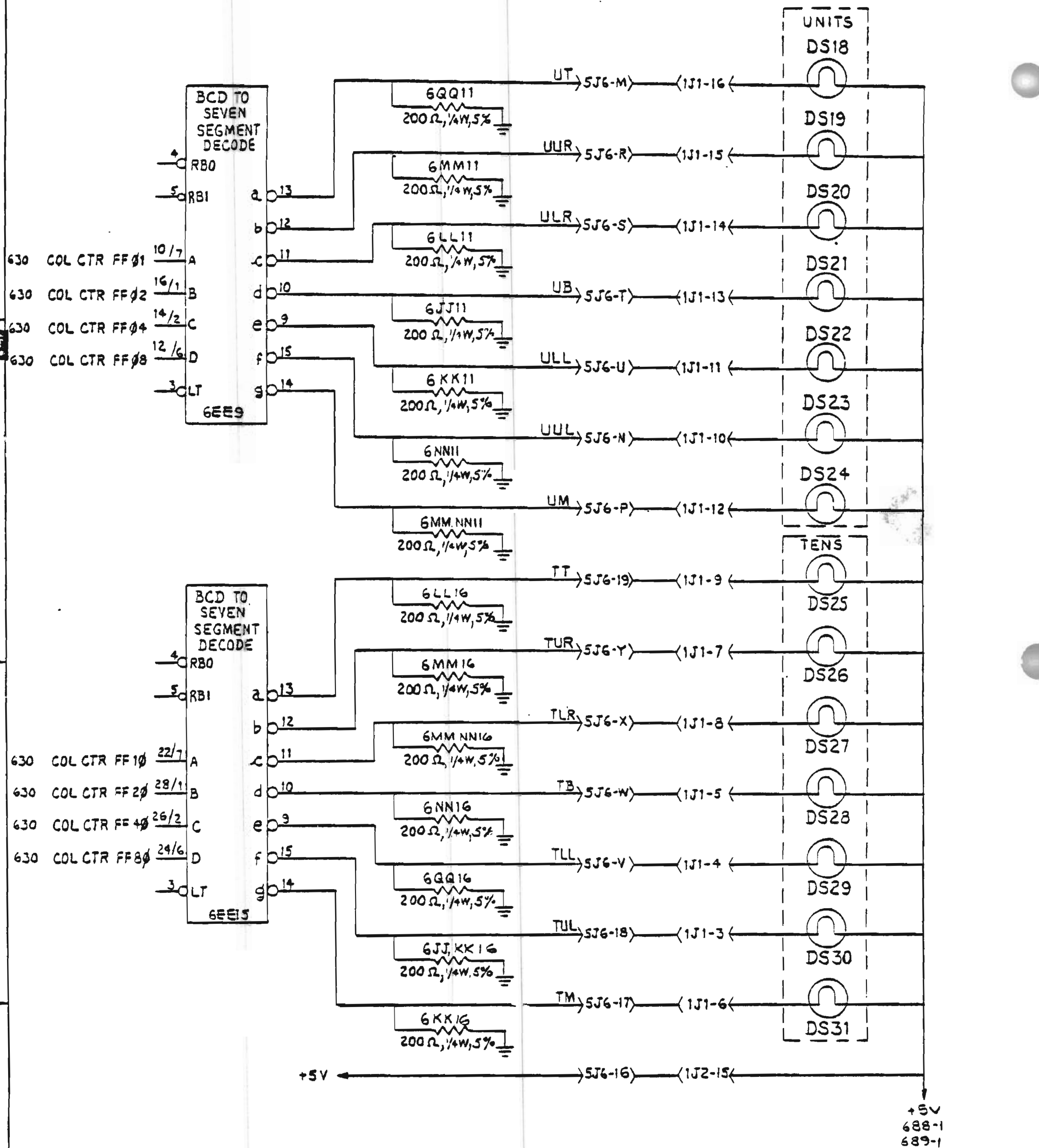
DR80 - B - 105

DRN 12/7/71 ENG
CHK MFG

TITLE STATUS DISPLAY, PART 2

DWG NO. C 7689 DASH NO. 00 SH 1 OF 1

C 7690



A	3282-12	3281-12	2438-2C	
-	3282-09	3281-09	1665-1C	12-6-72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

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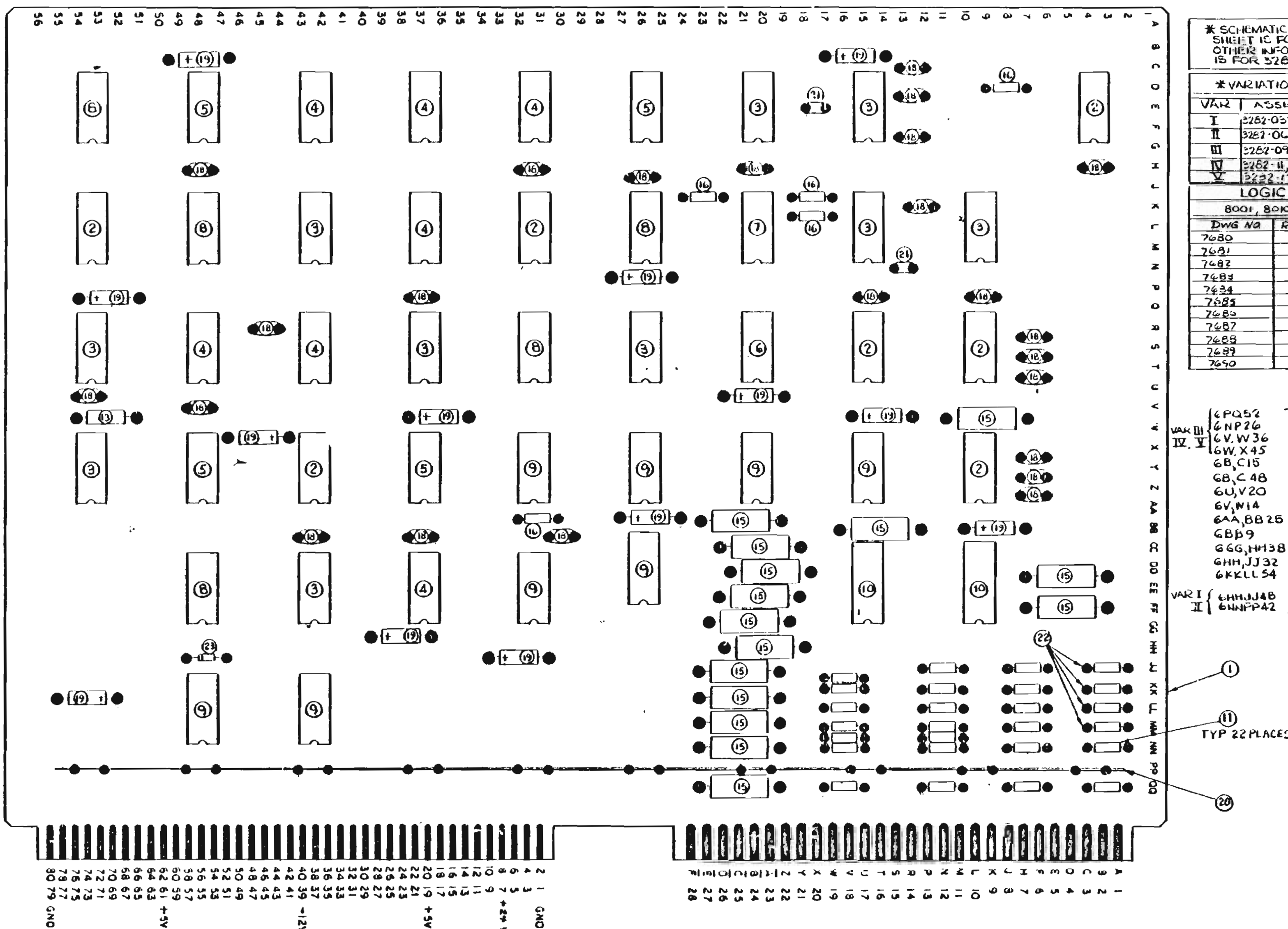
DR80-B-106

DRN 12/7/71 ENG
CHK 11/14/72 MFG

TITLE COLUMN DISPLAY

DWG NO. C 7690 DASH NO. 00 SH 1 OF 1

REV	DESCRIPTION	APPROVALS
S	ADDED THIS SHT/ECN 3332	[illegible]
T	REVISED/ECN 3572-1C	[illegible]
U	REVISED/ECN 3715-1C	[illegible]



✱ SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHT IS FOR 3282-12 ON 3281-12

✱ VARIATION REFERENCE

VAR	ASSEMBLY	ARTWORK
I	3282-05 THRU -11	3281-03
II	3282-06 THRU -11	3281-06
III	3282-09 THRU -11	3281-09
IV	3282-11,12	3281-11
V	3282-12	3281-12

LOGIC DIAGRAM CHART

8001, 8010 DWG NO	REV	9601, 9610, 9660 DWG NO	REV	960110,60-03 DWG NO	REV
7680	C	3400	T	11463	-
7681	B	8401	R	11464	-
7682	A	8402	M	11465	-
7683	A	8403	L	11466	-
7684	A	8404	K	11467	-
7685	B	8405	L	11468	-
7686	B	8406	M	11469	-
7687	B	8407	M	11470	-
7688	B	8408	M	11471	-
7689	B	8409	L	11472	-
7690	A	8410	J	11473	-

SPARE GATES

TYPE	LOC	OUTPUT PIN NO.
6/1 INPUT	E4	6,8
2/4 EXPANDER	E20	11

FOR LIST OF PARTS SEE DETACHED PARTS LIST

COMPANY CONFIDENTIAL INFORMATION

DR80 - B - 107

COMPONENT ASSEMBLY

MODE CONTROL

D 3282

-1

DR80- B- 108

LOGIC/SCHEMATIC CONTENTS

PCB LOC 7

TYPE NUMBER/NAME BASIC 8001, 8010 INTERFACE

DWG NO	TITLE	PCB/ASM DRAWING DASH NO	
7489	PWB	05	05
7490	PROGRAM MEMORY (8) F-1042	05	05
7492	PROGRAM MEMORY (4)	06	06

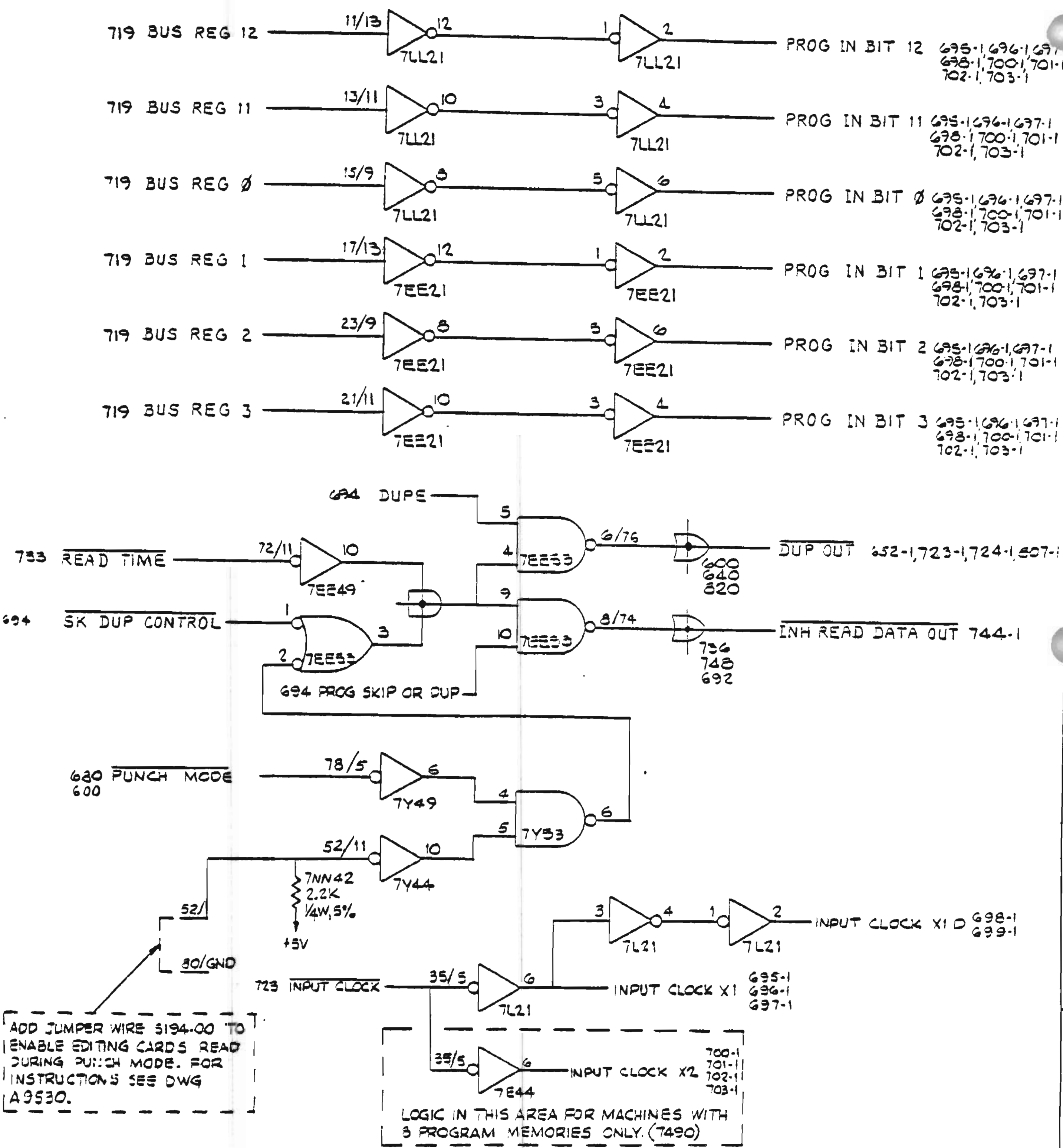
LOGIC SCHEMATIC	LOGIC DRAWING TITLE SCHEMATIC SHEET NO	LOGIC/SCHEMATIC DRAWING REVISION	
7691	PROGRAM LOAD PT-1	C	C
7692	" " PT-2	C	C
7693	" " PT-3	A	A
7694	PROGRAM CONTROL	D	D
7695	PROGRAM MEMORY 1	B	B
7696	" " 2	B	B
7697	" " 3	B	B
7698	" " 4 PT-1	B	B
7699	" " 4 PT-2	A	A
7700	" " 5	B	B
7701	" " 6	B	B
7702	" " 7	B	B
7703	" " 8	B	B
7704	PROGRAM OUTPUTS	B	B
7705	PROGRAM 4, AUXILIARY DUP	A	A
7706	PROGRAM SELECT HOLDING REGISTER	E	E
7707	PROGRAM SELECT DECODE	D	D
7708	PROGRAM INDICATOR DECODE	A	B
7490	Sheet 5	K	K
7492	Sheet 5	L	L

REVISION OF CONTENTS	K/L	K/L
DATE OF ECN	28 FEB 75	28 MAY 75
APPROVAL		
ENGINEERING CHANGE NOTICE	3175-IC	3939-IC

DWG NO 7490 7492 REV — SHT OF —

DR80-B-109

C 7691



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	7490-05 7492-06	7489-05	2706-2C	
B	7490-04 7492-05	7489-04	2161-2C	
A	7490-04 7492-05	7489-03	2161-1P	
-	7490-00 7492-00	7489-00	1622-2C	

REVISIONS

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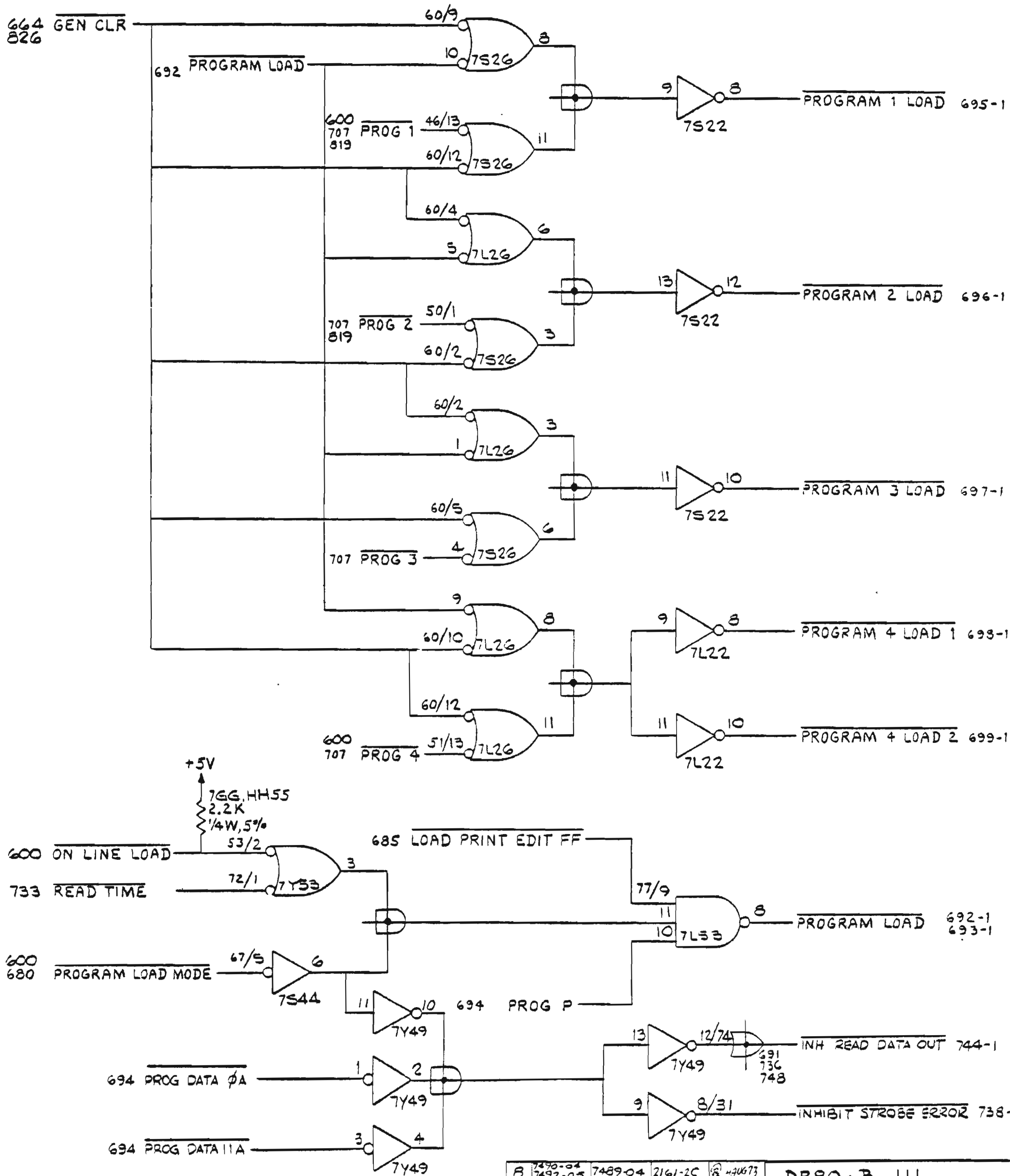
DR80- B- 110

TITLE: PROGRAM LOAD, PART 1

DWG NO. C 7691

DASH NO -00

SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	7490-05 7492-06	7489-05	2706-2C	
B	7490-04 7492-05	7489-04	2161-2C	
A	7490-05 7490-04	7489-03	2161-1P	N.D. 6-15-73
—	7490-00 7492-00	7489-00	1622-2C	11-30-72

REVISIONS

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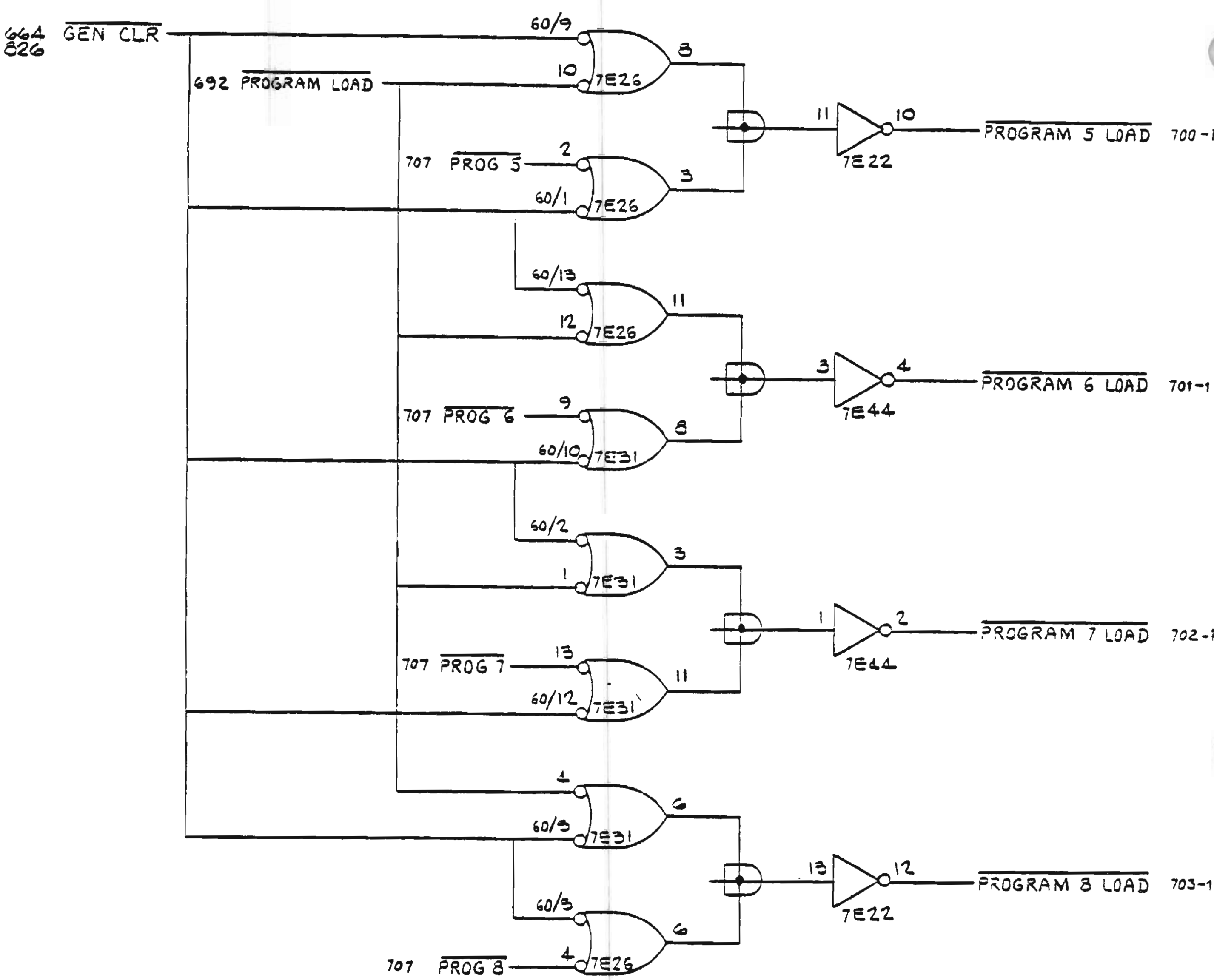
DR80-B-111

DRN J. ALESI 10/9/72
CHK 11-22-72

TITLE PROGRAM LOAD, PART 2

DWG NO. C 7692 DASH NO -00 SH 1 OF 1



LOGIC ON THIS SHEET FOR MACHINES WITH
8 PROGRAM MEMORIES ONLY. (7490).

C 7693

DR80-B-112

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	17490-05	17489-05	2706-2C	
-	17490-00	17489-00	1622-2C	11-10-72

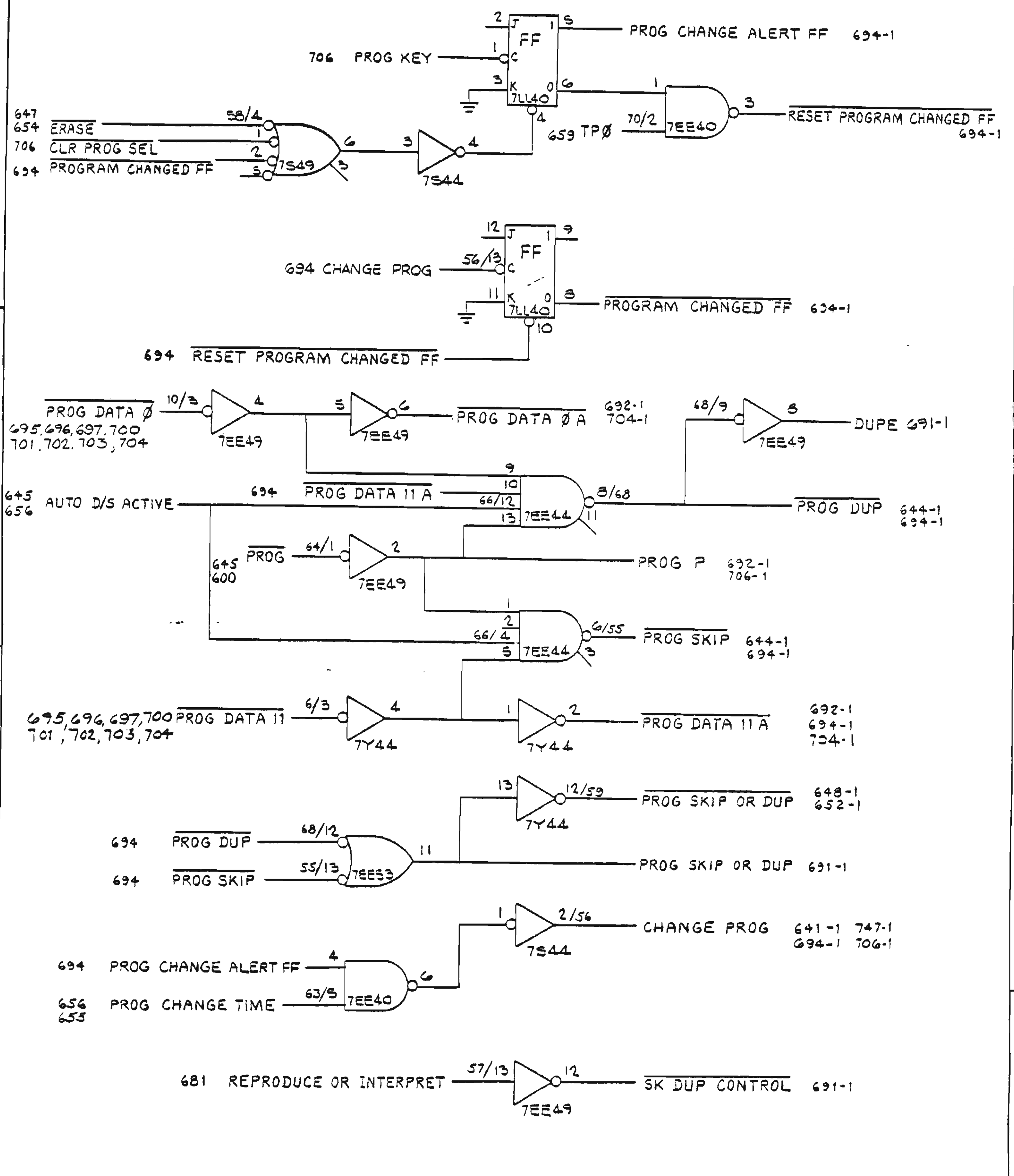
REVISIONS

DRN J. ALESI 9/9/72

TITLE: PROGRAM LOAD, PART 3

DWG NO. C 7693 DASH NO. -00 SH 1 OF 1

C 7694



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
D	7490-05 7492-06	7489-05	2706-2C	2-8-74
C	7490-05 7492-06	7489-04	2706-1P	2-11-73
B	7490-04 7492-05	7489-04	2161-1P 2160-1P	6-14-73
A	7490-02 7492-02	7489-02	1686-3C	2-23-73
-	7490-00 7492-00	7489-00	1622-2C	11-30-72

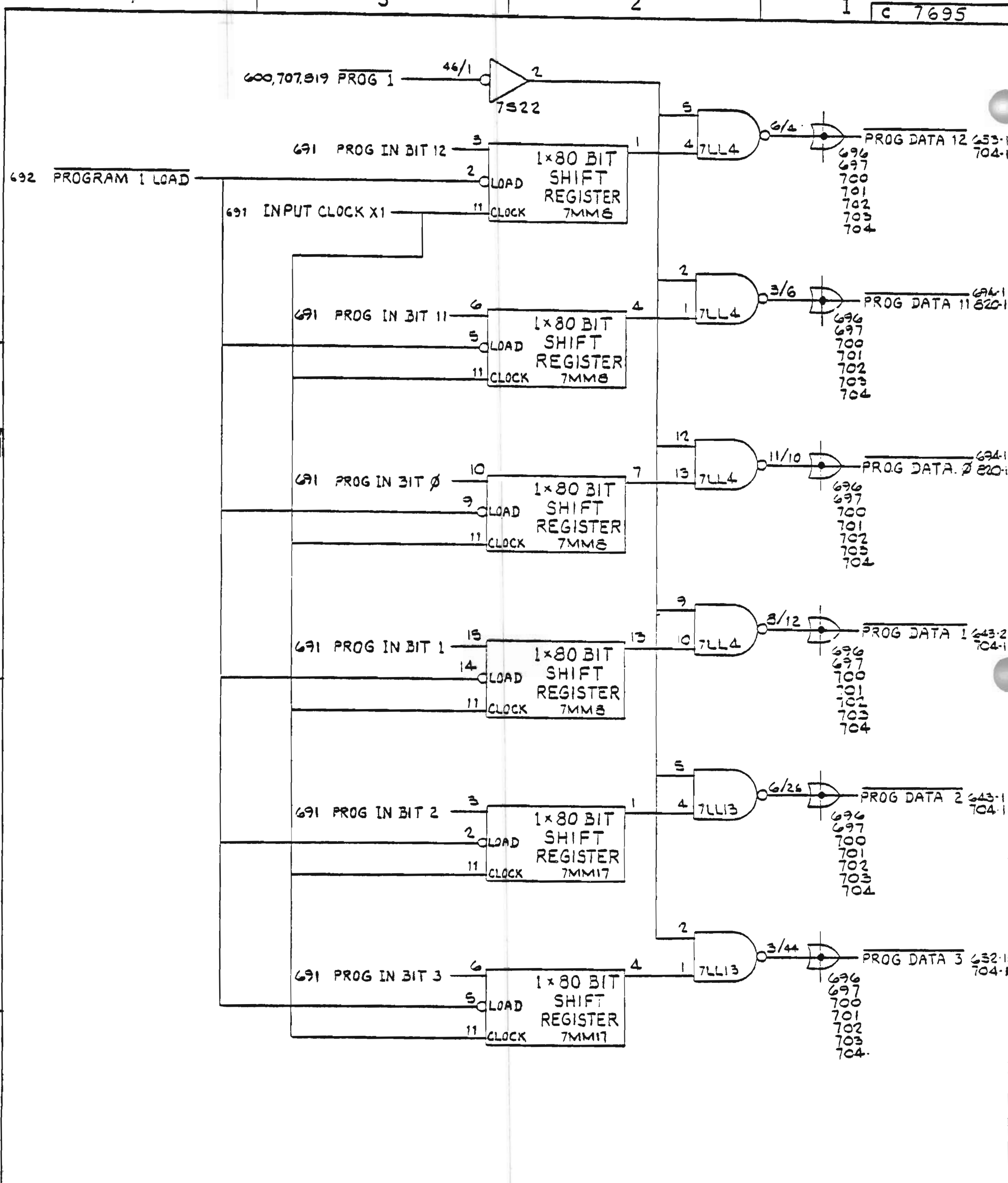
REVISIONS

DR87-B-113

DRN J. ALESI 10/9/72 ENG
CHK 11-27-72 MFG

TITLE PROGRAM CONTROL

DWG NO. C 7694 DASH NO. -00 SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	7490-05 7492-06	7489-05	2706-2C	
A	7490-00 7492-00	7489-00	2681-2P	1 Nov 73
–	7490-00 7492-00	7489-00	1622-2C	-72

REVISIONS

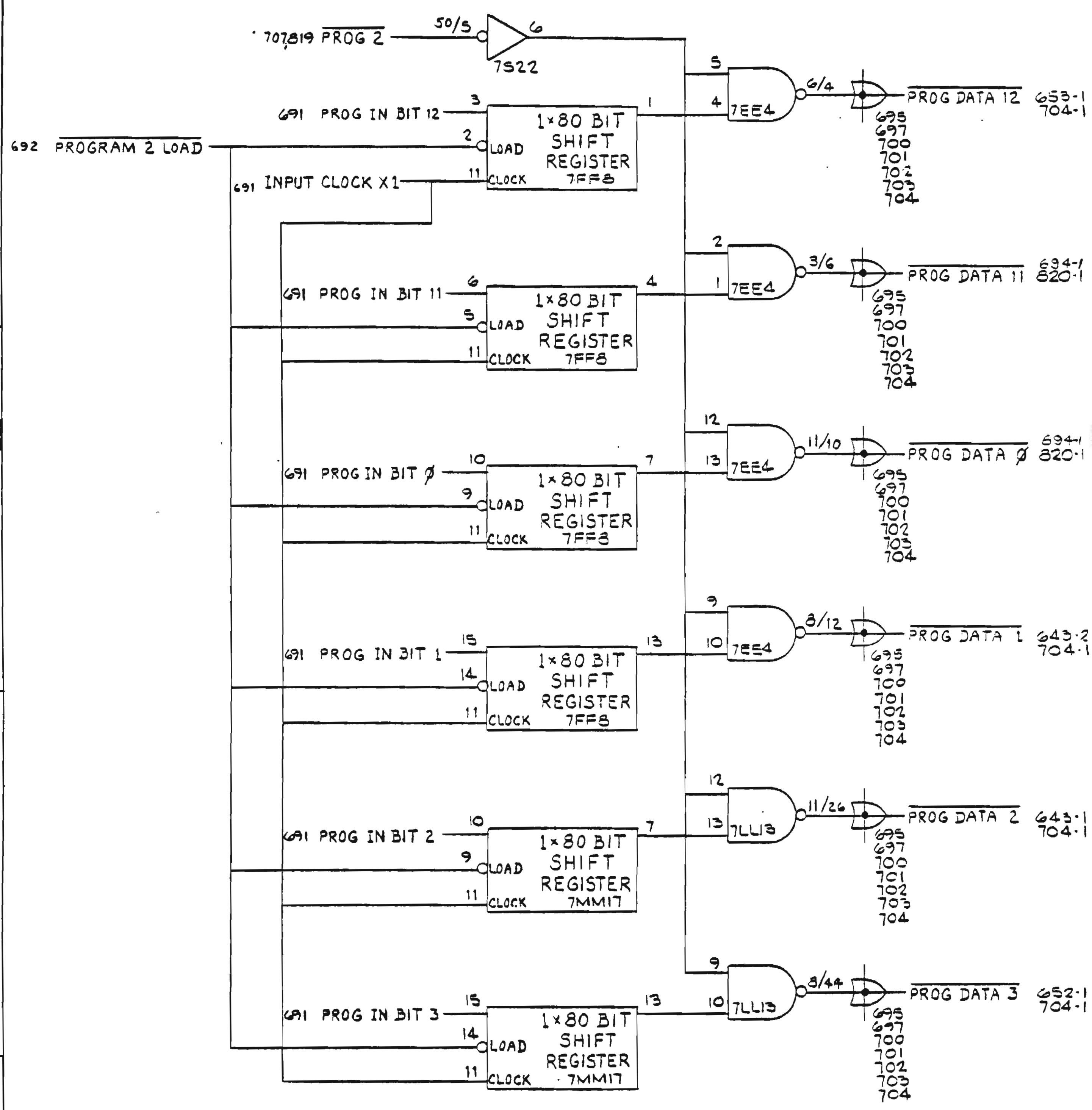
DR80-B-114

DRN J. ALESI 10/9/72 ENG
CHK 11-22-72 MFG

TITLE PROGRAM MEMORY 1

DWG NO. C 7695 DASH NO -00 SH 1 OF 1

C 7696



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	7490-05 7492-06	7489-05	2706-2C	4 FEB 74
A	7490-00 7492-00	7489-00	2681-2P	12 DEC 73
–	7490-00 7492-00	7489-00	1622-2C	1-24-72

REVISIONS

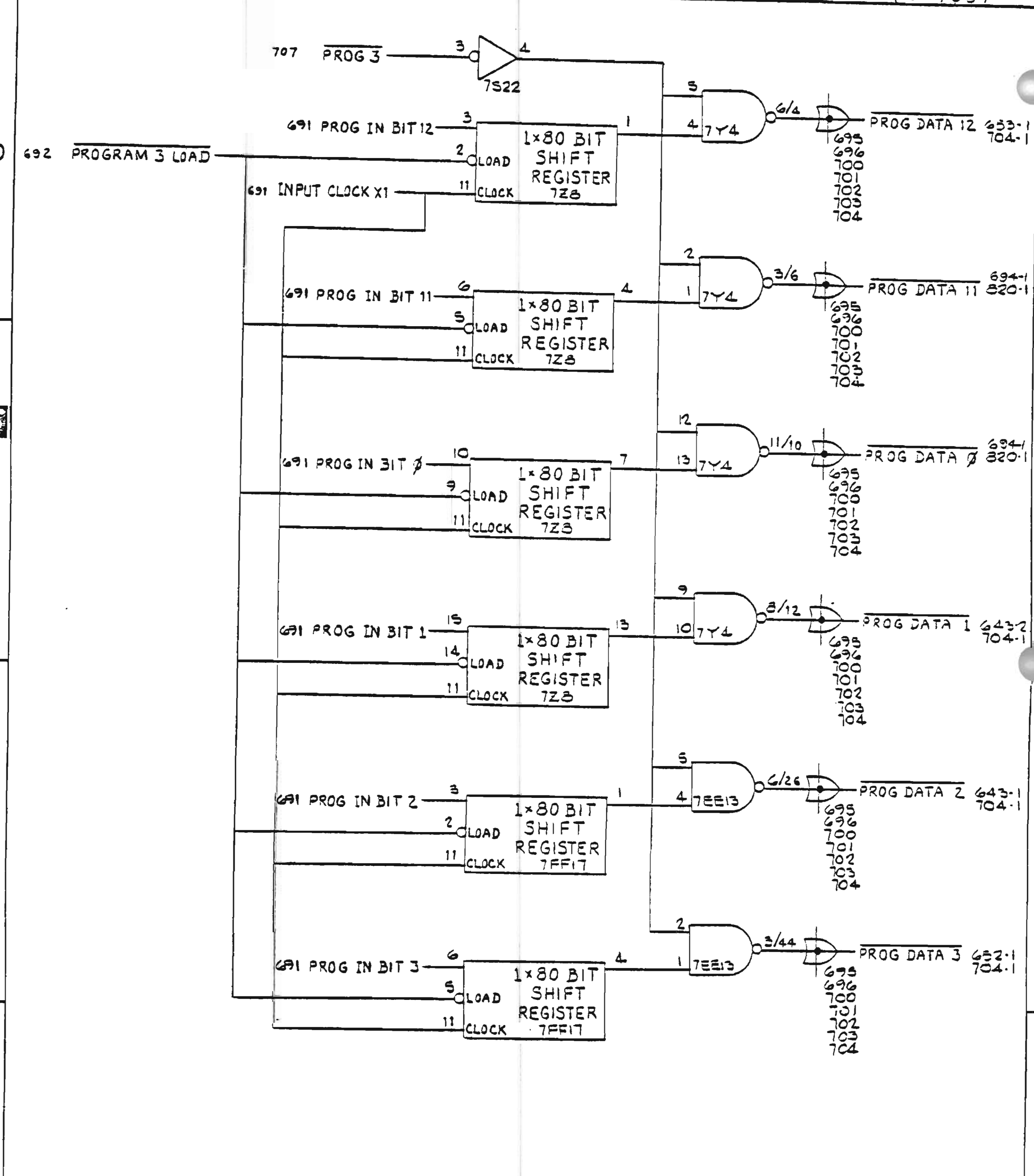
DR80-B-115

DRN J. ALESI 10/9/72 ENG F. SHEPPARD
CHK 11-22-72 MFG

TITLE PROGRAM MEMORY 2

DWG NO. C 7696 DASH NO. -00 SH 1 OF 1

C 7697



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	7490-05 7492-06	7489-05	2706-2C	
A	7490-00 7492-00	7489-00	2681-2P	
-	7490-00 7492-00	7489-00	1622-2C	

REVISIONS

DR 80 - B - 116

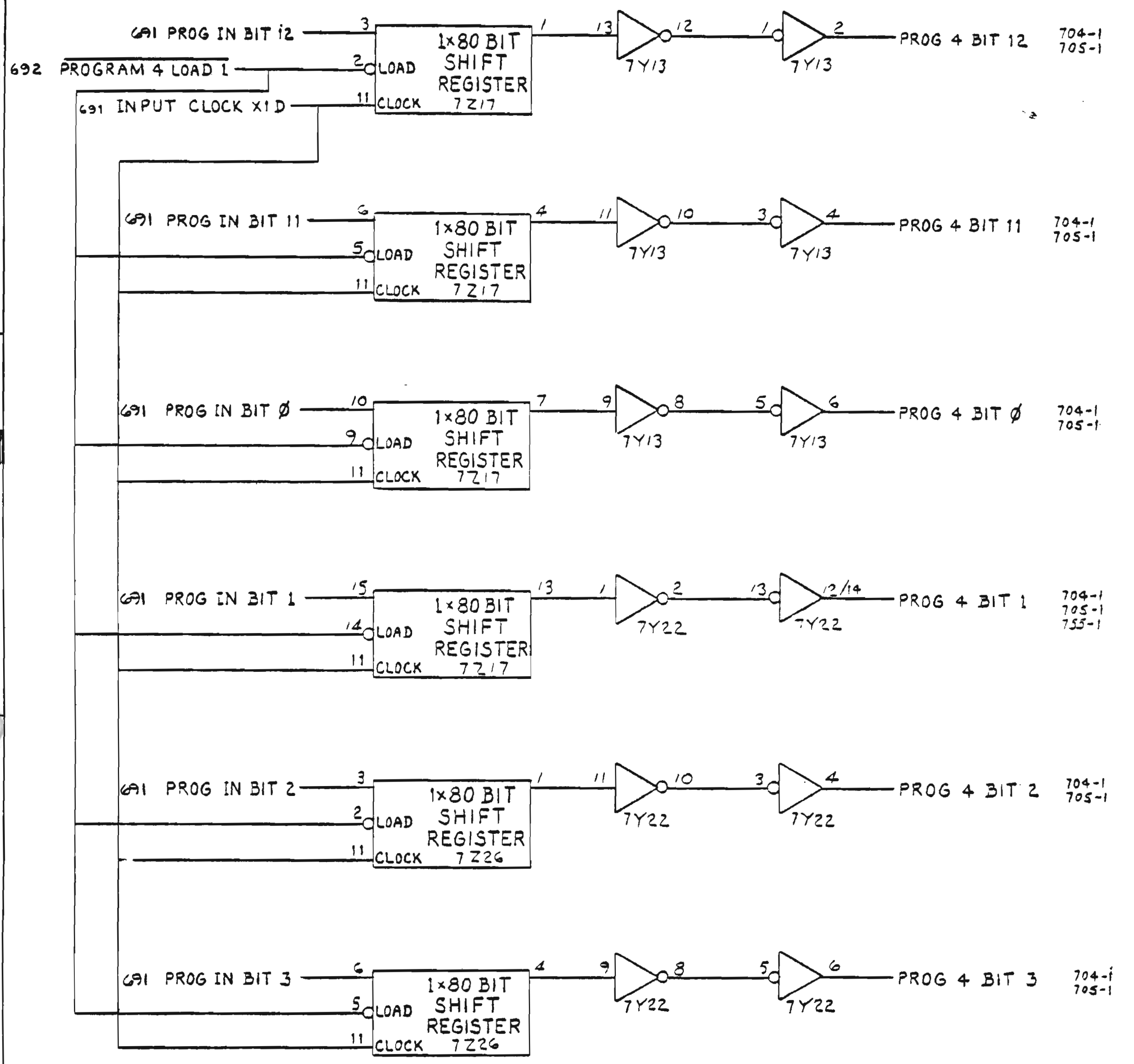
DRN J. ALESI 10/9/72

CHK 10-22-72

TITLE PROGRAM MEMORY 3

DWG NO. C 7697 DASH NO. -00 SH 1 OF 1

C 7698



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	7490-05 7492-06	7489-05	3175-1C	[illegible]
A	7490-05 7492-06	7489-05	2706-2C	[illegible]
-	7490-00 7492-00	7489-00	1622-2C	[illegible] 1-30-73

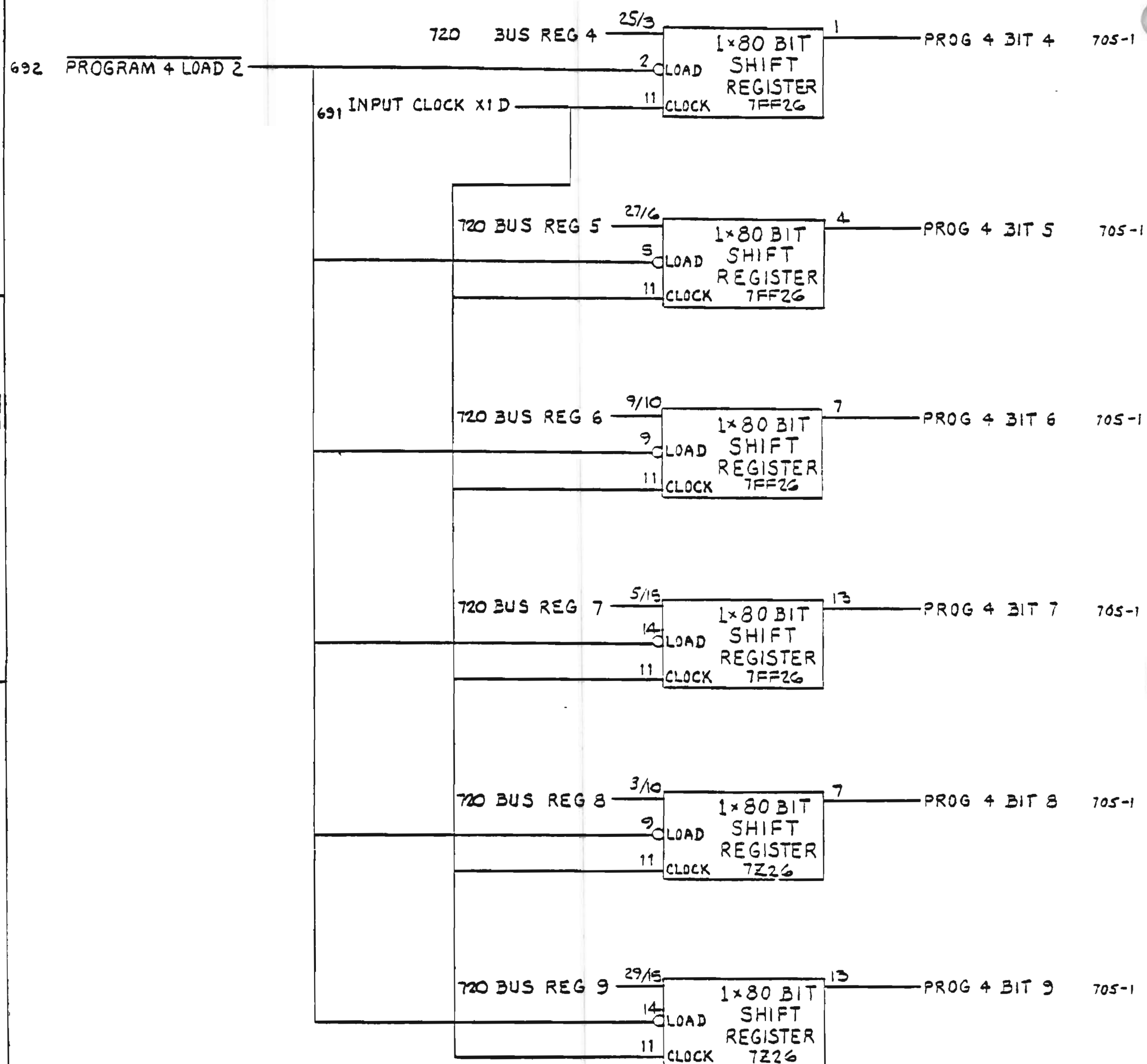
REVISIONS

DR80-B-117

DRN J. ALESI 10/9/72 ENG [illegible]
CHK [illegible] 11-22-72 MFG [illegible]

TITLE PROGRAM MEMORY 4, PART 1

DWG NO. C 7698 DASH NO. -00 SH 1 OF 1



DR80- B- 118

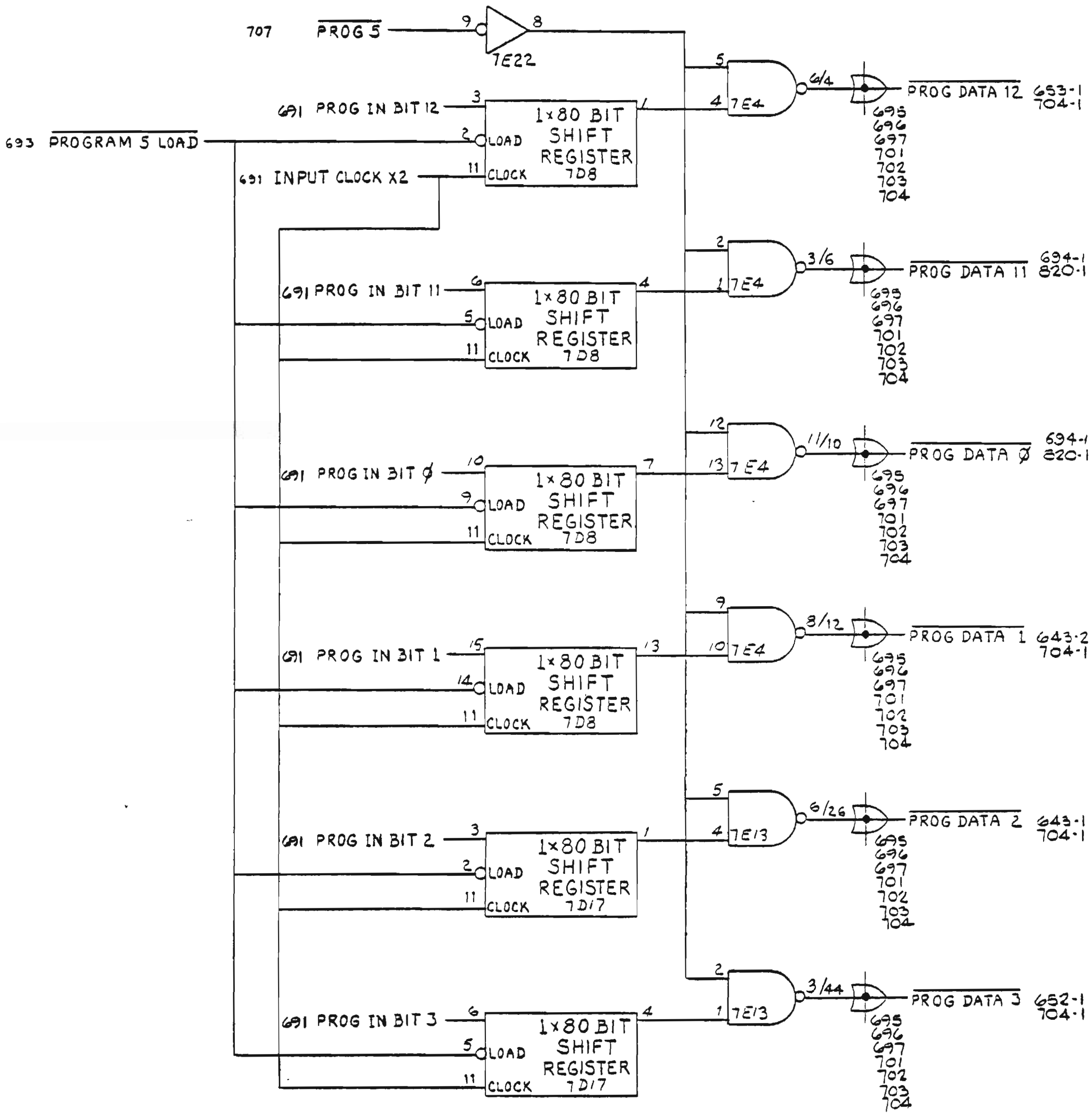
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	7490-05 7492-06	7489-05	2706-2C	
—	7490-00 7492-00	7489-00	1622-2C	11-20-72

REVISIONS

DRN J. ALESI 10/9/72
CHK 11-22-72

TITLE PROGRAM MEMORY 4 PART 2

DWG NO. C 7699 DASH NO. -00 SH 1 OF 1



LOGIC ON THIS SHEET FOR MACHINES WITH
8 PROGRAM MEMORIES ONLY. (7490)

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	7490-05	7489-05	2706-2C	4-4-74
A	7490-00	7489-00	2681-2P	12-4-73
-	7490-00	7489-00	1622-2C	11-30-72

REVISIONS

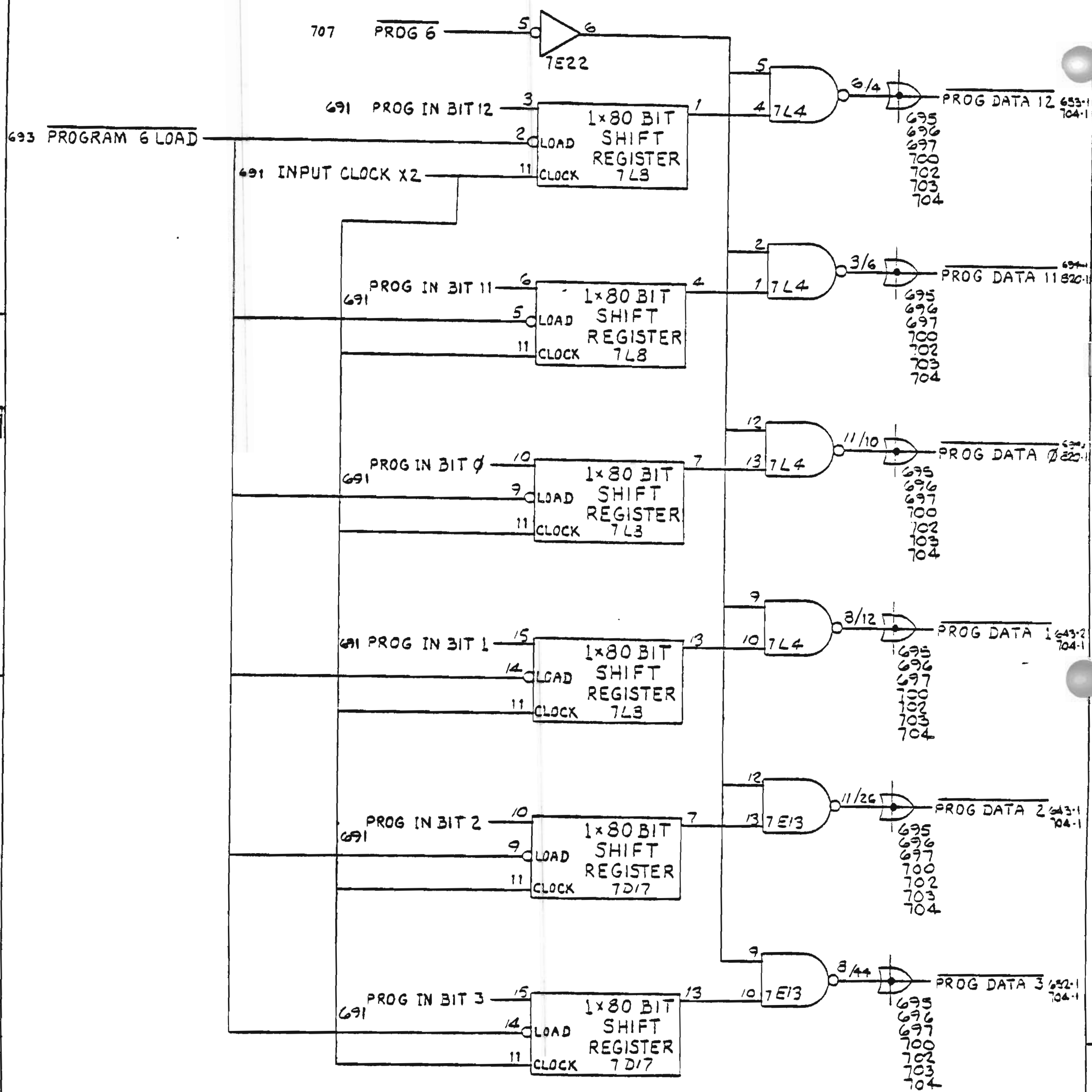
DR80 · B · 119

DRN J. ALESI 10/10/72 ENG
CHK 11-22-72 MFG

TITLE PROGRAM MEMORY 5

DWG NO. C 7700 DASH NO -00 SH 1 OF 1

C 7701



LOGIC ON THIS SHEET FOR MACHINES WITH
8 PROGRAM MEMORIES ONLY. (7490).

DR80- B- 120

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	7490-05	7489-05	2706-2C	
A	7490-00	7489-00	2681-2P	
-	7490-00	7489-00	1622-2C	11-30-72

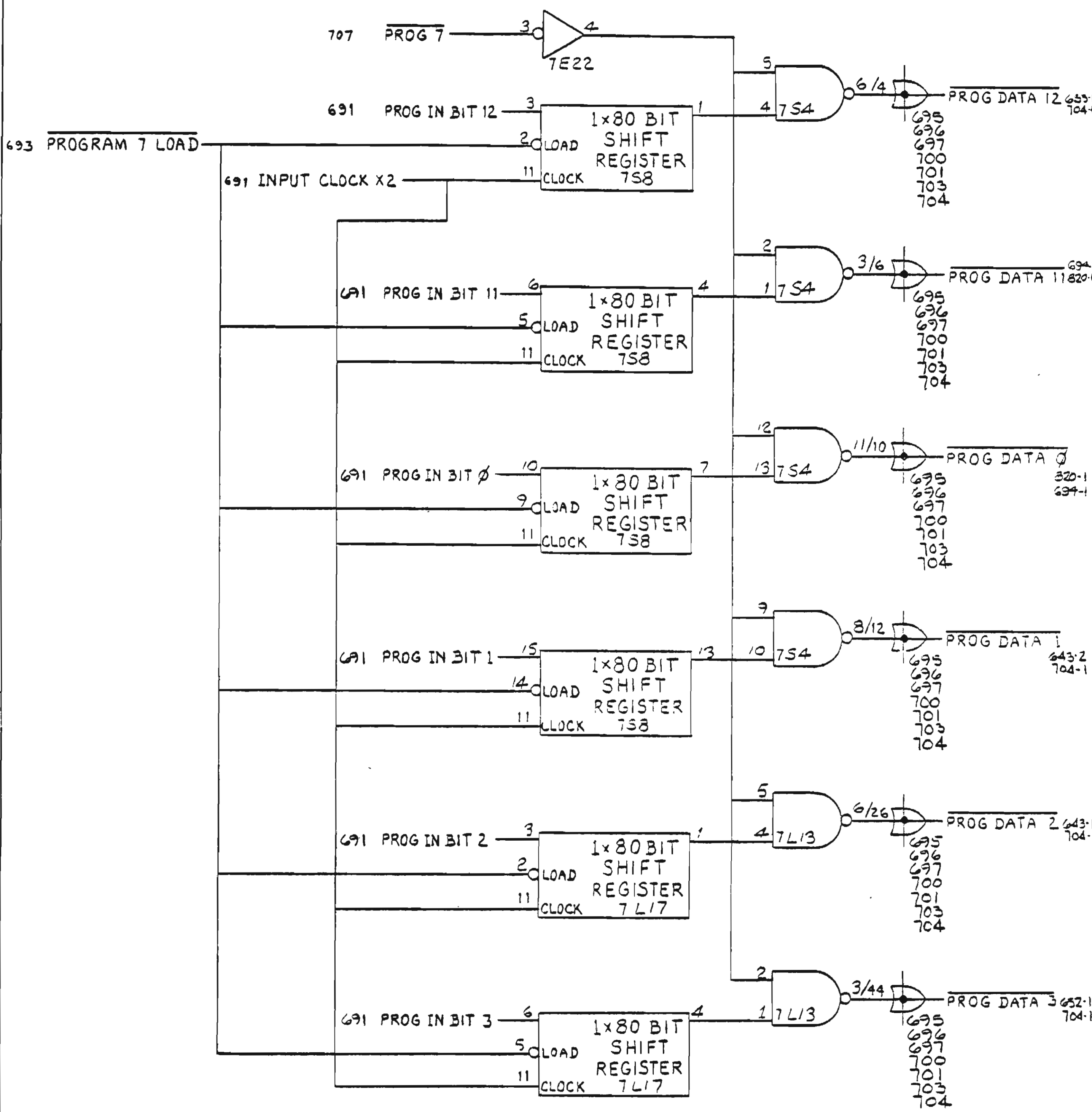
REVISIONS

DRN J. ALESI 10/10/72

TITLE PROGRAM MEMORY 6

DWG NO. C 7701 — DASH NO. -00 — SH 1 OF 1

C 7702



LOGIC ON THIS SHEET FOR MACHINES WITH
8 PROGRAM MEMORIES ONLY. (7490).

DR80 - B - 121

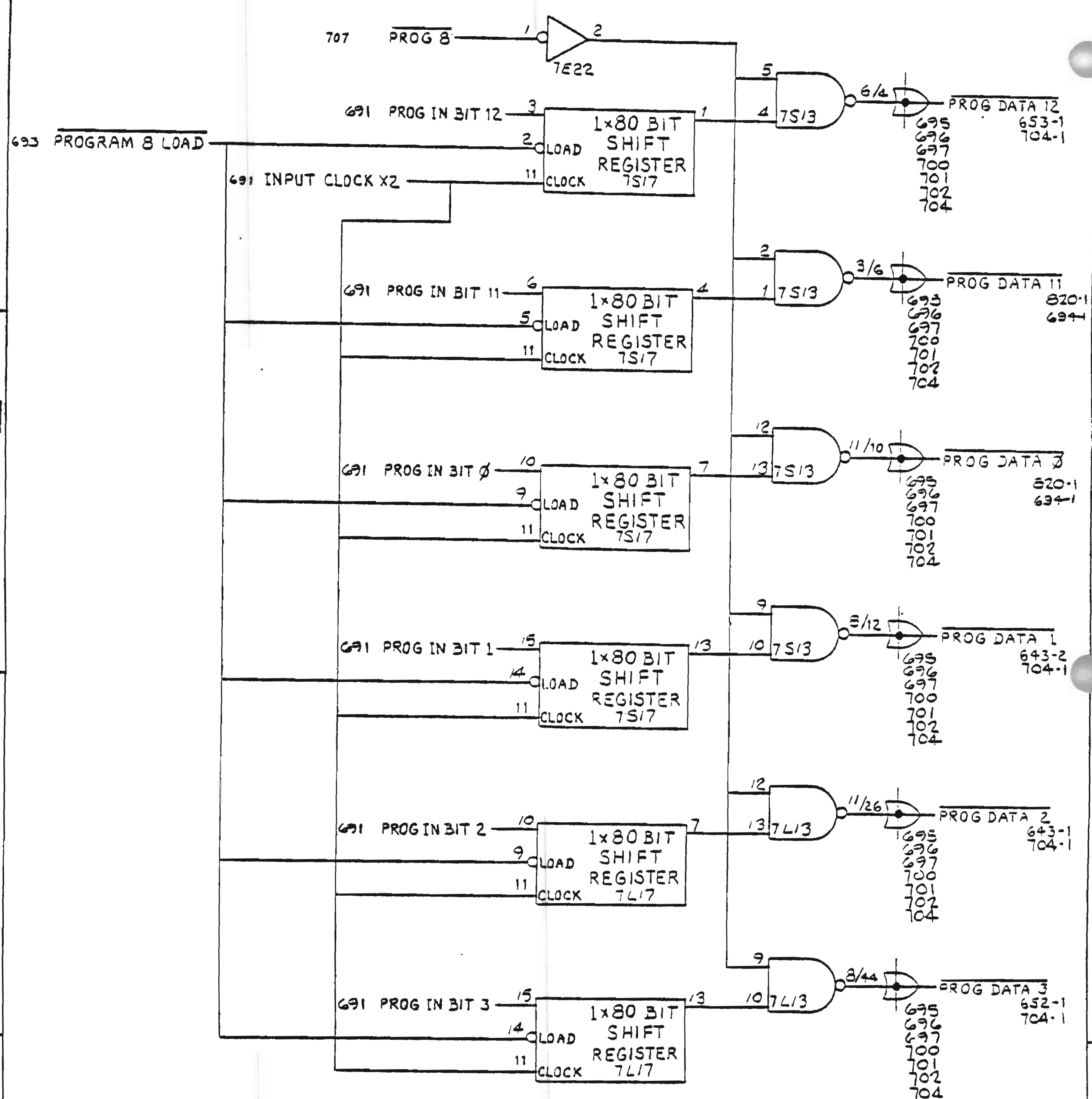
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	7490-05	7489-05	2706-2C	4-03-74
A	7490-00	7489-00	2681-2P	12-03-73
-	7490-00	7489-00	1622-2C	11-30-72

REVISIONS

DRN J. ALESI 10/10/72
CHK 11-22-72

TITLE PROGRAM MEMORY 7

DWG NO. C 7702 DASH NO. -00 SH 1 OF 1



LOGIC ON THIS SHEET FOR MACHINES WITH
8 PROGRAM MEMORIES ONLY. (7490).

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
3	7490-05	7489-05	12706-2C	12-6-74
A	7490-0017489	0012681-2P		12 JAN 73
-	7490-0017489	001622-2C		11-29-72

REVISIONS

DR80 - B - 122

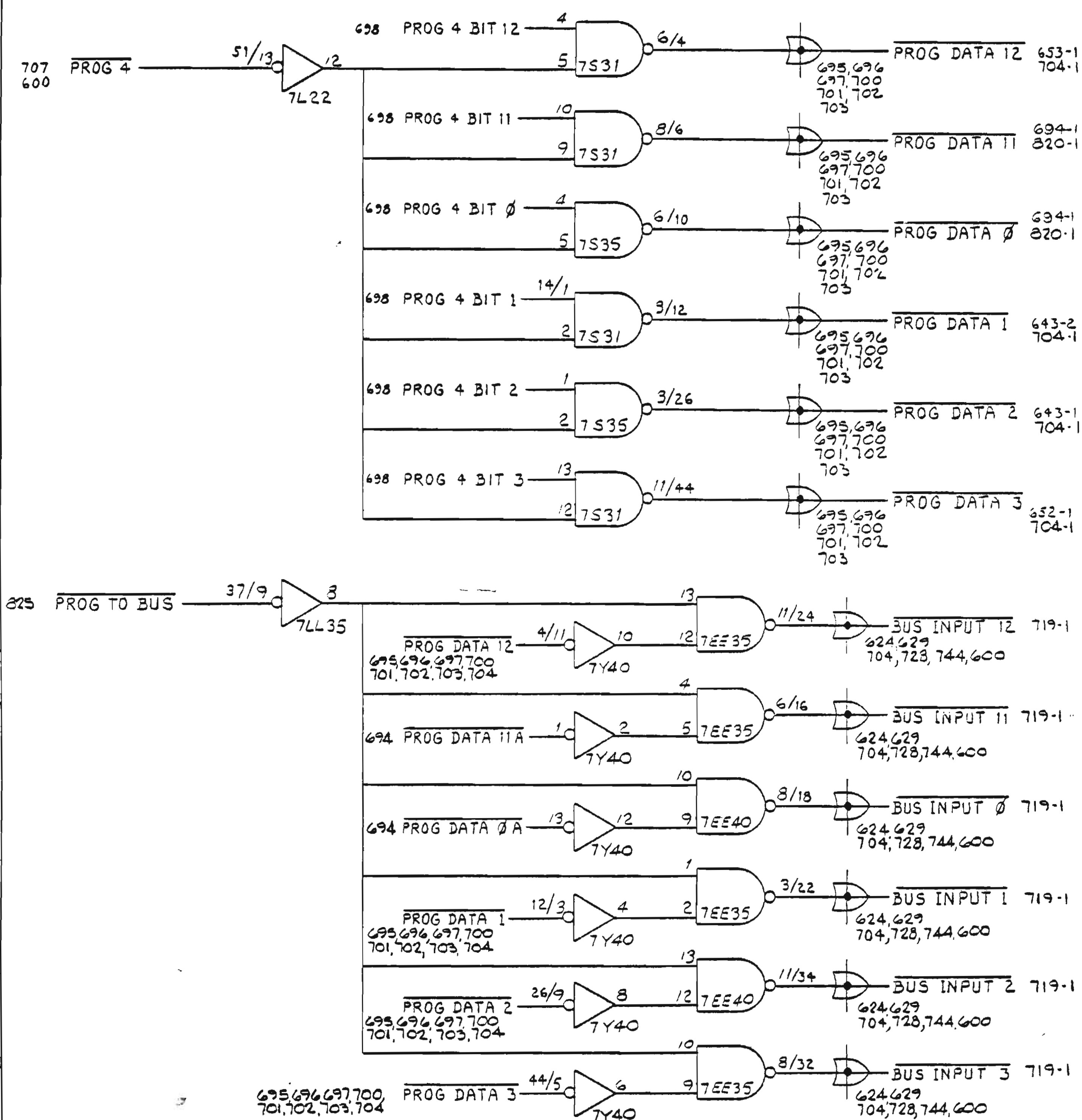
DRN J. ALESI 10/10/72 ENG
CHK 11-22-72 MFG

TITLE PROGRAM MEMORY 8

DWG NO. C 7703 DASH NO. -00

SH 1 OF 1

C 7704



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	7490-05 7492-06	7489-05	2706-2C	4/16/74
A	7490-00 7492-00	7489-00	2681-2P	12 Nov 73
-	7490-00 7492-00	7489-00	1622-2C	11-30-72

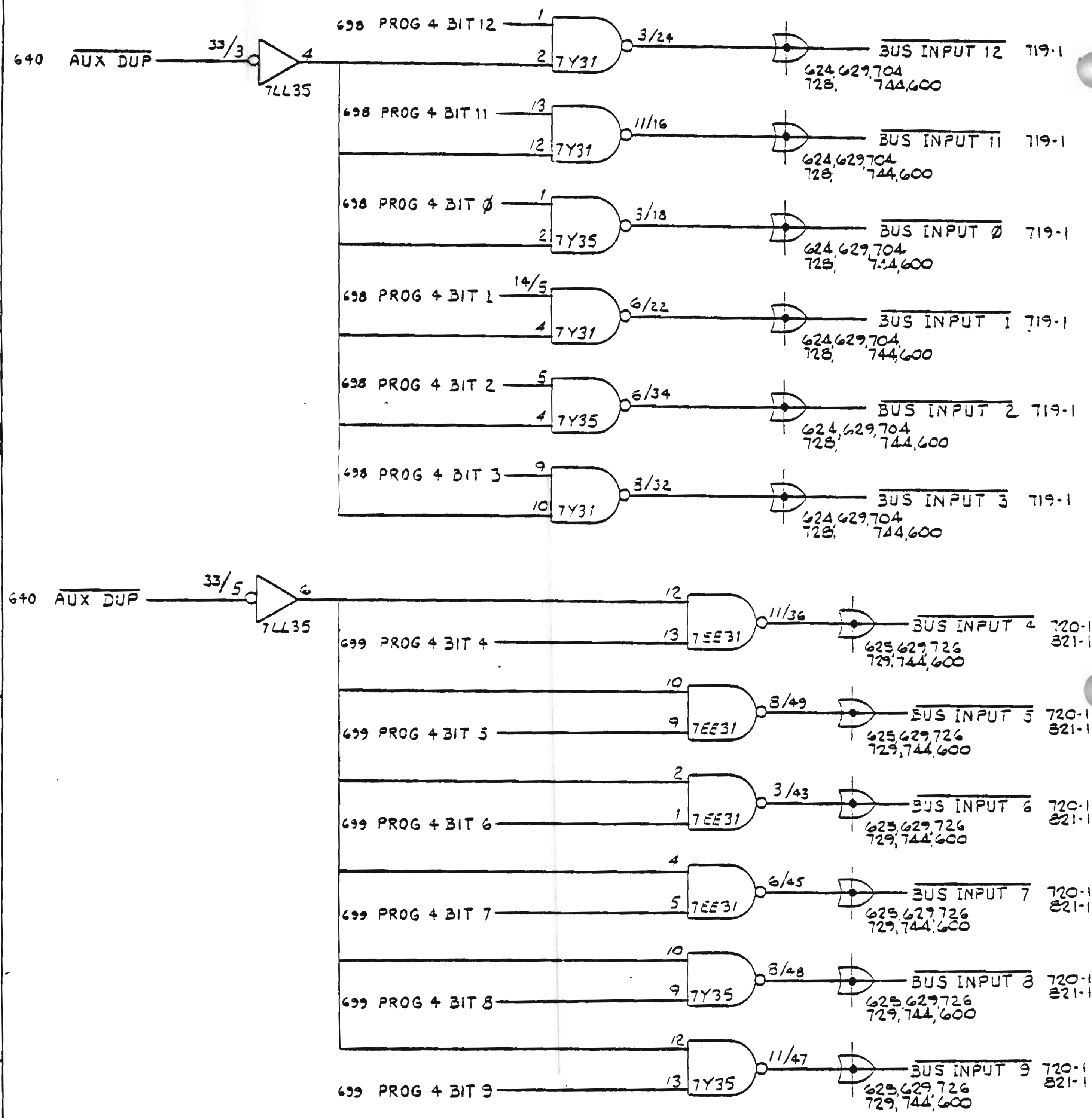
REVISIONS

DR80-B-123

DRN J. ALESI 10/10/72 ENG
CHK 11-22-72 MFG

TITLE PROGRAM OUTPUTS

DWG NO. C 7704 DASH NO. -00 SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	7490-05 7492-06	7489-05	2708-2C	4 Feb 74
-	7490-00 7492-00	7489-00	1622-2C	11-30-72

REVISIONS

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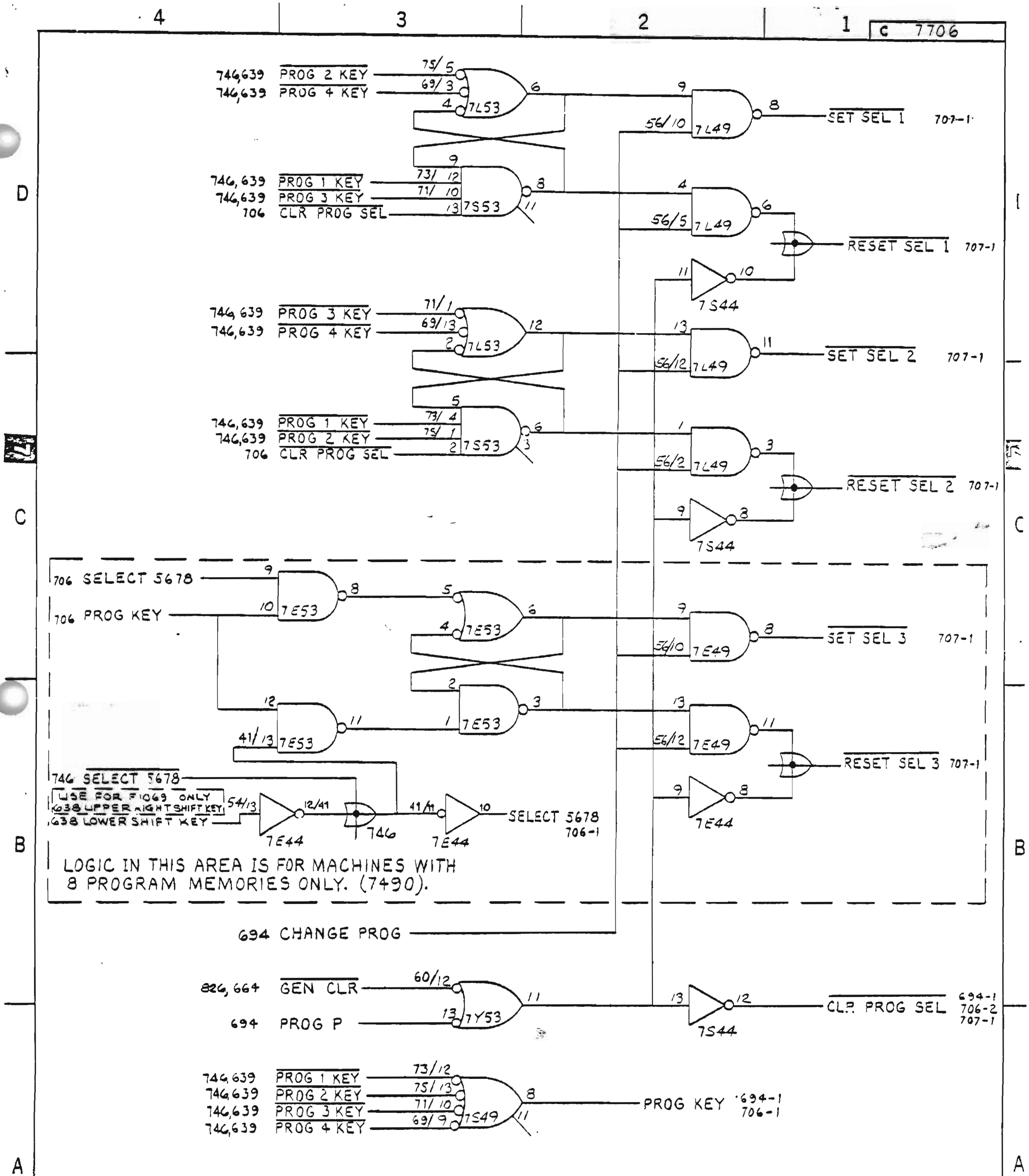
DR80 - B - 124

DRN J. ALESI 10/11/72 ENG

CHK 11-22-72 MFG

TITLE PROGRAM 4, AUXILIARY DUP

DWG NO. C 7705 DASH NO. -00 SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
E	7490-05 7492-06	7489-05	3330-1C	2-5-74
D	7490-05 7492-06	7489-05	2706-2C	[illegible]
C	7490-04 7492-05	7489-04	2161-2C	[illegible]
B	7490-02 7492-02	7489-02	1686-3C	2-13-73
A	7490-01 7492-01	7489-00	1686-1P	12-18-72
—	7490-00 7492-00	7489-00	1622-2C	11-30-72

REVISIONS

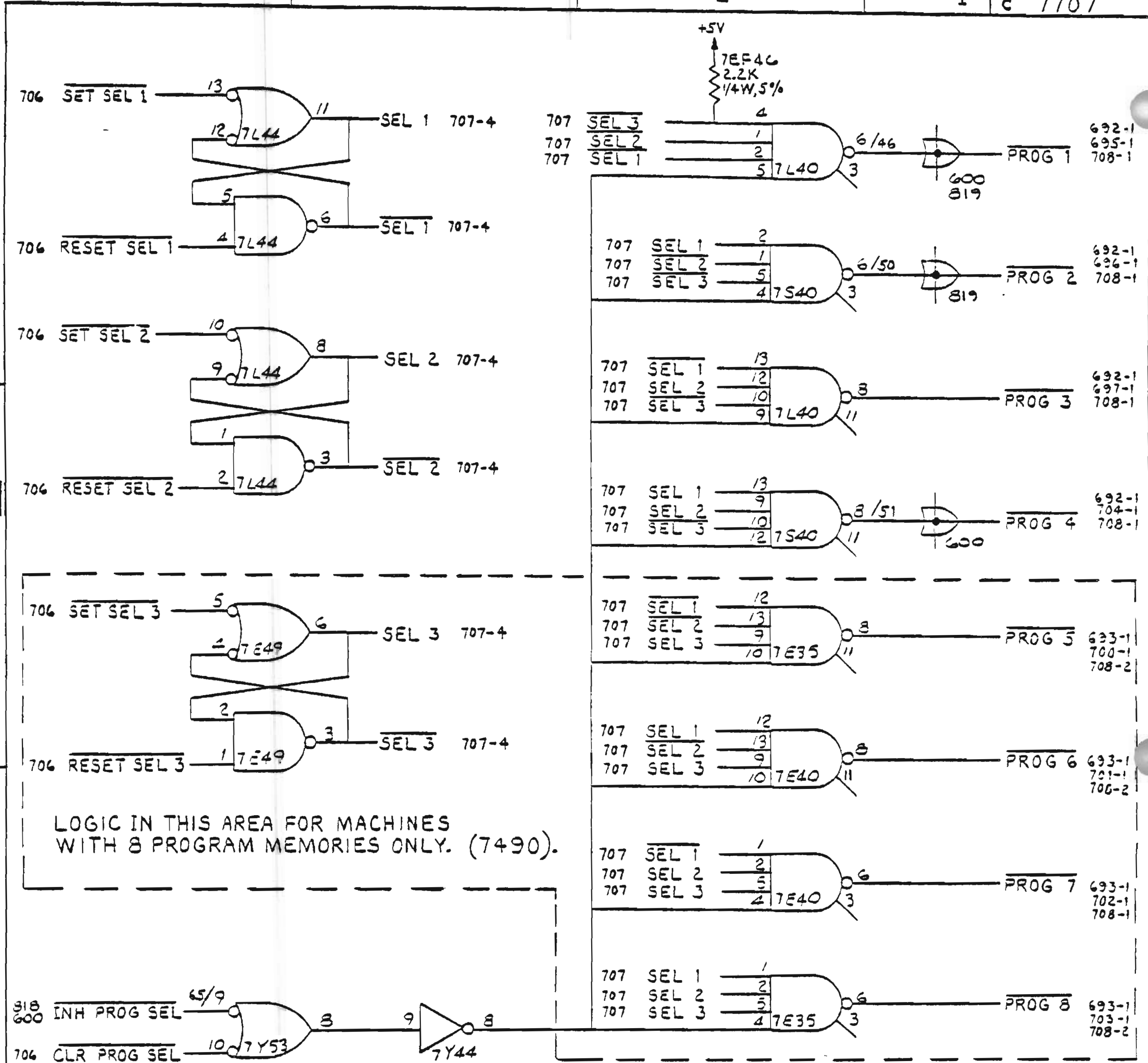
DR80- B- 125

DRN J. ALESI 10/11/72 ENG
CHK 11-22-72 MFG

TITLE PROGRAM SELECT HOLDING REGISTER

DWG NO. C 7706 DASH NO. -00 SH 1 OF 1

C 7707



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	7490-02 7492-02	7489-02	1686-3C	2-23-73
–	7490-00 7492-00	7489-00	1622-2C	11-30-72
D	7490-05 7492-06	7489-05	2706-2C	
C	7490-04 7492-05	7489-04	2161-2C	
B	7490-04 7492-05	7489-03	2161-1P	6-14-73

REVISIONS

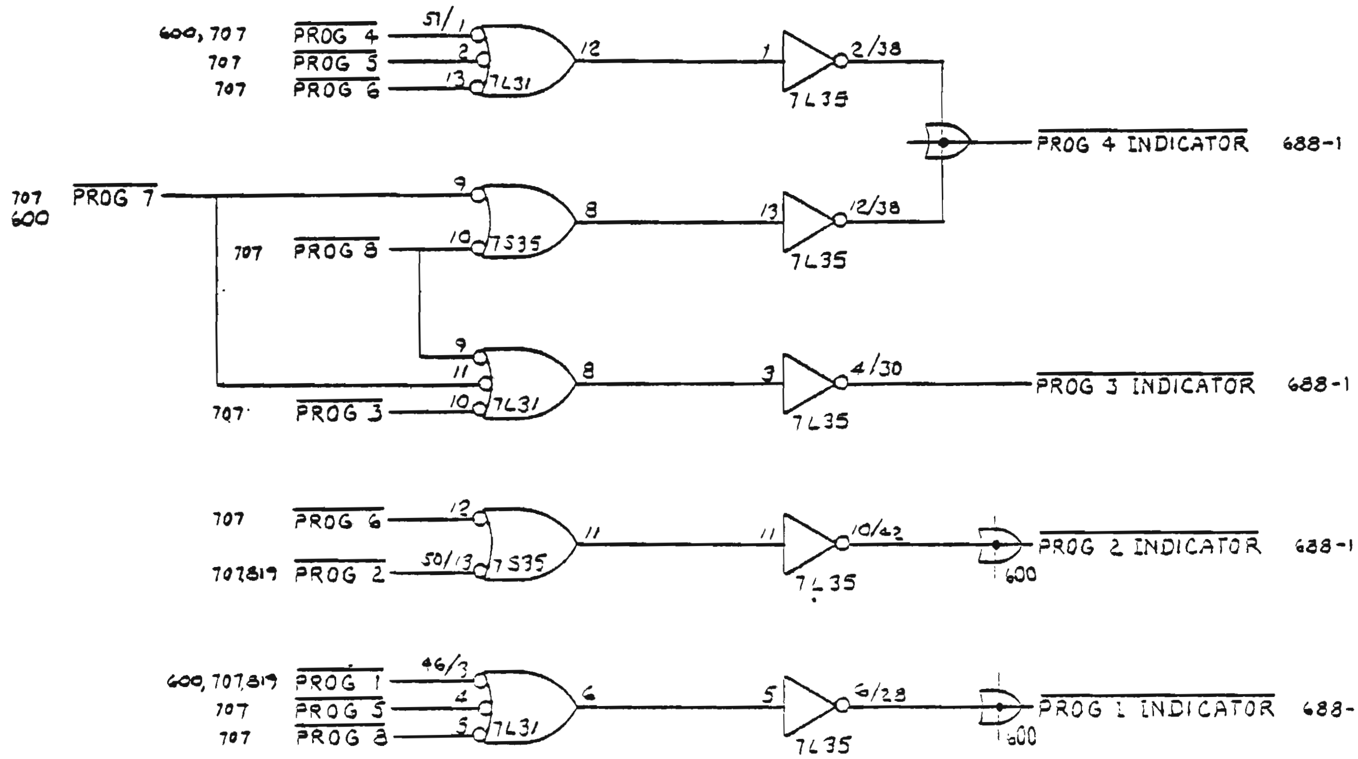
DR80-B-126

DRN J. ALESI 10/11/72 ENG
CHK 11-22-72 MFG

TITLE PROGRAM SELECT DECODE

DWG NO. C 7707 DASH NO. -00 SH 1 OF 1

C 7708



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	7490-05 7492-02	7489-05	3939-1C	
A	7490-05 7492-06	7489-05	2706-2C	
-	7490-00 7492-00	7489-00	1622-2C	11-30-72

REVISIONS

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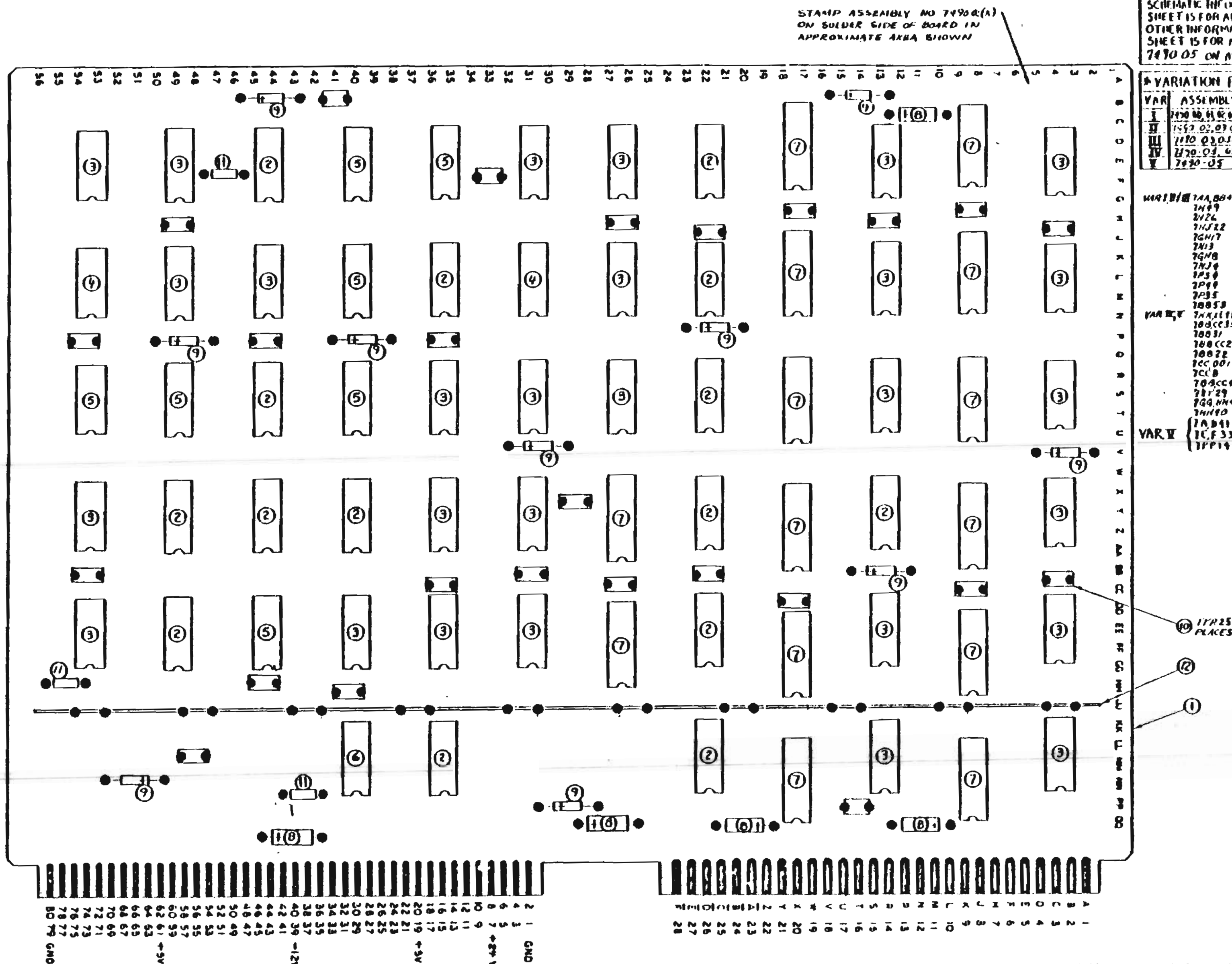
DR80 - B - 127

DRN J. ALESI 10/11/72 ENG

CHK 11-22-72 MFG

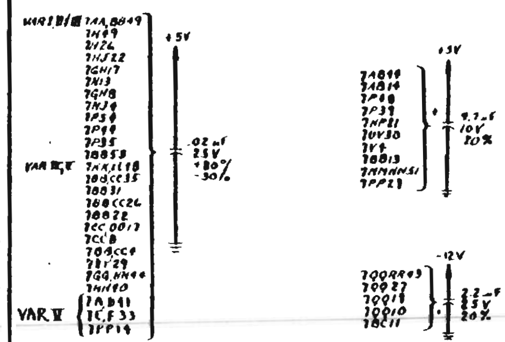
TITLE PROGRAM INDICATOR DECODE

DWG NO. C 7708 DASH NO. -00 SH 1 OF 1



*UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 7490 05 ON ARTWORK 7489-05

*VARIATION REFERENCE		
VAR	ASSEMBLY	ARTWORK
I	7490 00, 01, 02, 03, 04, 06	7489-00
II	7490 02, 03 04, 05	7489-02
III	7490 02, 03, -05	7489 03
IV	7490-03, 05	7489-04
V	7490-05	7489-05



LOGIC DIAGRAM CHART	
DWG. NO.	REV.
7691	C
7692	C
7693	A
7694	D
7695	B
7696	B
7697	D
7698	A
7699	A
7700	B

LOGIC DIAGRAM CHART	
DWG. NO	REV.
7701	B
7702	B
7703	B
7704	B
7705	A
7706	D
7707	D
7708	A

TYPE	LOC	OUTPUT PIN NO
4/8 INPUT	E812	AU
QUAD 80-BIT S.R.	EF17	13
6/1 INPUT	L35	8
6/1 INPUT	Y44	6
6/1 INPUT	LL35	2, 10, 12

SPARE GATES

FOR LIST OF PARTS SEE DETACHED PARTS LIST

DR80 - B - 128

COMPONENT ASSEMBLY
PROGRAM MEMORY (8)

D 7490

REV K

REV	DESCRIPTION	APPR DATE
L	ADDED THIS SHEET ECN 2722 2C	[illegible]

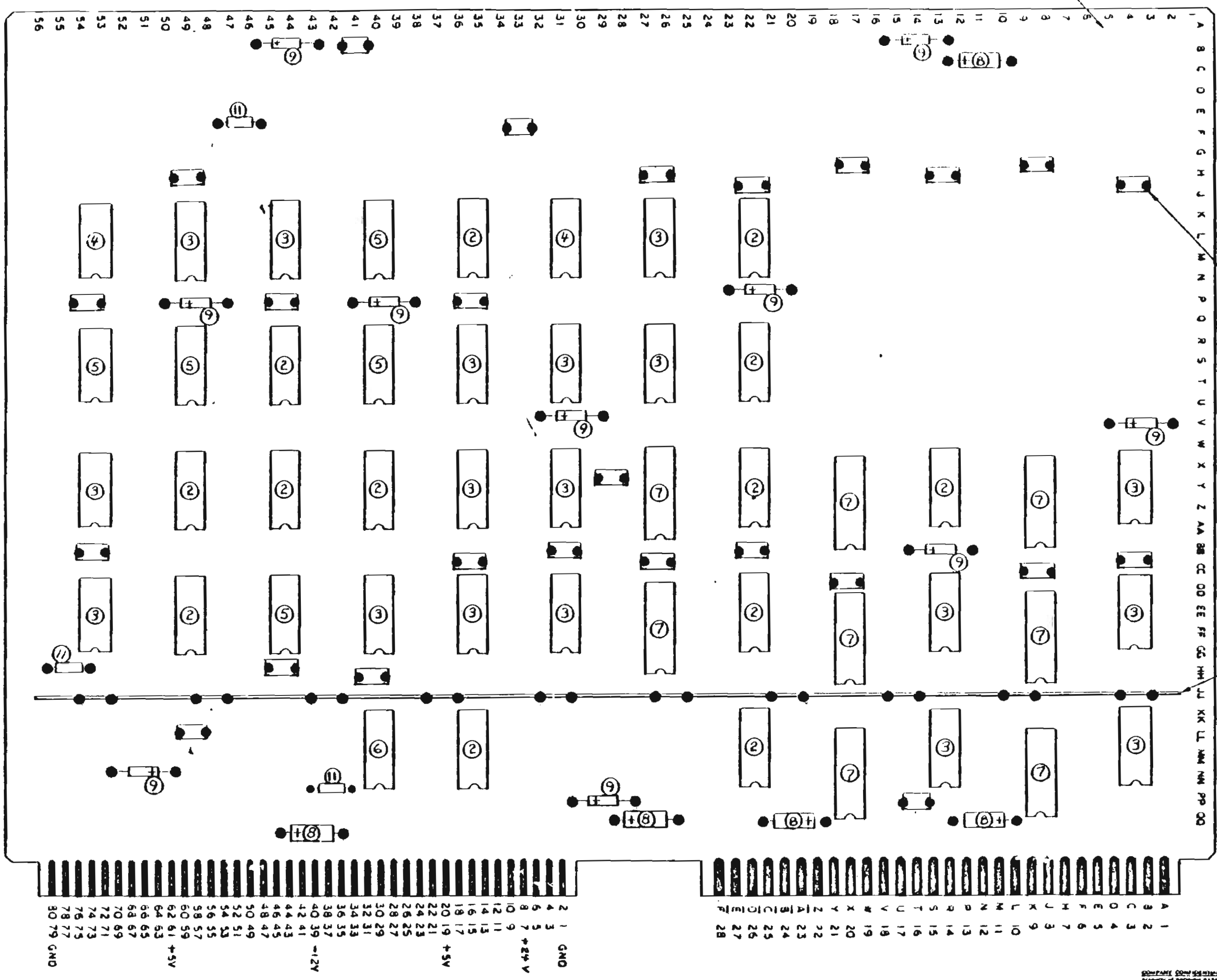
STAMP ASSEMBLY NO 7492-0x(L) ON SOLDER SIDE OF BOARD IN APPROXIMATE AREA SHOWN

+5V: 7AB44, 7AB14, 7P48, 7P39, 7NP21, 7UV30, 7V4, 7BE13, 7MMNN51, 7PP29 — 47 µf 10V 20%

-12V: 7QQRR13, 7QQ27, 7QQ14, 7QQ10, 7BC11 — 2.2 µf 25V 20%

+5V: VAR V {7PP14, 7E,F33, 7A,B41}; VAR I, II, III 7AAB49; 7H49, 7H26, 7HJ22, 7GH17, 7H13, 7GH8, 7HJ4, 7P54, 7P44, 7P35, 7BB53, VAR IV 7KKLL48, 7BBCC35, 7BB31, 7BBCC26, 7BB22, 7CCDD17, 7CC8, 7BBCC4, 7XY29, 7GGHH44, 7HH40 — .02 µf 25V +80% -30%

⑩ TYP 25 PLACES



LOGIC DIAGRAM CHART	
DWG. NO.	REV.
7691	C
7692	C
7694	D
7695	B
7696	B
7697	B
7698	A
7699	A
7704	B
7705	A
7706	D
7707	D
7708	A

*UNLESS OTHERWISE SPECIFIED ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 7492-05 ON ARTWORK 7489-05.

*	VARIATION	REFERENCE
VAR	ASSEMBLY	ARTWORK
I	7492-00,01,02,03,04,05,06	7489-00
II	7492-02,03,04,05,06	7489-02
III	7492-04,05,06	7489-03
IV	7492-05,06	7489-04
V	7492-06	7489-05

TYPE	LOC	OUTPUT PIN NO
4/2 INPUT	EE13	8,11
QUAD 80 BIT S.R	FF17	7,13
6/1 INPUT	L35	8
6/1 INPUT	Y+4	C
6/1 INPUT	L35	2,10,12

SPARE GATES

FOR LIST OF PARTS SEE DETACHED PARTS LIST

COMPANY CONFIDENTIAL INFORMATION

DR80-B-129

COMPONENT ASSEMBLY PROGRAM MEMORY 4

D 7492

7492

LOGIC/SCHEMATIC CONTENTS

PCB LOC 8

TYPE NUMBER/NAME: BASIC 8001, 8010 INTERFACE

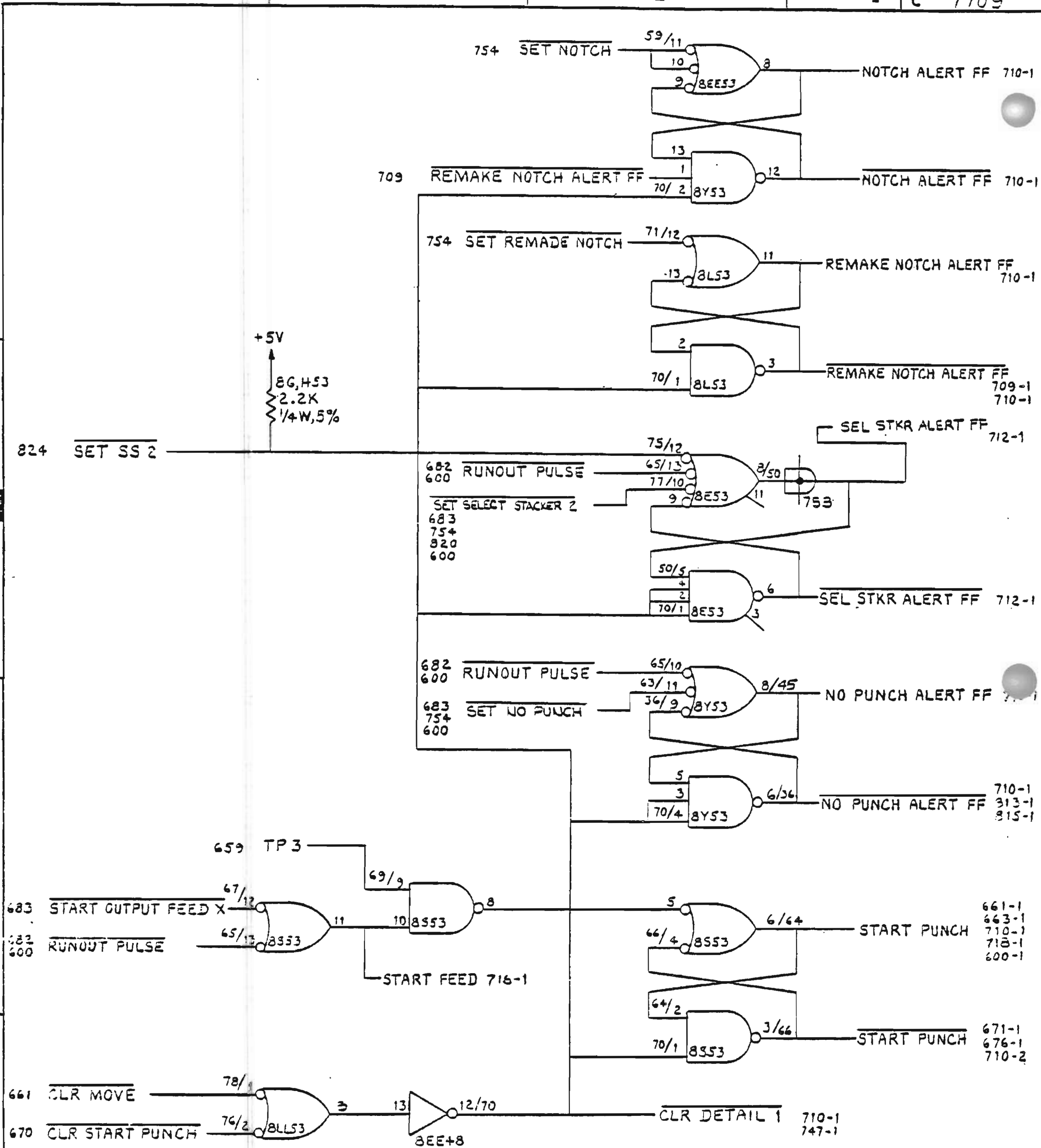
DWG NO	TITLE	PCB/ASM DRAWING DASH NO				
9593	PWB	04	04	05	05	X
9594	MOTION CONTROL PT-2	04	05	05	X	X
12816	MOTION CONTROL PT-2	X	X	X	00	01

LOGIC SCHEMATIC	LOGIC DRAWING TITLE SCHEMATIC SHEET NO	LOGIC/SCHEMATIC DRAWING REVISION				
7 709	OUTPUT FEED DETAIL FF's PT-1	D	D	D	D	E
7 710	OUTPUT FEED DETAIL FF's PT-2	C	C	C	D	E
7 711	NOTCH TIMING CONTROL	E	C	C	C	D
7 712	STACKER CONTROL	E	F	F	F	G
7 713	DRIVE MOTOR CONTROL	G	G	H	H	J
7 714	ACTUATORS	F	F	G	H	J
7 715	HOPPER/STACKER SWITCHES	D	D	D	E	F
7 716	CLICK COIL COUNTER	C	C	D	D	E
7 717	STACKER DRIVE SIGNALS	B	B	B	B	C
7 718 sh1	CARD REGISTRATION	G	H	J	K	L
7 718 sh2	CARD REGISTRATION	F	F	F	F	G
9594	Sheet 2	D	D	D	D	X
9594	Sheet 3	D	D	D	D	X
12816	Sheet 2				X	A
12816	Sheet 3				X	A

REVISION OF CONTENTS	G	H	J	-
DATE OF ECN	16 JAN 75	21 JAN 75	18 APR 75	13 JUN 75
APPROVAL				
ENGINEERING CHANGE NOTICE	3435-2C	3979-1P	3979-2C	4392-1P

DR80-3-131

DWG NO 9594 / 12816 REV — SHT — OF



C 7709

DR 80 B 132

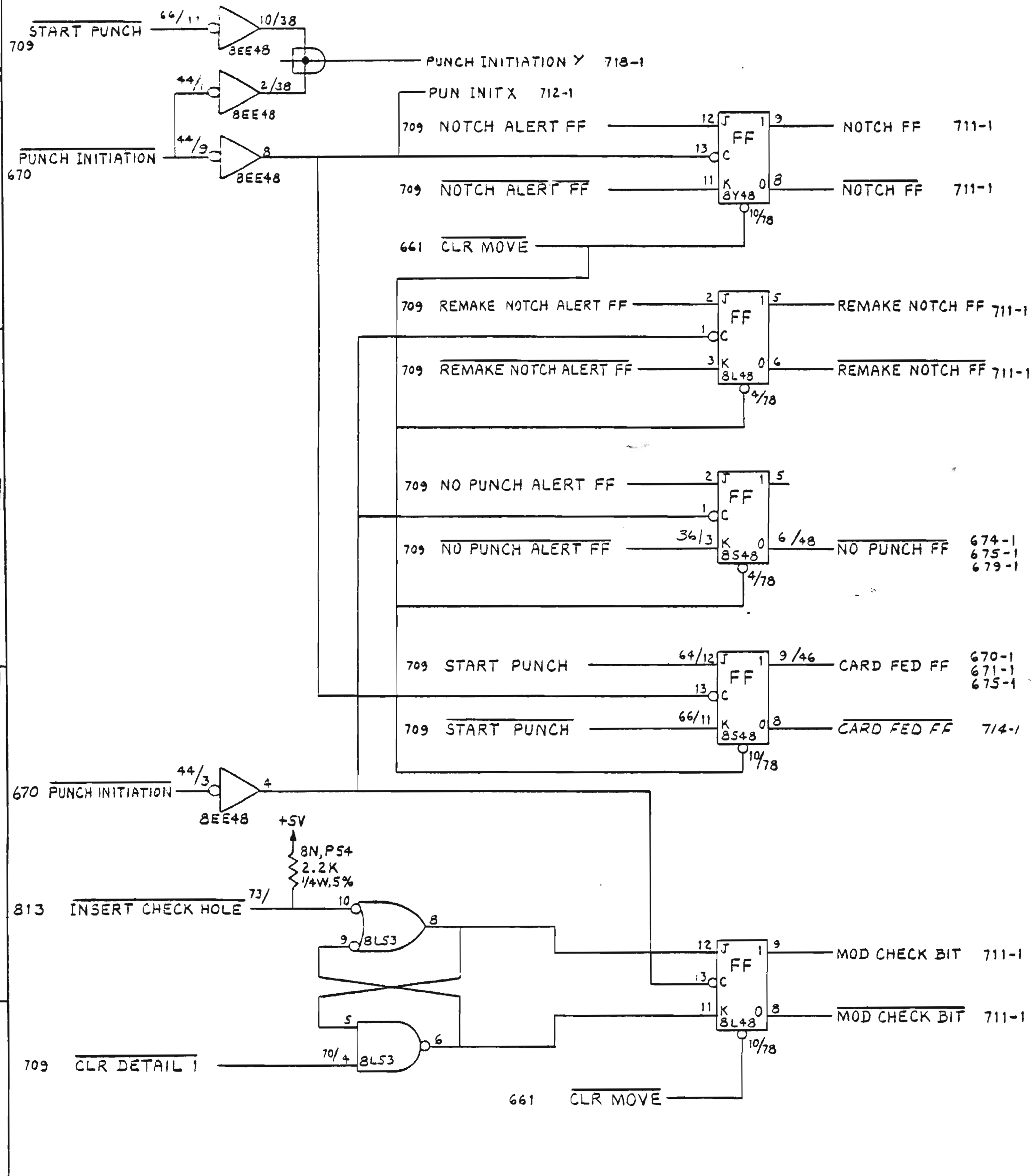
OUTPUT FEED DETAIL FLIP-FLOPS, PART 1

DWG NO. C 7709

REV | COMP ASM | ARTWORK | ECN | APPVD DATE

REVISIONS

C 7710



REV	COMP ASM	ARTWORK	ECN	APPV'D-DATE
D	12316-00	9593-05	4392-1P	[illegible]
C	9594-00	9593-00	2162-1C	[illegible]
B	8832-04	8832-04	1934-5C	[illegible]
A	[illegible]-00	[illegible]-00	1637-4C	[illegible]
–	5106-01	5105-01	1424-2C	[illegible]

REVISIONS

E	12316-01	12315-01	4392-3C	[illegible]

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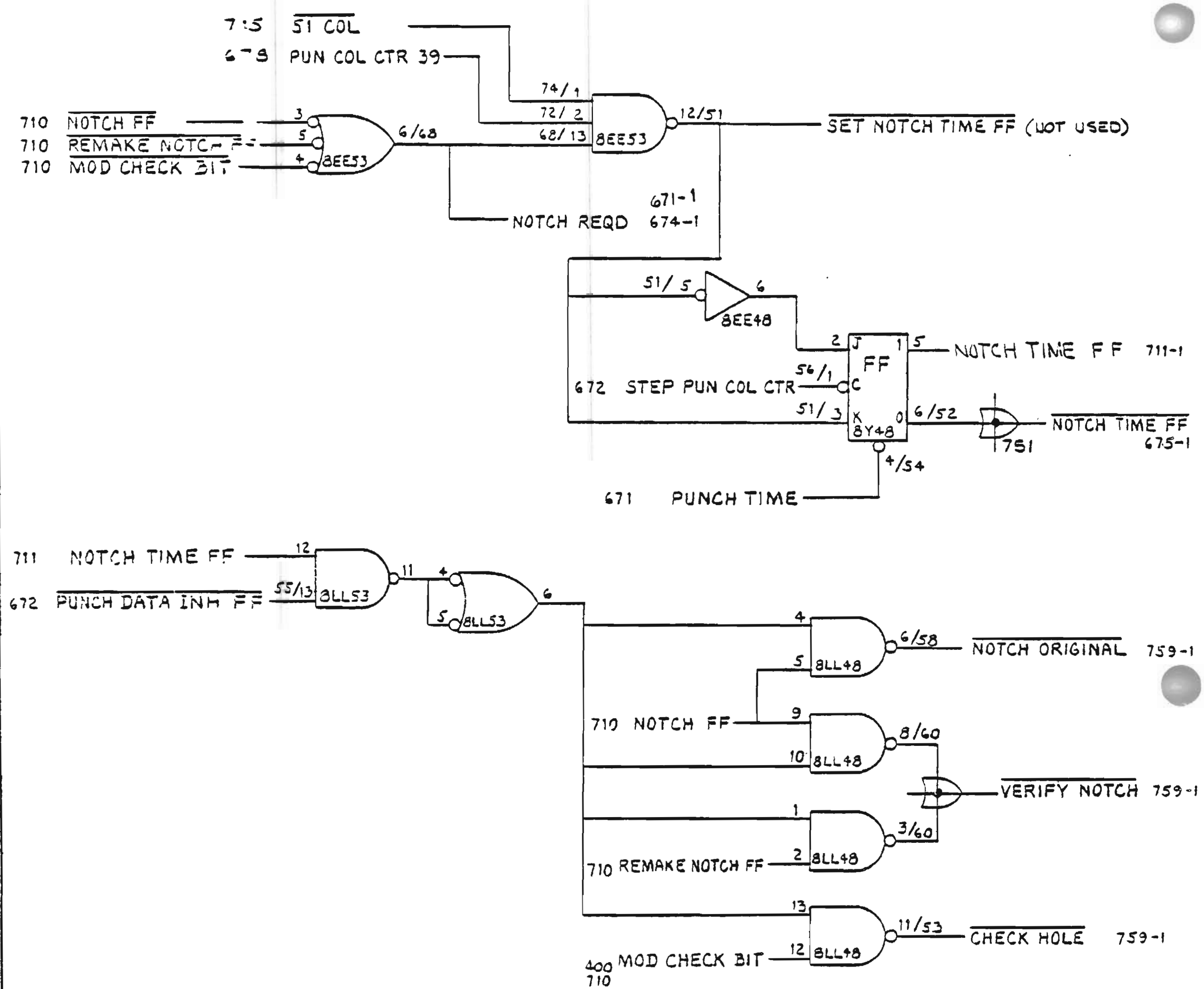
DR 80-B-133

DRN J. ALESSI 4/18/72 ENG [illegible]
CHK [illegible] 5/22/72 MFG [illegible]

TITLE
OUTPUT FEED DETAIL
FLIP-FLOPS, PART 2

DWG NO. C 7710 — DASH NO -00 — SH 1 OF 1

C 7711



REV	COMP ASM	ARTWORK	ECN	APPRO-DATE
C	9594-04	9593-04	3135-2C	
B	9594-00	9593-00	2162-1C	
A	-	5105-00	1687-4C	
-	5106-01	5105-01	1424-2C	

REVISIONS

DR80-B-134

DRN J. ALESI 4/18/72

CHK

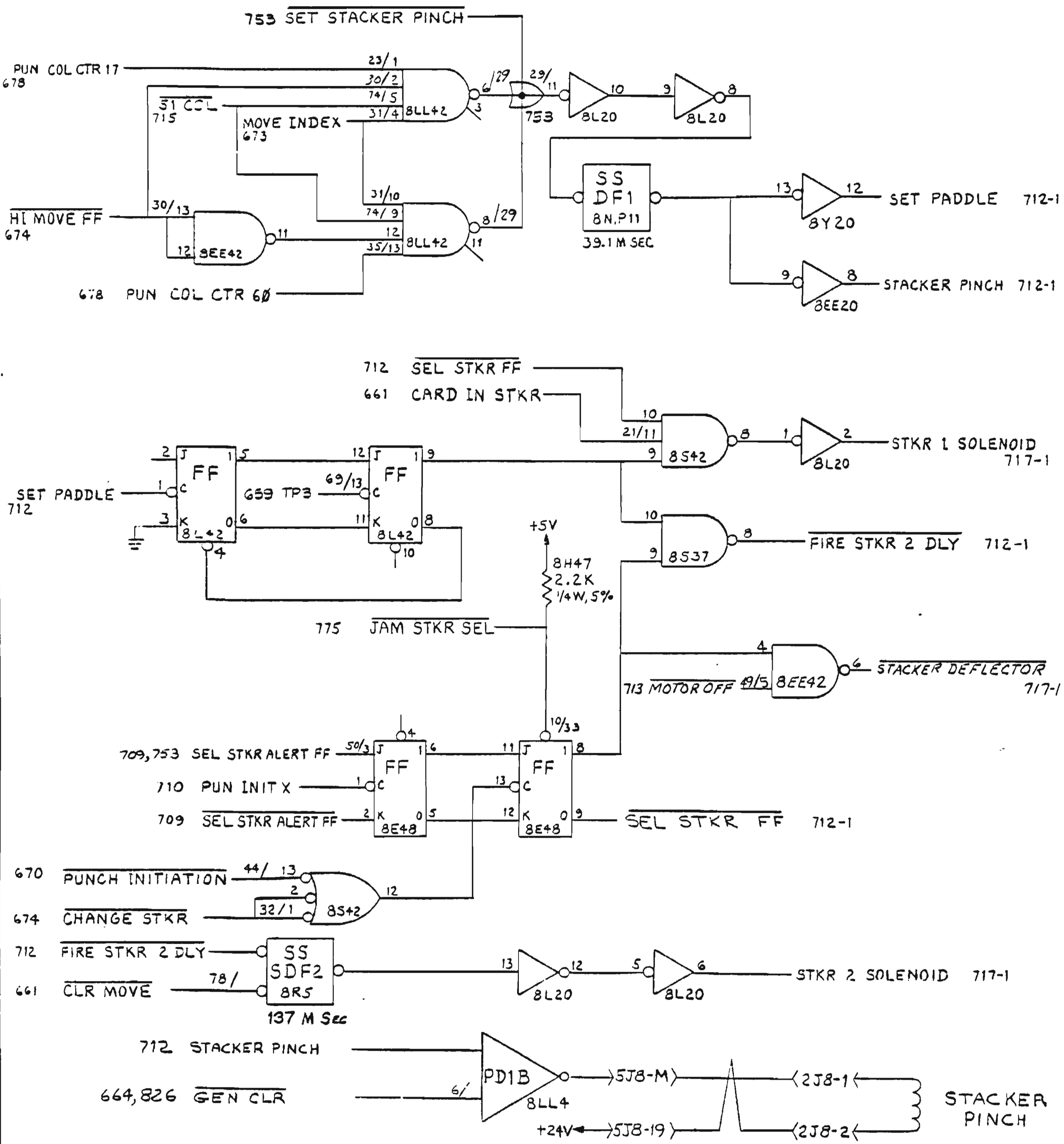
TITLE NOTCH TIMING CONTROL

DWG NO. C 7711

DASH NO. -00

SH 1 OF 1

C 7712



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
D	9594-01	9593-01 B	2436-3C	[illegible]
C	9594-00	9593-00	2162-1C	[illegible]
B	8882-02	8881-00	1934-2P	[illegible]
A	[illegible]-00	[illegible]-00	1687-4C	[illegible]
—	5106-01	5105-01	1424-2C	[illegible]

REVISIONS

G	12816-01	12815-01	4392-3C	[illegible]
F	9594-04	9593-04	3435-2C	[illegible]
E	9594-04	9593-03	3435-P	[illegible]

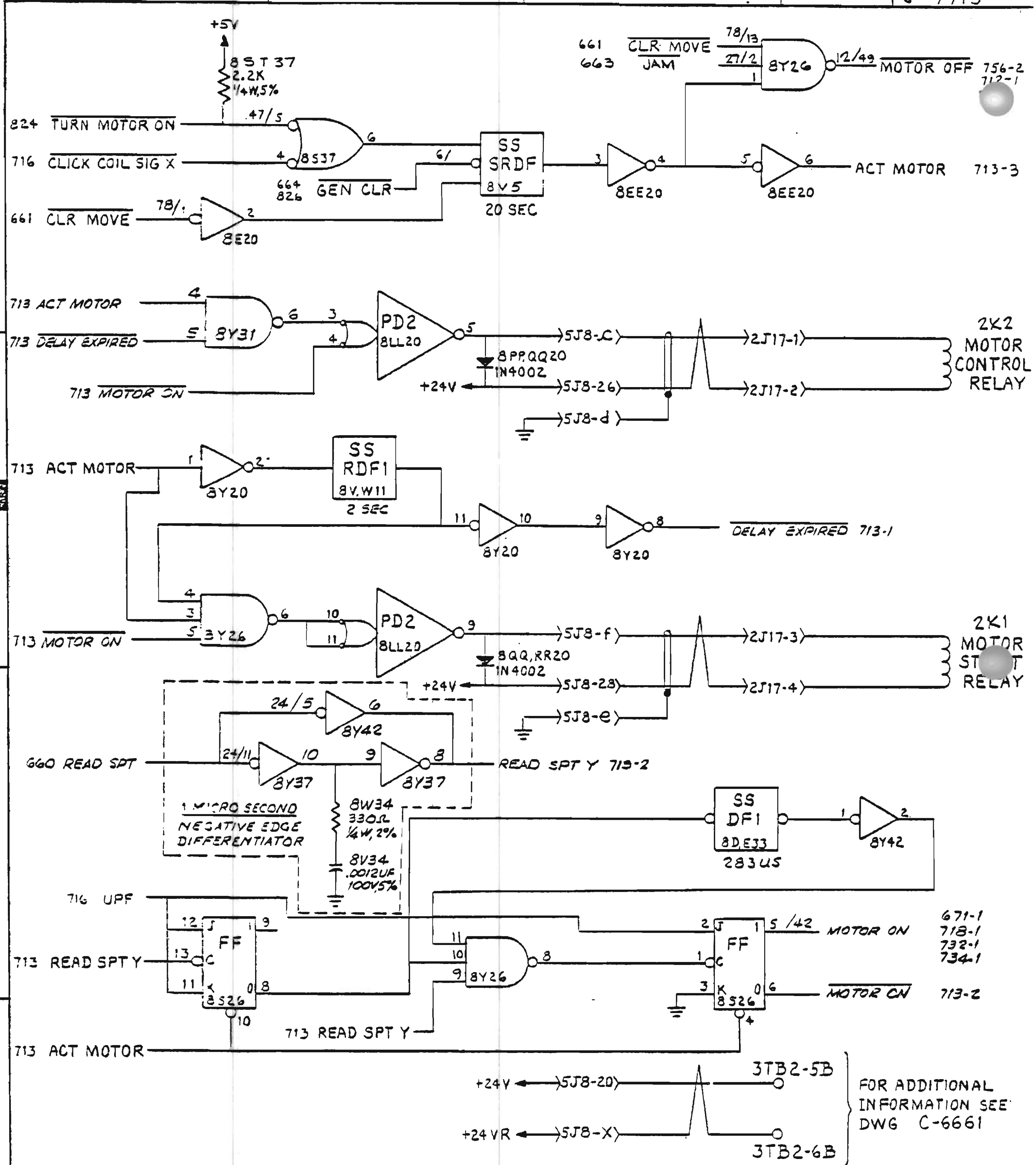
DR80-B-135

CRN J. ALESI 11/2/72 ENG [illegible]
CHK [illegible] MFG [illegible]

TITLE STACKER CONTROL

DWG NO. C 7712 DASH NO -00 SH 1 OF 1

4 3 2 1 C 7713



DR80-B-136

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
J	12816-01	12815-01	4392-3C	
H	9594-03	9593-03	3979-2C	
G	9594-03	9593-03	3435-1P	
F	9594-01	9593-01	3436-3C	
E	3594-01	9593-01	2436-1P	
D	9594-00	9593-00	2162-1C	
C	8882-04	8881-04	1934-5C	
B	8882-00		1687-4C	
A		5105-01	1687-1P	
-	5106-01	5105-01	1424-2C	

REVISIONS

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DRN J. ALESI 4/3/72
CHK 5/22/72

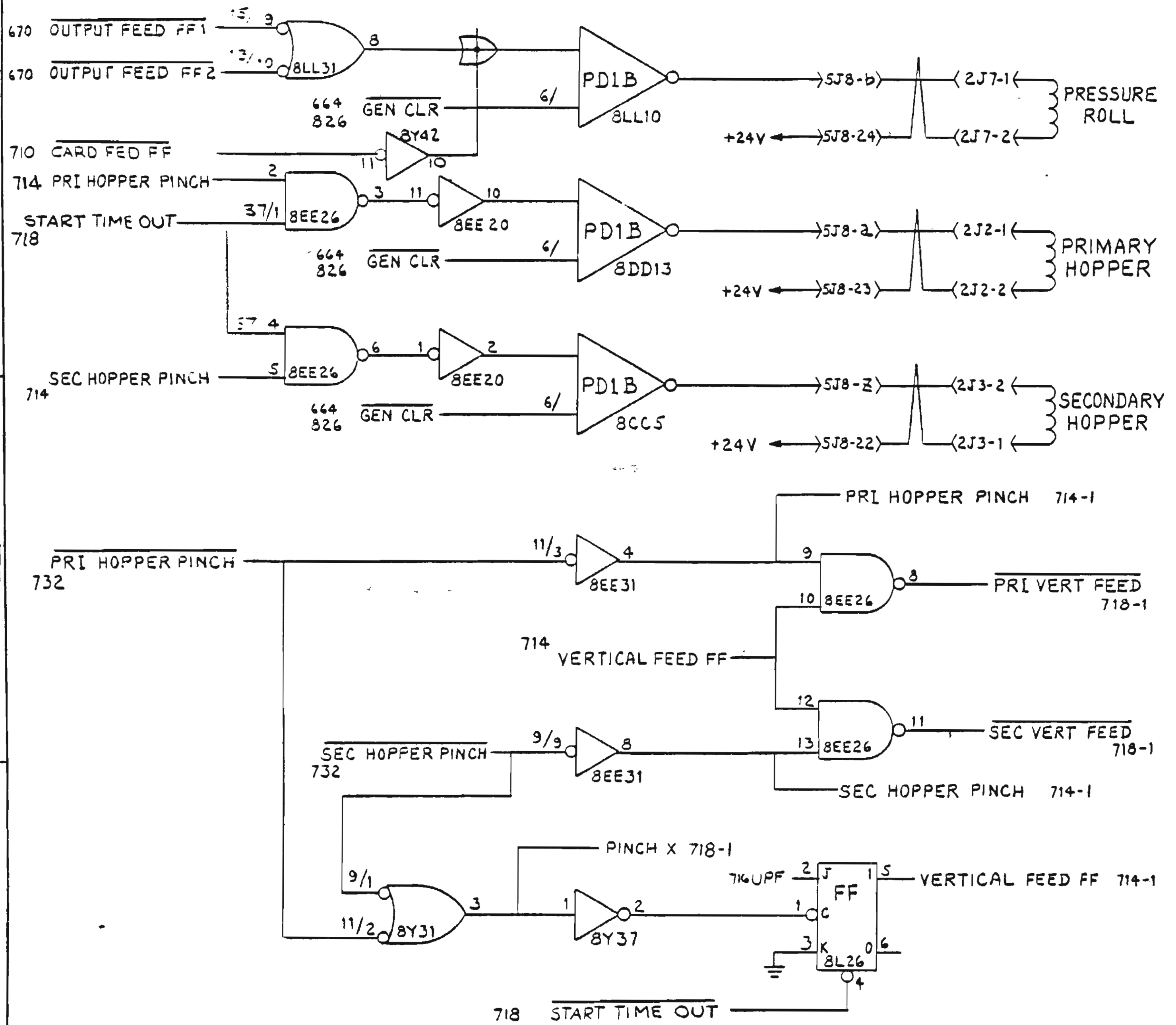
TITLE DRIVE MOTOR CONTROL

DWG NO. C 7713

SH 1 of 1

4 3 2 1

C 7714



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
J	12516-01	12515-01	4392-3C	[illegible]
H	12516-00	9595-05	4392-1P	[illegible]
G	9594-05	9593-05	3979-2C	[illegible]
F	9594-01	9593-01	2436-3C	[illegible]
E	9594-00	9593-00	2162-1C	[illegible]
D	8882-04	8881-04	1934-5C	
C	8882-02	8881-00	1934-2P	[illegible]
B	8882-01	8881-00	1934-1P	[illegible]
[illegible]	[illegible]	[illegible]	1697-4C	[illegible]
—	5106-01	5105-01	1424-2C	[illegible]

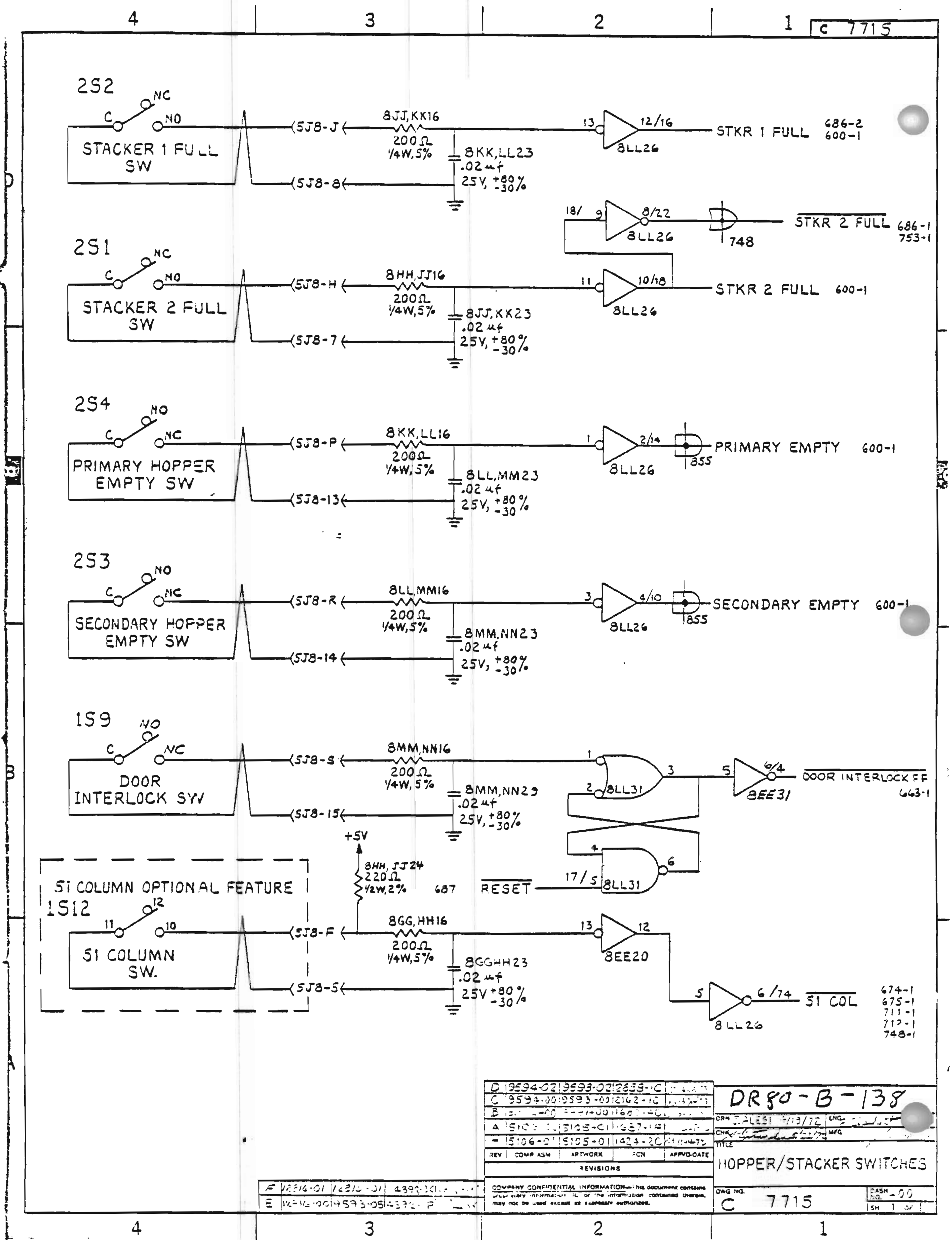
REVISIONS

DR80-B-137

DRN J. ALESI 4/20/72 ENG [illegible]
CHK [illegible] 5/4/72 MFG

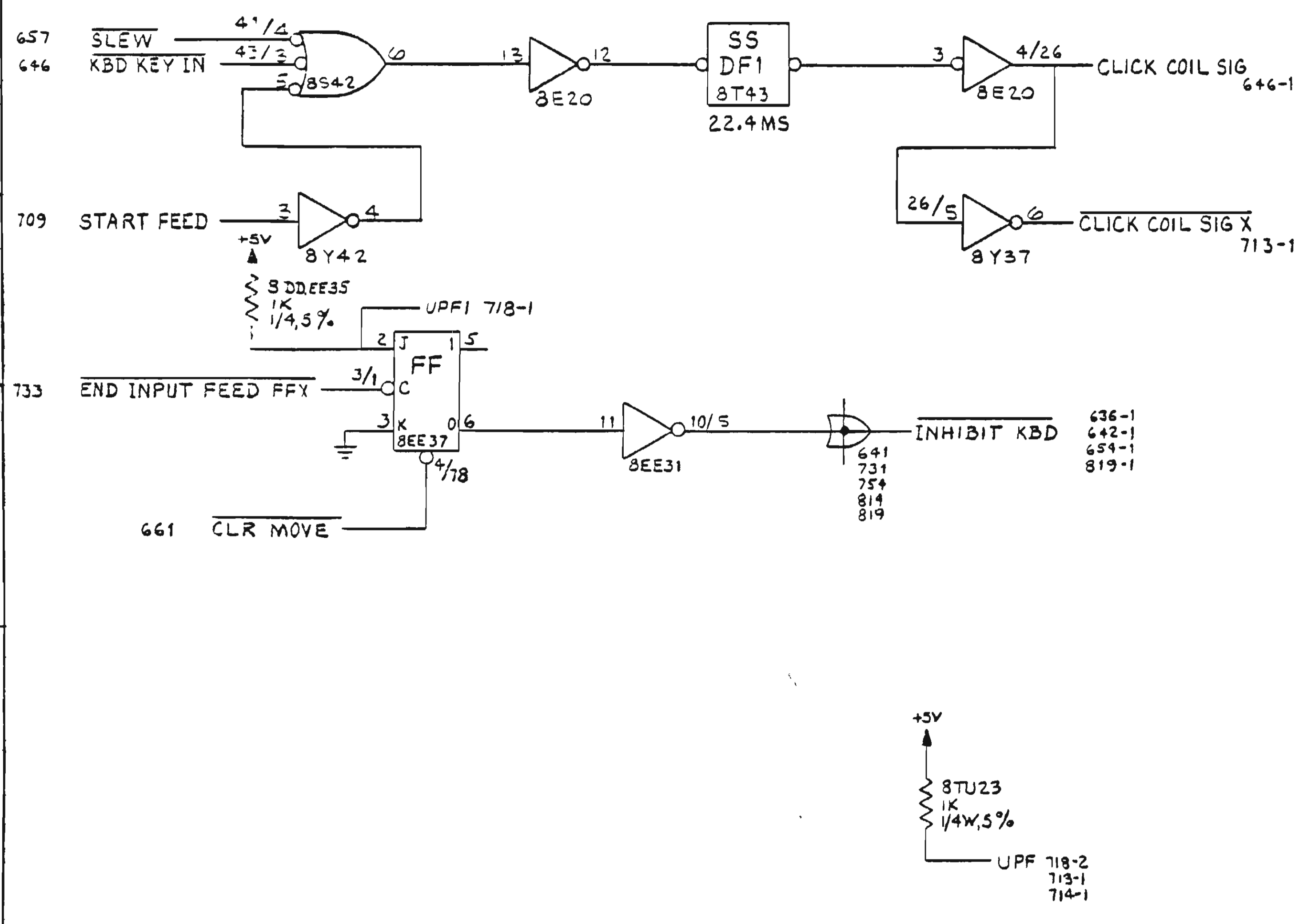
TITLE: ACTUATORS

DWG NO. C 7714 DASH NO -00 SH 1 OF 1



C 7715
2S2
NC
C
NO
STACKER 1 FULL SW
5J8-J
5J8-8
8JJ,KK16
200Ω
1/4W,5%
8KK,LL23
.02 uf
25V, +80% -30%
8LL26
STKR 1 FULL 686-2 600-1
18/9
8/22
8LL26
748
STKR 2 FULL 686-1 753-1
2S1
STACKER 2 FULL SW
5J8-H
5J8-7
8HH,JJ16
200Ω
1/4W,5%
8JJ,KK23
.02 uf
25V, +80% -30%
10/18
8LL26
STKR 2 FULL 600-1
2S4
PRIMARY HOPPER EMPTY SW
5J8-P
5J8-13
8KK,LL16
200Ω
1/4W,5%
8LL,MM23
.02 uf
25V, +80% -30%
2/14
8LL26
855
PRIMARY EMPTY 600-1
2S3
SECONDARY HOPPER EMPTY SW
5J8-R
5J8-14
8LL,MM16
200Ω
1/4W,5%
8MM,NN23
.02 uf
25V, +80% -30%
4/10
8LL26
855
SECONDARY EMPTY 600-1
1S9
DOOR INTERLOCK SW
5J8-S
5J8-15
8MM,NN16
200Ω
1/4W,5%
8MM,NN23
.02 uf
25V, +80% -30%
8LL31
8EE31
DOOR INTERLOCK FF 663-1
+5V
8HH,JJ24
220Ω
1/2W,2%
687
RESET
17/5
8LL31
51 COLUMN OPTIONAL FEATURE
1S12
51 COLUMN SW.
5J8-F
5J8-5
8GG,HH16
200Ω
1/4W,5%
8GGHH23
.02 uf
25V +80% -30%
8EE20
6/74
8LL26
51 COL 674-1 675-1 711-1 712-1 748-1
REV
COMP ASM
ARTWORK
FCN
APPVD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION
DR80-B-138
HOPPER/STACKER SWITCHES
DWG NO.
C 7715

C 7716



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
E	12816-01	2815-01	4392-3C	[illegible]
D	9594-05	9593-05	3974-2C	[illegible]
C	9594-00	9593-00	2162-1C	[illegible]
B	[illegible]	[illegible]	2019-[illegible]	[illegible]
A	[illegible]	[illegible]	1687-4C	[illegible]
-	5106-01	5105-01	1424-2C	[illegible]

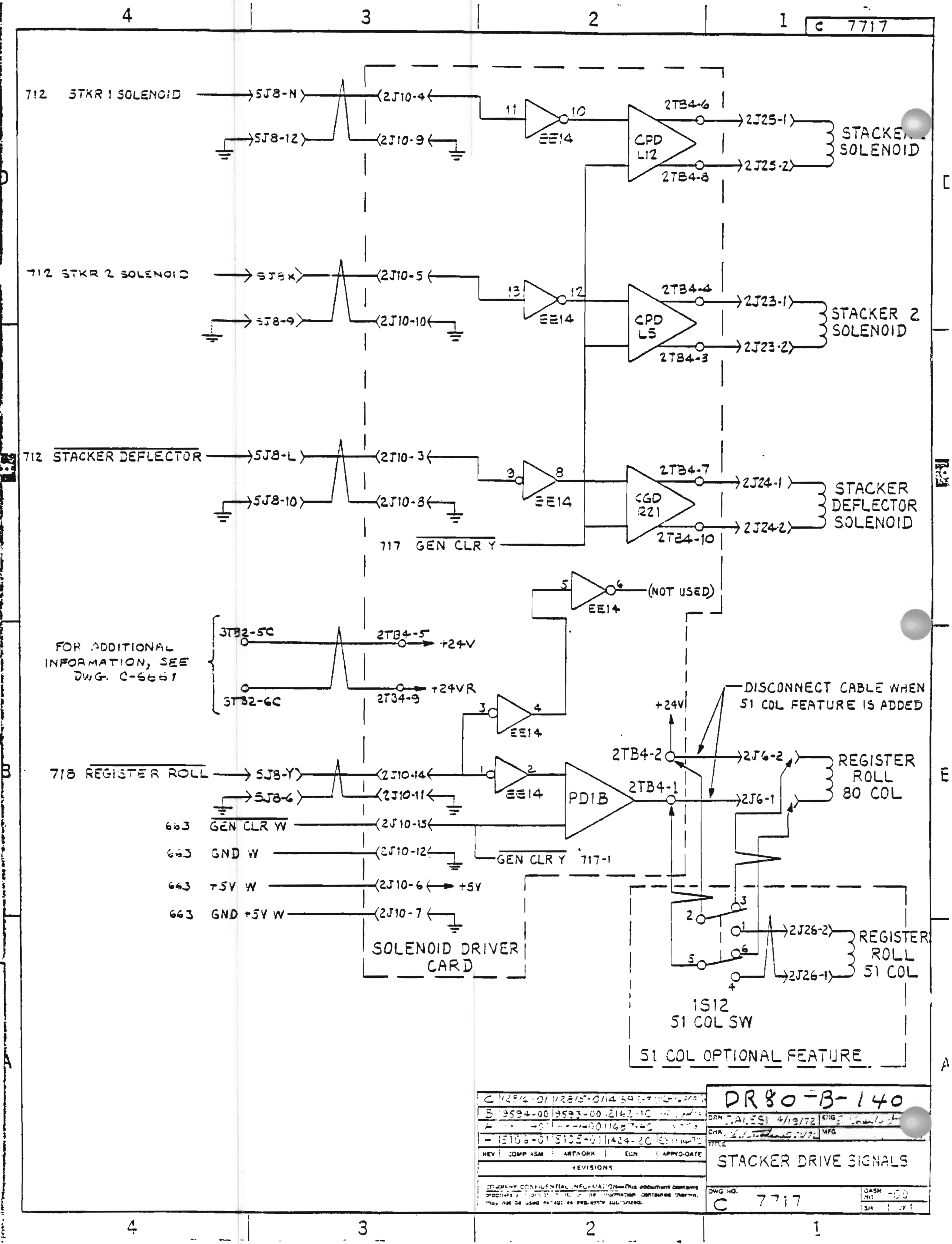
REVISIONS

DR 80-B-139

DRN J. ALESI 4/19/72

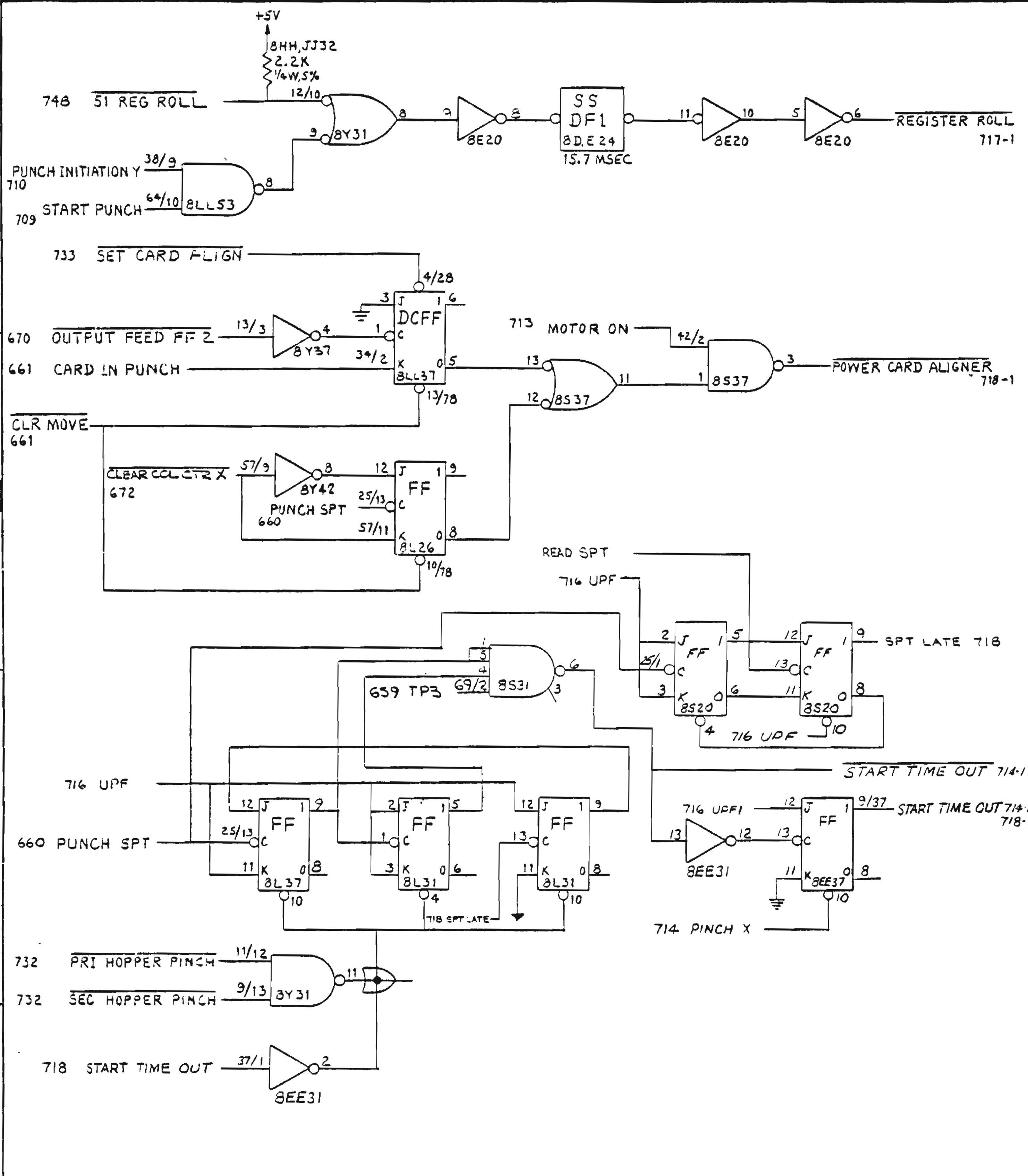
TITLE CLICK COIL COUNTER

DWG NO. C 7716 DASH NO. -00 SH 1 OF 1



C 7717
712 STKR 1 SOLENOID
5J8-N
5J8-12
2J10-4
2J10-9
11
10
EE14
CPD L12
2TB4-6
2TB4-8
2J25-1
2J25-2
STACKER 1 SOLENOID
712 STKR 2 SOLENOID
5J8-K
5J8-9
2J10-5
2J10-10
13
12
EE14
CPD L5
2TB4-4
2TB4-3
2J23-1
2J23-2
STACKER 2 SOLENOID
712 STACKER DEFLECTOR
5J8-L
5J8-10
2J10-3
2J10-8
9
8
EE14
CGD 221
2TB4-7
2TB4-10
2J24-1
2J24-2
STACKER DEFLECTOR SOLENOID
717 GEN CLR Y
5
6
EE14
(NOT USED)
FOR ADDITIONAL INFORMATION, SEE DWG. C-6661
3TB2-5C
3TB2-6C
2TB4-5
2TB4-9
+24V
+24VR
3
4
EE14
DISCONNECT CABLE WHEN 51 COL FEATURE IS ADDED
+24V
718 REGISTER ROLL
5J8-Y
5J8-6
2J10-14
2J10-11
1
2
EE14
PD1B
2TB4-2
2TB4-1
2J6-2
2J6-1
REGISTER ROLL 80 COL
663 GEN CLR W
2J10-15
663 GND W
2J10-12
GEN CLR Y 717-1
663 +5V W
2J10-6
+5V
663 GND +5V W
2J10-7
SOLENOID DRIVER CARD
2J26-2
2J26-1
REGISTER ROLL 51 COL
1S12
51 COL SW
51 COL OPTIONAL FEATURE
DR80-B-140
REV
COMP ASM
ARTWORK
ECN
APPVD-DATE
REVISIONS
C 12814-01 12815-01
B 9594-00 9593-00 2162-1C
- 5106-01 5105-01 1424-2C
DRN TALES 4/19/72
CHK
ENG
MFG
TITLE
STACKER DRIVE SIGNALS
DWG NO. C 7717
DASH NO -00
SH 1 OF 1

C 7718



L	12816-01	12815-01	4392-3C	[illegible]
K	12816-00	9593-05	4392-1P	[illegible]
J	9594-05	9593-05	3979-2C	[illegible]
H	[illegible]	9593-04	3979-1P	[illegible]
G	9594	9593-01	2436-3C	[illegible]
F	9594-00	9593-00	2162-1C	[illegible]
E	8882-03	8881-00	1734-3P	
D	8882-03 8810-03	8881-00 8809-00	1904-3P	[illegible]

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	8882-00	8881-00	2000-1C	[illegible] 4-3-73
B	[illegible]-00	[illegible]-00	1687-4C	[illegible]
A	5106-[illegible]	5105-01	1[illegible]57-1F	[illegible]
–	5106-01	5105-01	1424-2C	[illegible]-72

REVISIONS

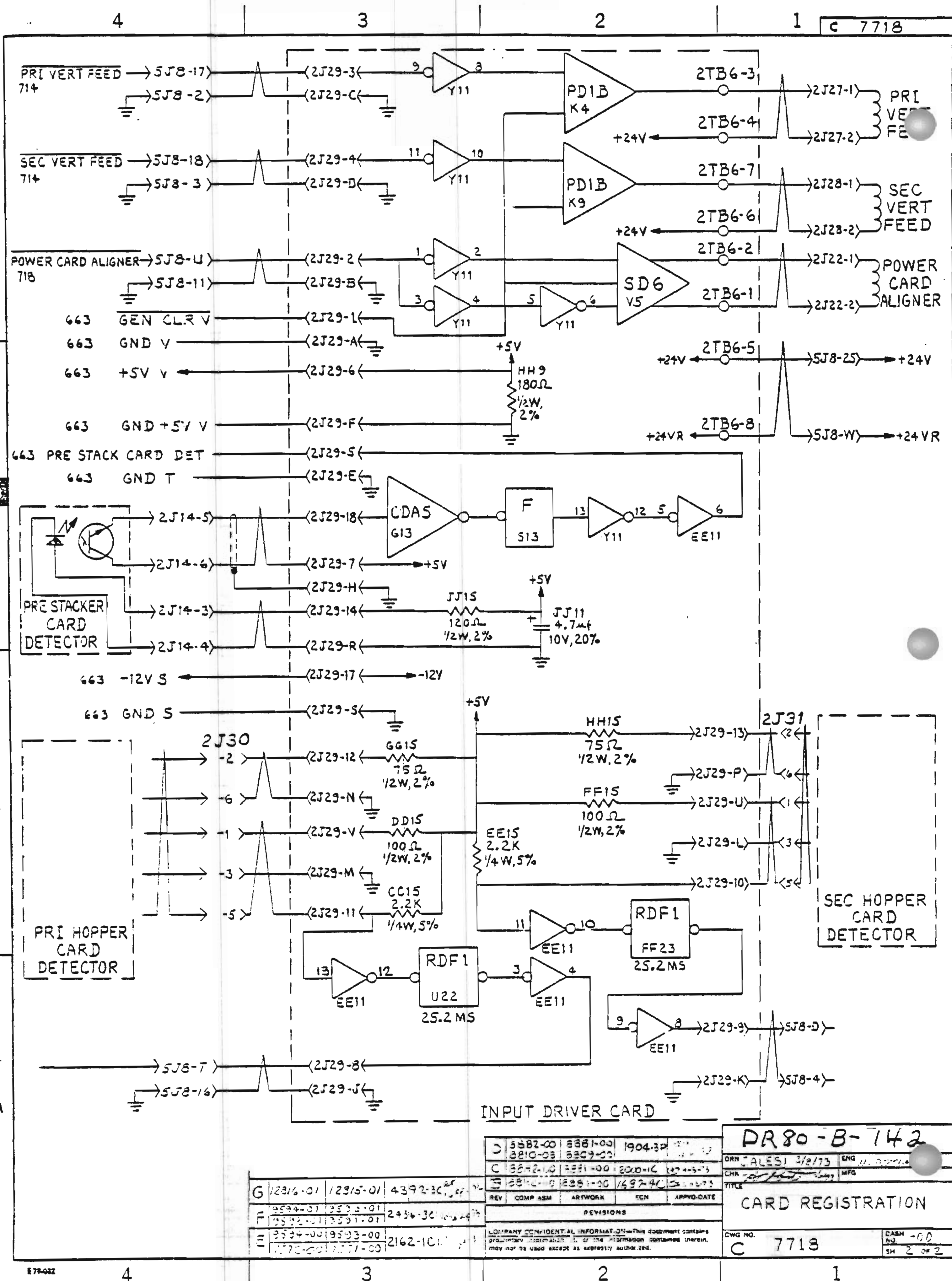
DR 80-B-141

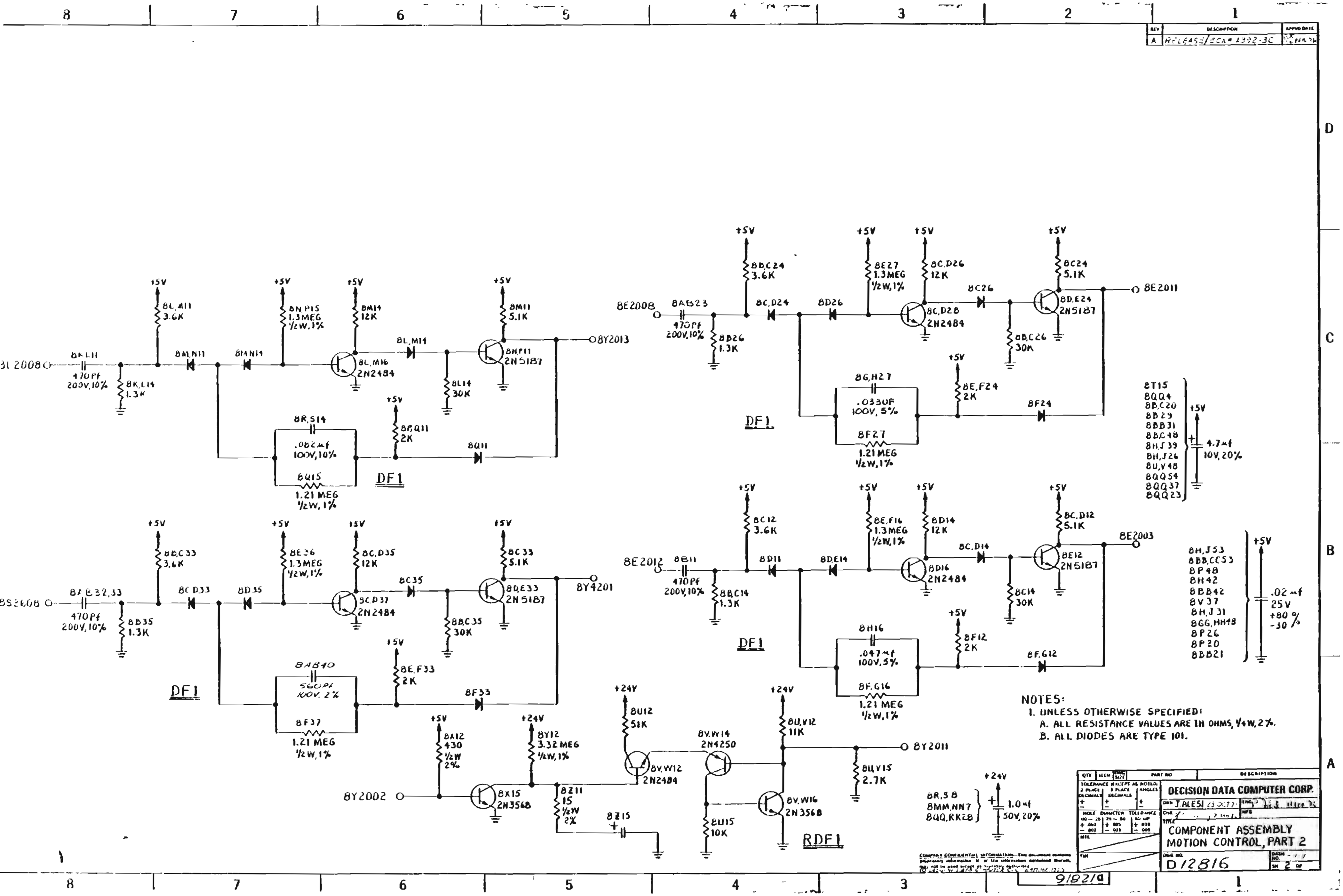
DRN J ALESI 4/20/72 ENG [illegible]
CHK [illegible] 5/22/72 MFG

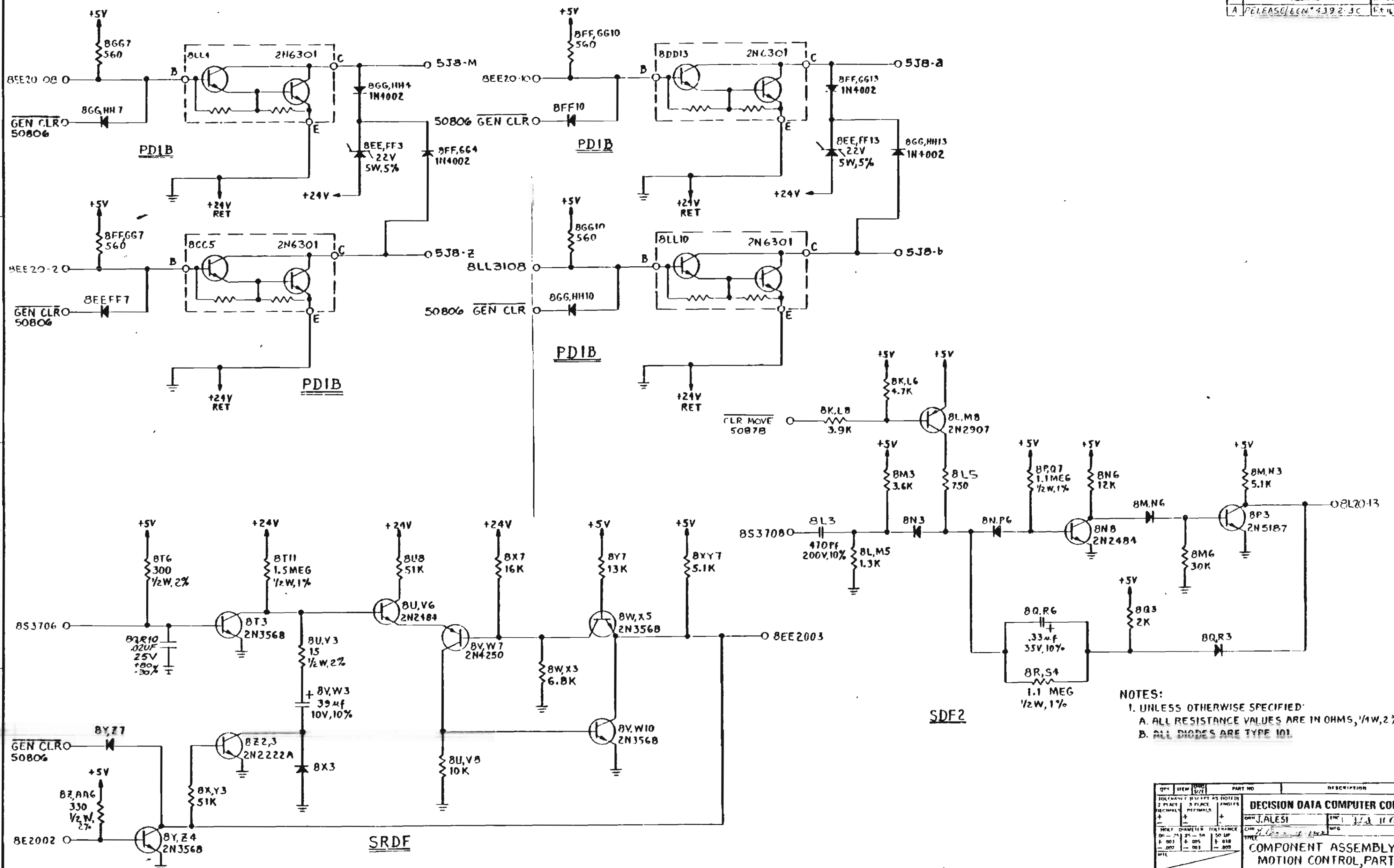
TITLE CARD REGISTRATION

DWG NO. C 7718 DASH NO. -00 SH 1 OF 2

SHEET INDEX	SHT	1	2
	REV	L	G


C 7718
PRI VERT FEED 714
SEC VERT FEED 714
POWER CARD ALIGNER 718
663 GEN CLR V
663 GND V
663 +5V V
663 GND +5V V
663 PRE STACK CARD DET
663 GND T
663 -12V S
663 GND S
PD1B K4
PD1B K9
SD6 V5
Y11
CDA5 G13
F S13
EE11
HH9 180Ω 1/2W, 2%
JJ15 120Ω 1/2W, 2%
JJ11 4.7µf 10V, 20%
HH15 75Ω 1/2W, 2%
GG15 75Ω 1/2W, 2%
FF15 100Ω 1/2W, 2%
DD15 100Ω 1/2W, 2%
EE15 2.2K 1/4W, 5%
CC15 2.2K 1/4W, 5%
RDF1 FF23 25.2 MS
RDF1 U22 25.2 MS
2J30
2J31
PRE STACKER CARD DETECTOR
PRI HOPPER CARD DETECTOR
SEC HOPPER CARD DETECTOR
PRI VERT FEED
SEC VERT FEED
POWER CARD ALIGNER
INPUT DRIVER CARD
DR80-B-142
CARD REGISTRATION
DWG NO. C 7718
SH 2 OF 2


DF1
DF1
DF1
DF1
RDF1
NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.
B. ALL DIODES ARE TYPE 101.
DECISION DATA COMPUTER CORP.
COMPONENT ASSEMBLY
MOTION CONTROL, PART 2
D12816
RELEASE/ECN# 1392-3C
9182/a



NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.
B. ALL DIODES ARE TYPE 101.

REV	DESCRIPTION	APPD DATE
A	RELEASE/ECN#4392-3C	

DECISION DATA COMPUTER CORP.

DRN J. ALESI

COMPONENT ASSEMBLY
MOTION CONTROL, PART 2

DWG NO D 12816

SH 3

31821 D

LOGIC/SCHEMATIC INDEX 6/27/78 LOCATION- MODULE -9-

8001/10 DR 80COL MANUAL- 7545 DD
8010-11 DR 80COL MANUAL-902193 DD-360

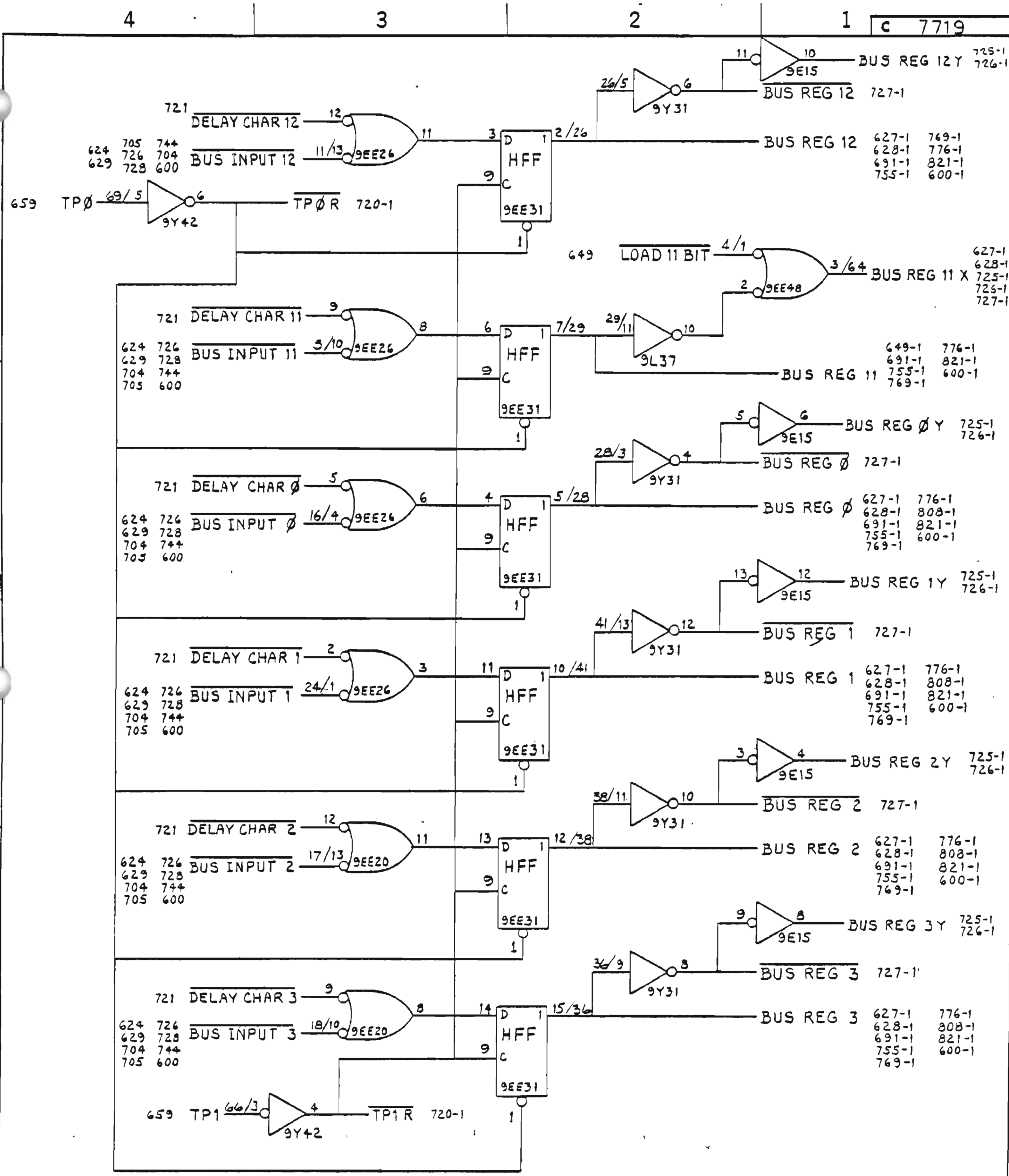
005107-06 PWB
INPUT MEMORY 005108-06 ASM

LOGIC-DWG	SH	REV	TITLE
007719		-A	BUS REGISTER PT-1
007720		--	BUS REGISTER PT-2
007721		-A	RIGHT ADJ SHIFT REG PT-1
007722		--	RIGHT ADJ SHIFT REG PT-2
007723		-A	MEMORY CONTROLS
007724		-D	INPUT MEM/VER MEM GATING
007725		-B	INPUT MEMORY
007726		-A	DUP MEMORY
007727		-D	VERIFY COMPARATOR
007728		-A	MEMORY TO BUS PT-1
007729		--	MEMORY TO BUS PT-2
007730		-A	PUNCH TRANSFER COUNTER
007731		-E	RELEASE TRANSFER CONTROL

SCHEM-DWG	SH	REV
005108	04	-J

DR80 - B - 146

C 7719



REV			ECN	APPVD·DATE
A	5108-05	5107-05	2288-2C	11 Sep 73
-	5108-02	5107-02	1425-2C	11-13-72

REVISIONS

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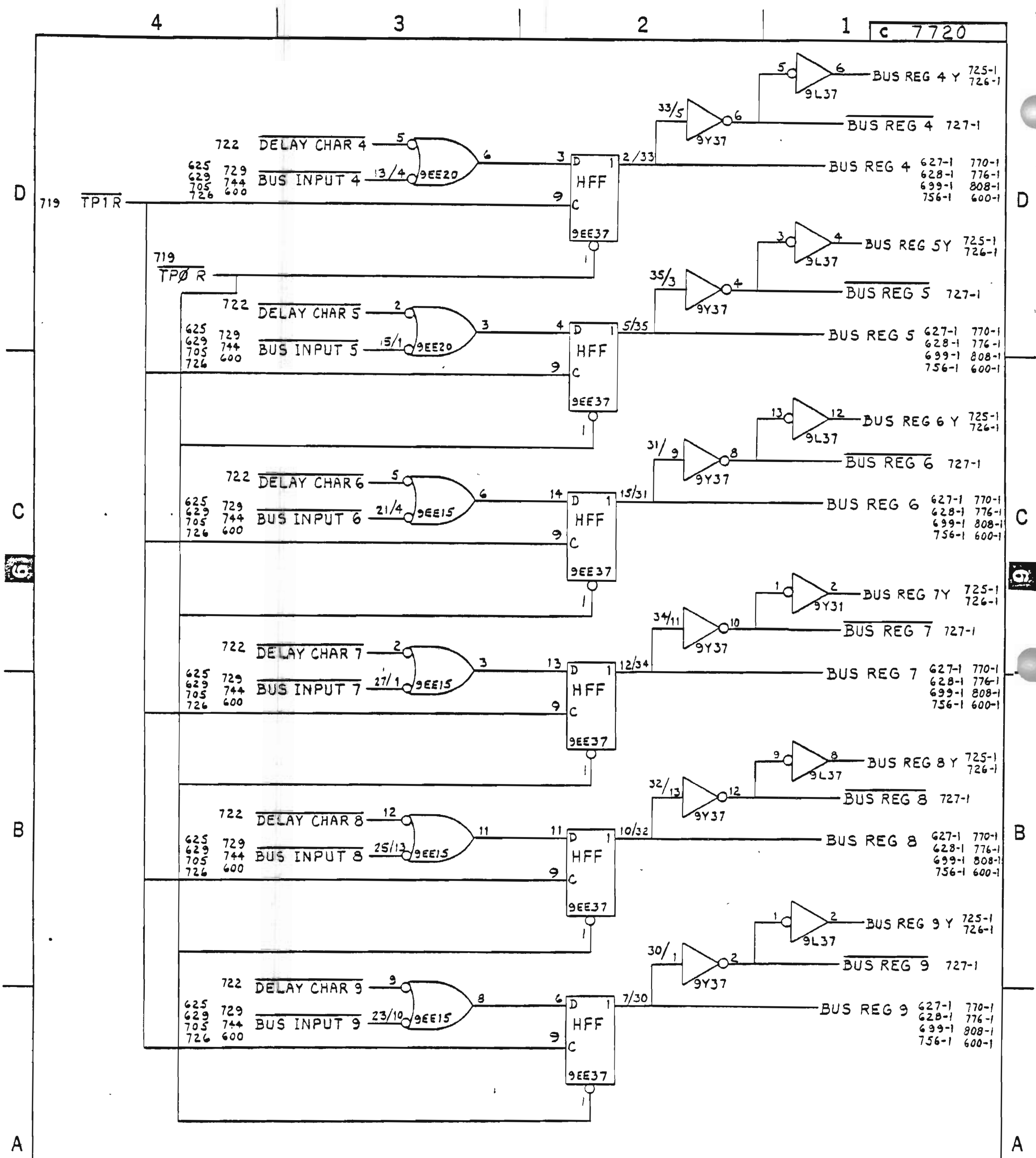
DR80-B-147

DRN J. ALESI 12/16/71

TITLE BUS REGISTER, PART 1

DWG NO. C 7719 DASH NO. -00 SH 1 OF 1



DR80-B-148

DRN J. ALESI 12/16/71

TITLE BUS REGISTER, PART 2

REV	COMPASM	ART WORK	ECN	APPVD-DATE
-	5108-02	5107-02	1425-2C	11-3-72

REVISIONS

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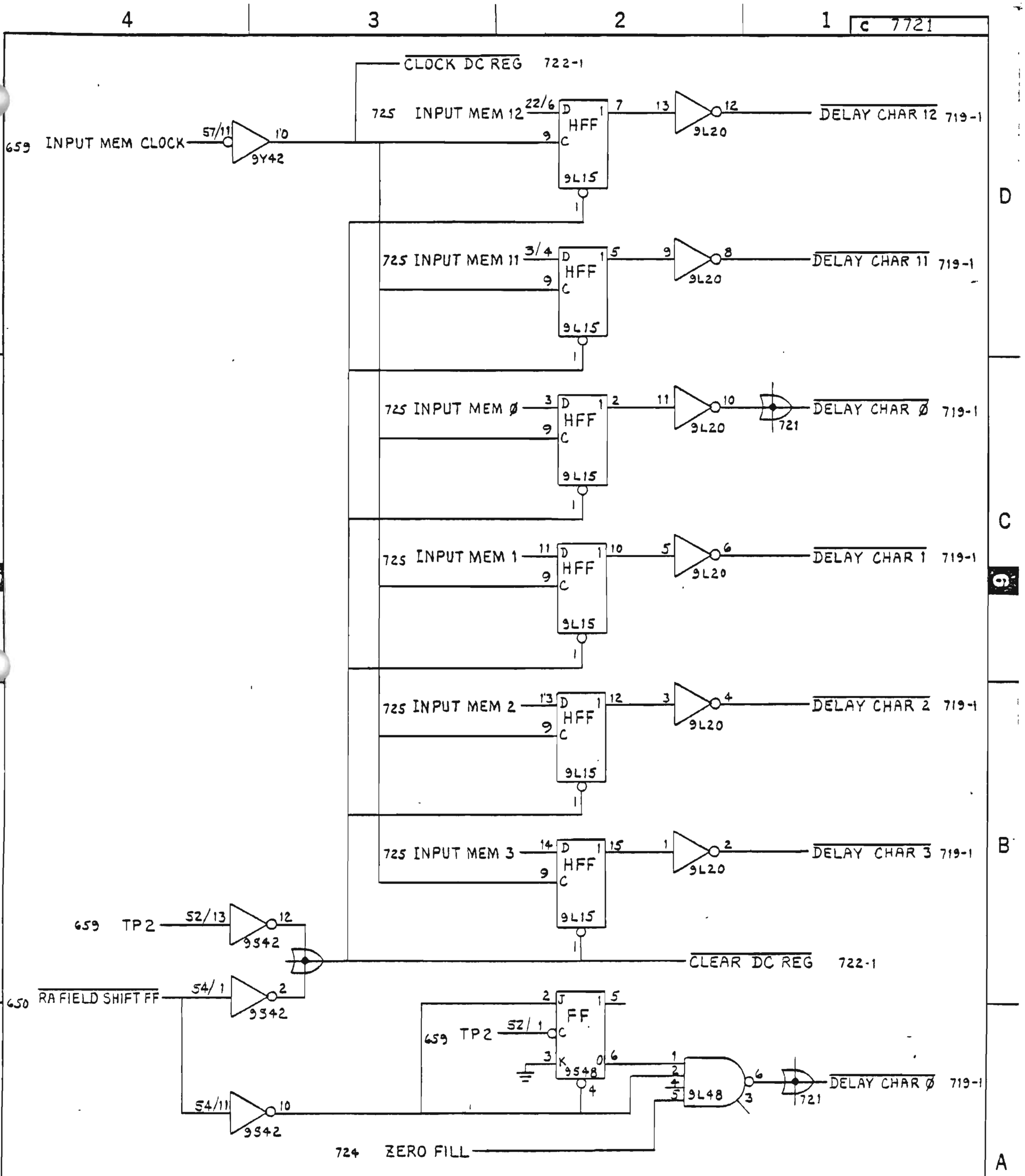
DWG NO. C 7720 DASH NO. -00 SH 1 OF 1



REV	COMP ASM	ART WORK	ECN	APPVD-DATE
A	5108-04	5107-03	1898-1C	3-6-73
–	5108-02	5107-02	1425-2C	11-13-72

REVISIONS

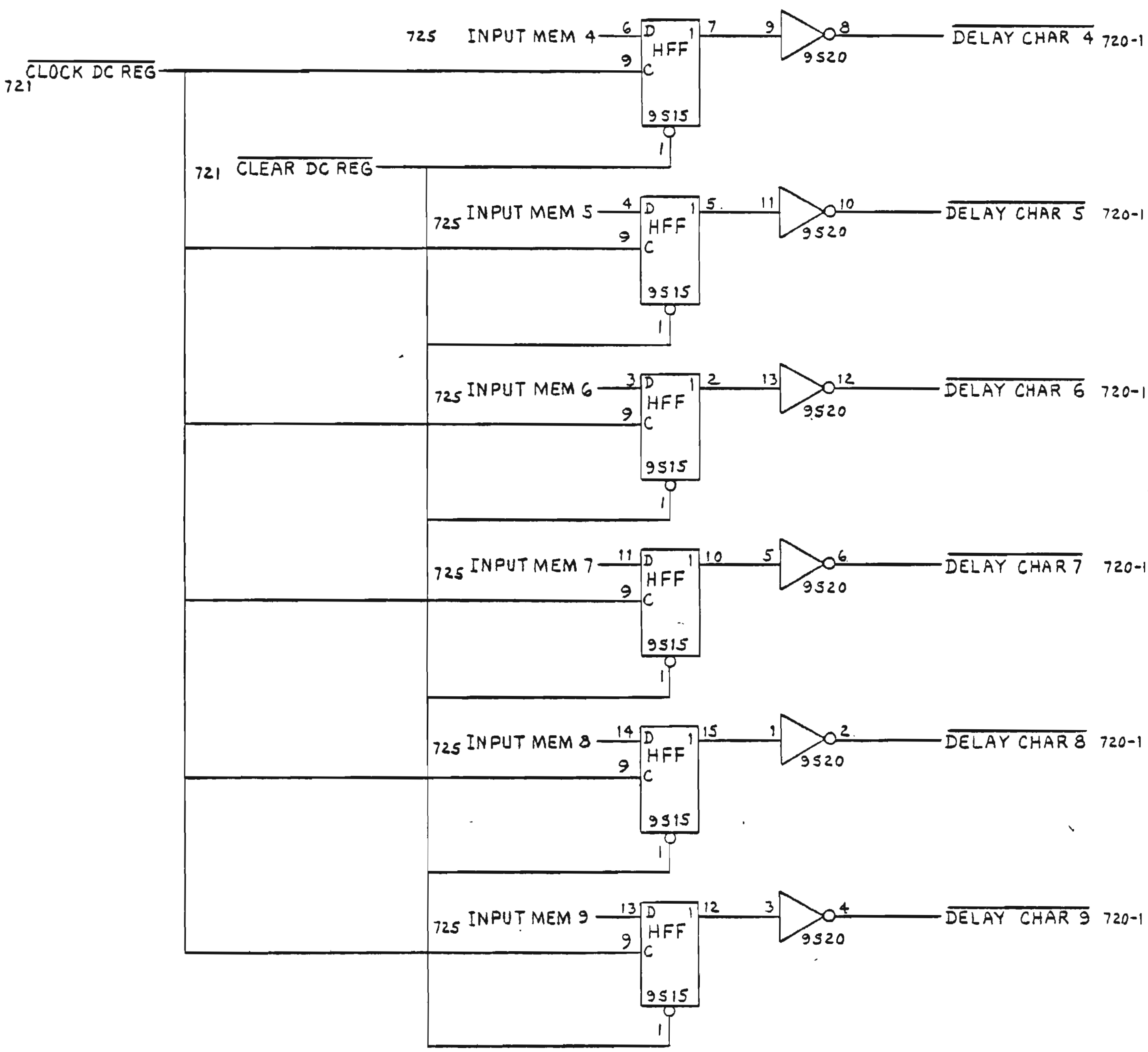
DR80-B-149

DRN J. ALESI 12/15/71

TITLE RIGHT ADJUST SHIFT REGISTER, PART 1

DWG NO. C 7721 DASH NO. -00 SH 1 OF 1

C 7722



DR80-B-150

REV	COMPASM	ART WORK	ECN	APPVD-DATE
–	5108-02	5107-02	1425-2C	11-13-72

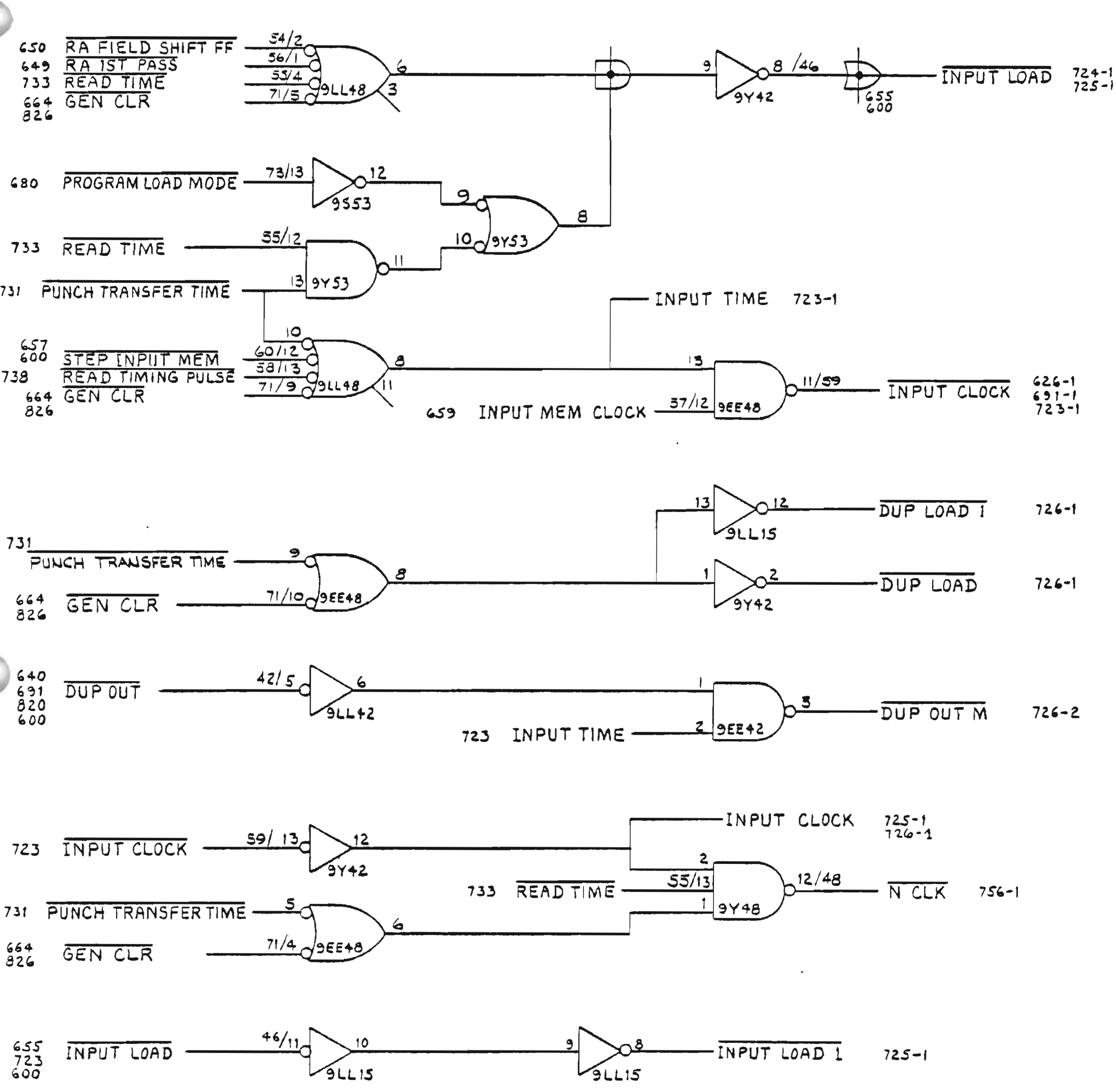
REVISIONS

DRN J. ALESI 12/15/71
ENG 11-13-72
CHK 4/27/72
MFG

TITLE: RIGHT ADJUST SHIFT REGISTER PART 2

DWG NO. C 7722
DASH NO. -00
SH 1 OF 1

C 7723



A	5108-05	5107-05	2288-2C	11-26-73
-	5108-02	5107-02	1425-2C	11-13-72
REV	COMP ASM	ART WORK	ECN	APPVD-DATE

REVISIONS

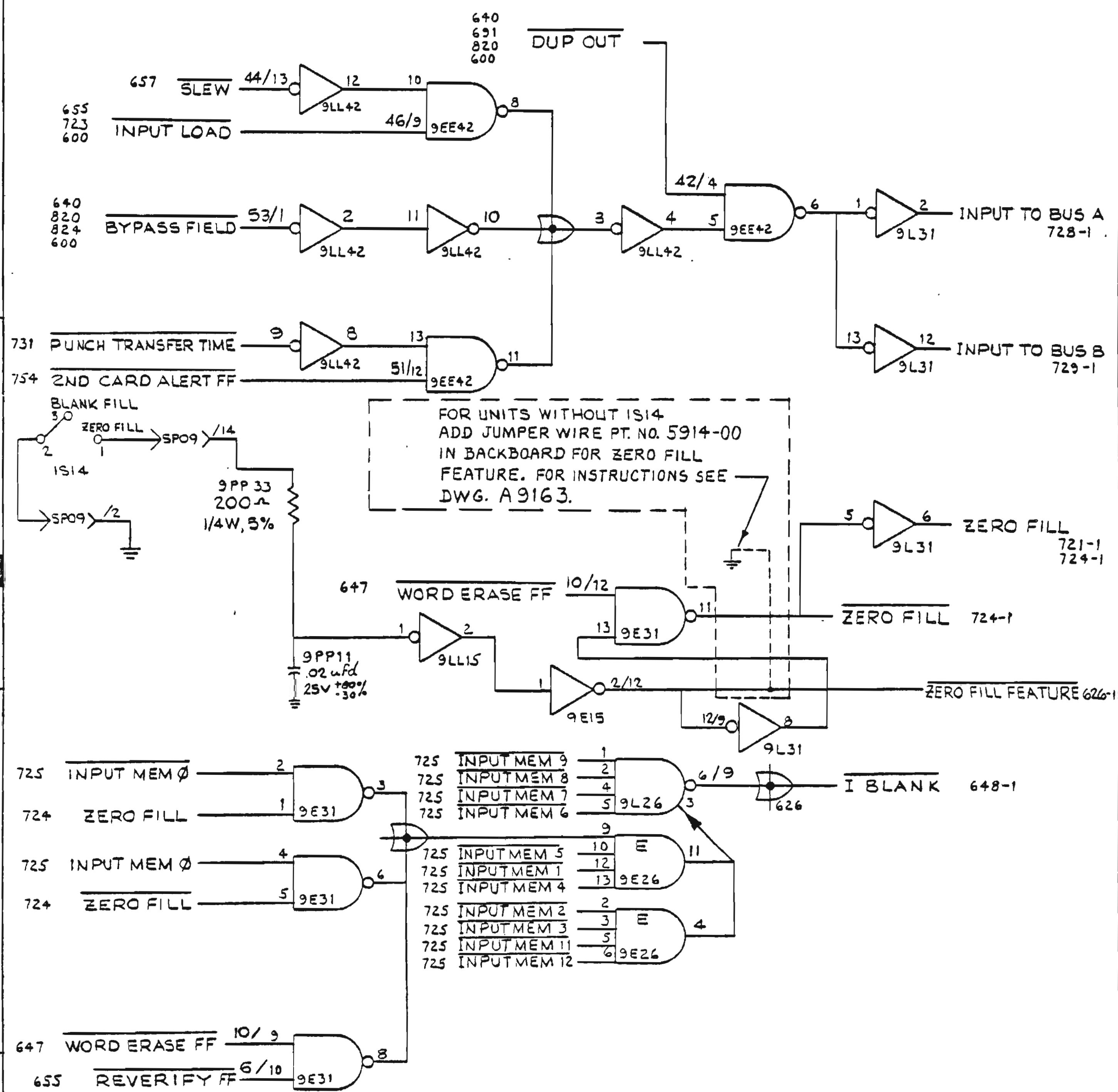
DR80-B-151

DRN J. ALESI 12/14/71 ENG 11-13-72
CHK MFG

TITLE MEMORY CONTROLS

DWG NO. C 7723 DASH NO. -00 SH 1 OF 1

C 7724



D	5108-05	5107-05	2288-2C	
C	5108-05	5107-04	2288-1P	
B	5108-03	5107-03	1988-1C	RVK 4-2-73
A	5108-03	5107-03	1818-1P	1-30-73
—	5108-02	5107-02	1425-2C	11-13-72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

DR80-B-152

DRN J. ALESI 12/15/71 ENG 11-13-72

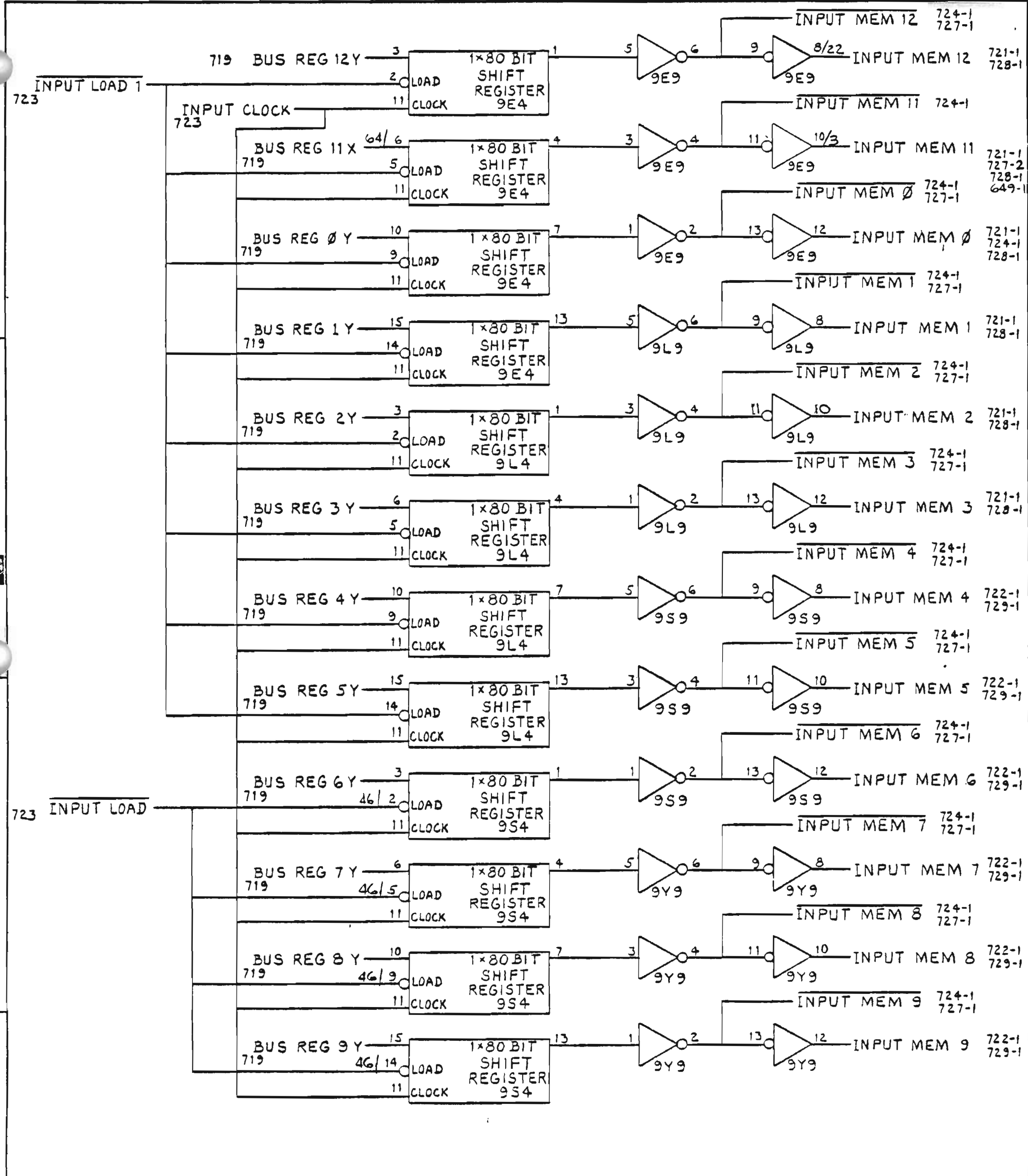
CHK 4/25/72 MFG

TITLE INPUT MEMORY AND VERIFY MEMORY GATING

DWG NO. C 7724 DASH NO -00

SH 1 OF 1

C 7725



DR80-B-153

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	5108-04	5107-03	1898-1P	3-5-73
A	5108-03	5107-03	1818-1P	1-30-73
-	5108-02	5107-02	1425-2C	11-13-72

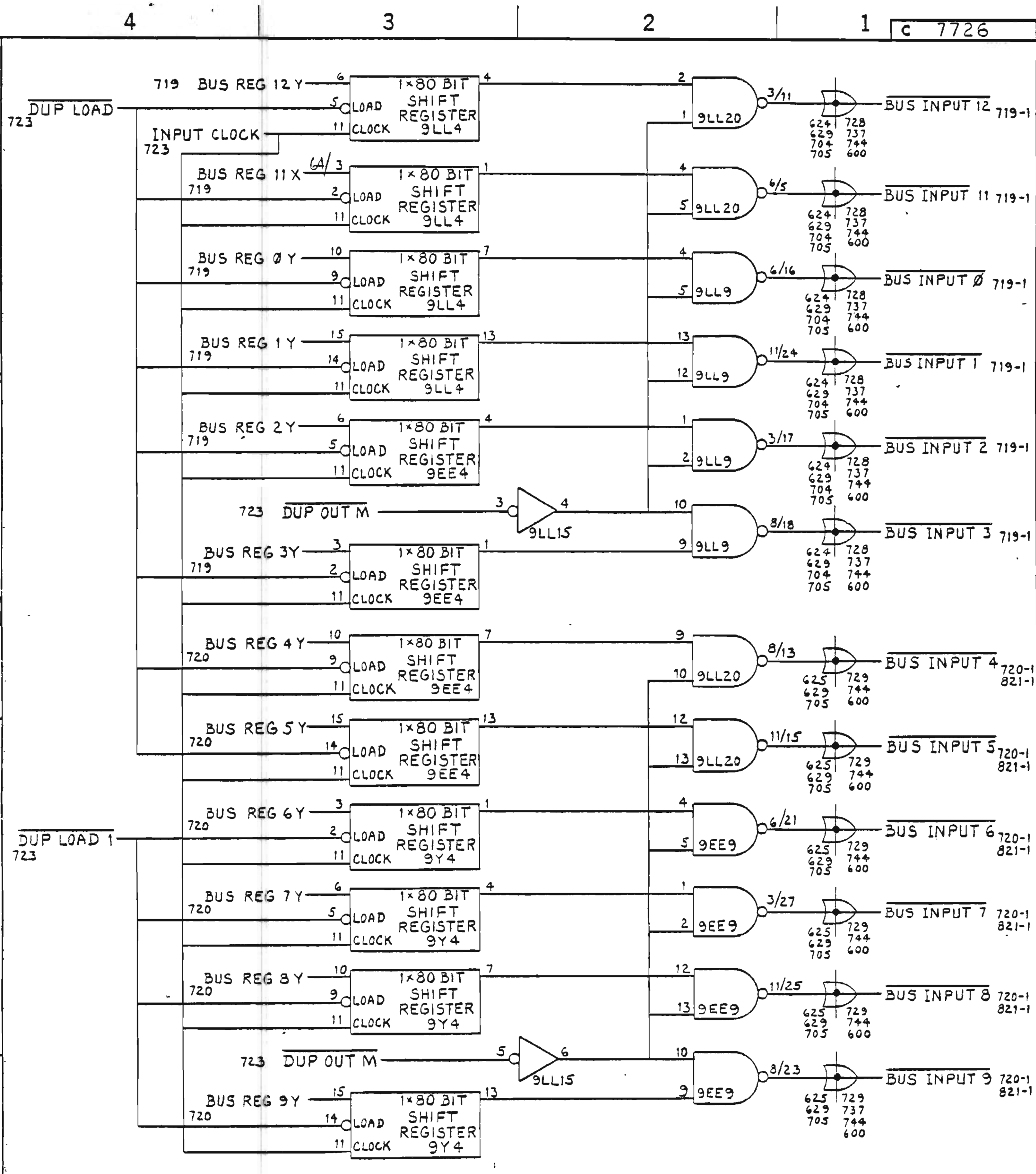
REVISIONS

DRN J. ALESI 10/20/72 ENG 11-13-72
CHK 11/9/72 MFG

TITLE INPUT MEMORY

DWG NO. C 7725 DASH NO. -00 SH 1 OF 1

C 7726



DR80-B-154

A	5108-02	5107-02	3330-1C	25SEP74
–	5108-02	5107-02	1425-2C	11-13-72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

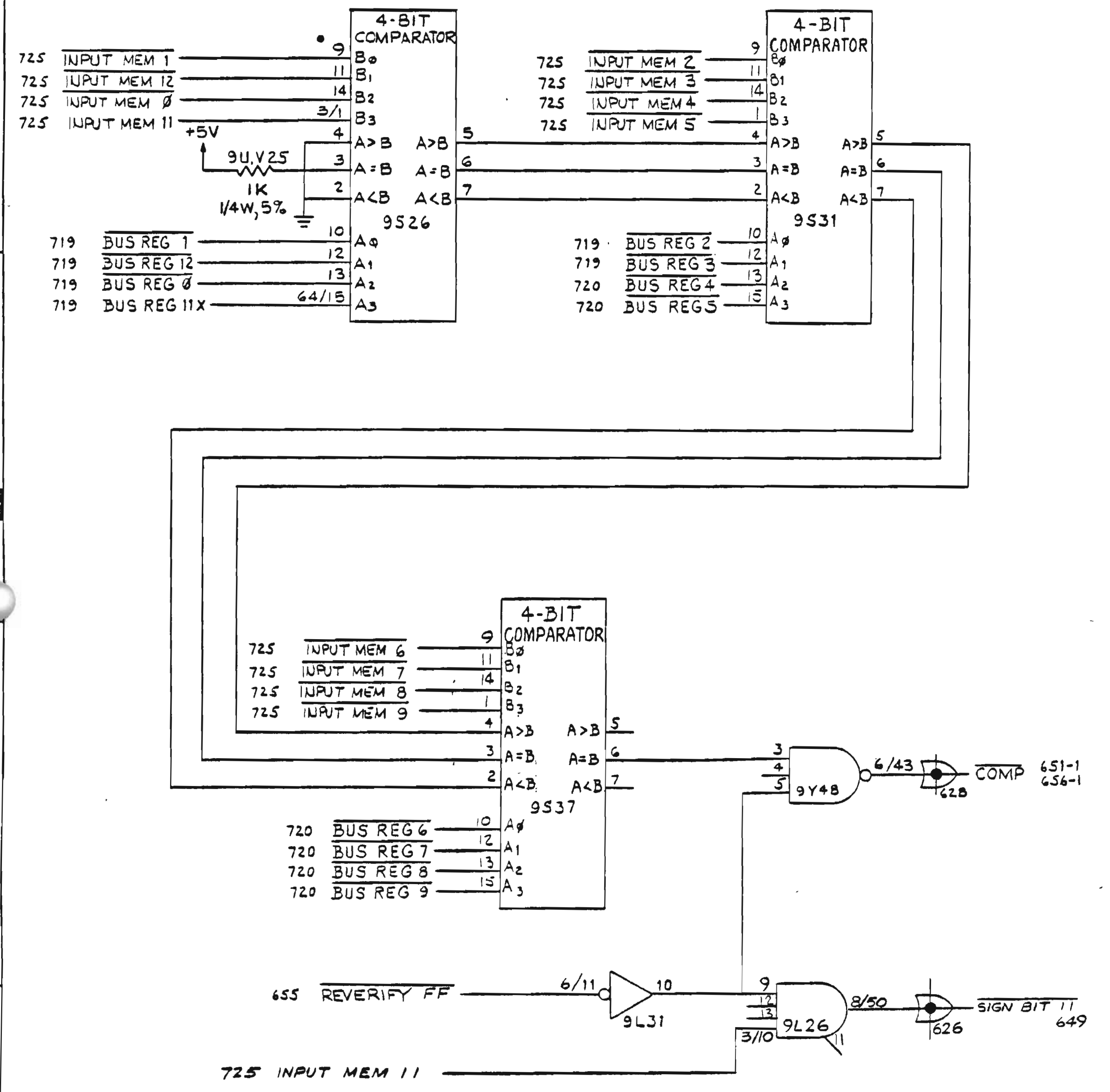
REVISIONS

DRN J. ALESI 10/20/72 ENG 11-3-72
CHK 11/9/72 MFG.

TITLE DUP MEMORY

DWG NO. C 7726 DASH NO. -00 SH 1 OF 1

1 C 7727



REV	COMPASM	ARTWORK	ECN	APPVD-DATE
D	5108-05	5107-05	2288-2C	
C	5108-04	5107-03	1898-1P	3-5-72
B	5108-03	5107-03	1818-2C	12-4-73
A	5108-03	5107-03	1818-1P	1-30-73
-	5108-02	5107-02	1425-2C	11-13-72

REVISIONS

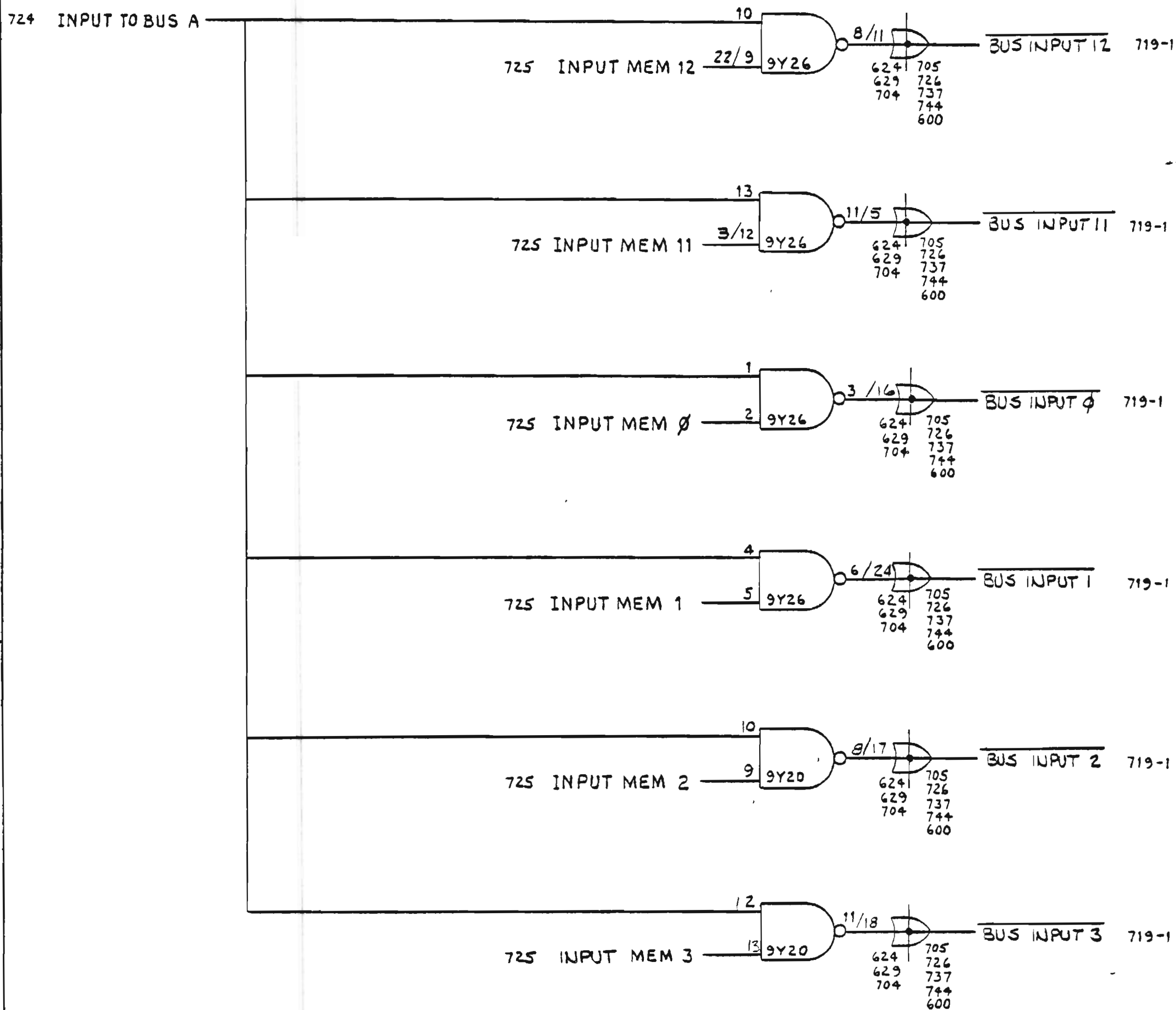
DR80-B-155

DRN J. ALESI 12/20/71 ENG 11-13-72
CHK MFG

TITLE VERIFY COMPARATOR

DWG NO. C 7727 DASH NO. -00 SH 1 OF 1

C 7728



DR80-B-156

REV	COMP ASM	ART WORK	ECN	APPVD-DATE
A	5108-04	5107-03	1898-1P	3-5-73
-	5108-02	5107-02	1425-2C	11-13-72

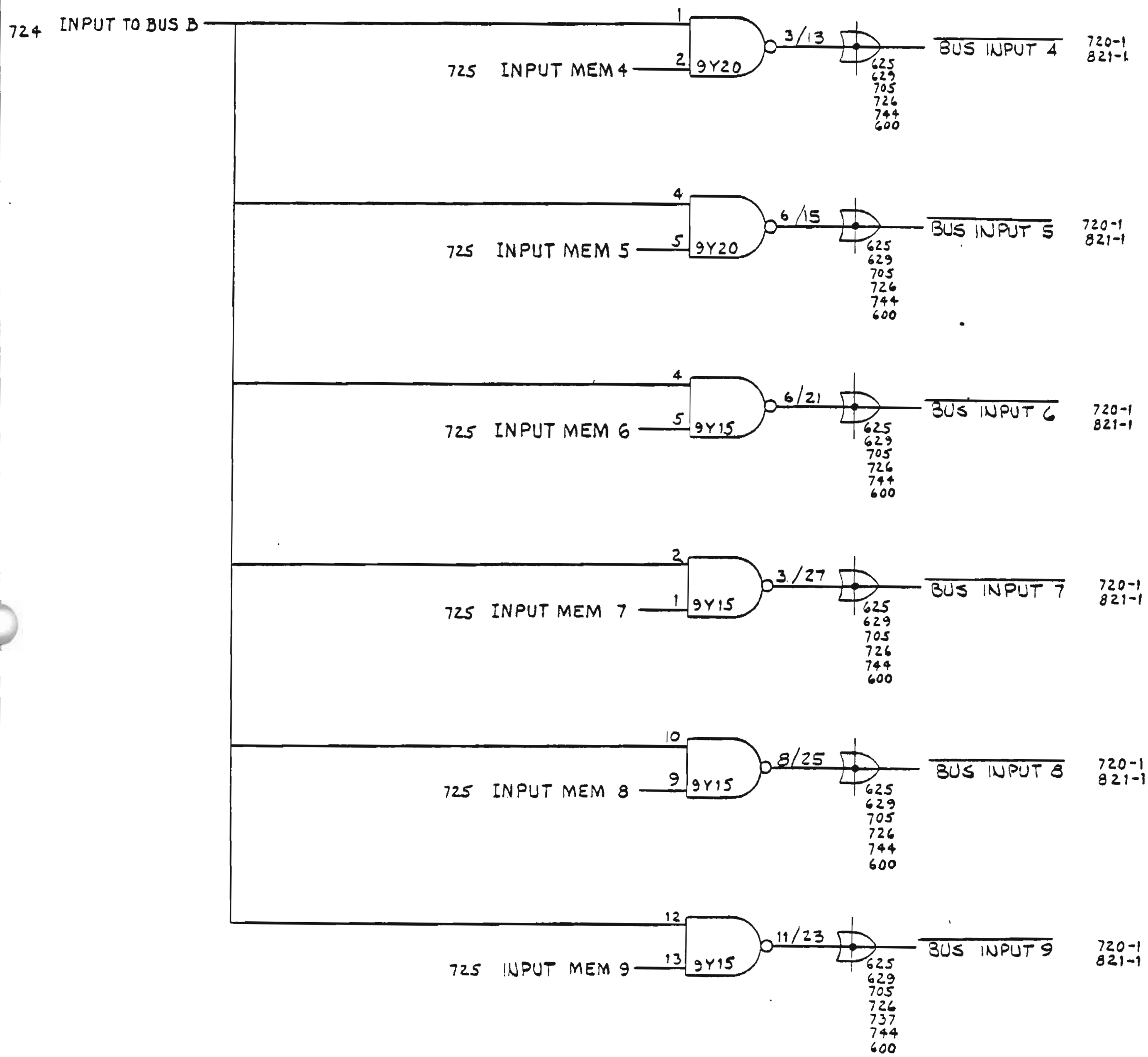
REVISIONS

DRN J. ALESI 12/15/71 ENG 11-13-72
CHK 4/2/72 MFG

TITLE MEMORY TO BUS, PART 1

DWG NO. C 7728 DASH NO. -00 SH 1 OF 1

C 7729



PR80-B-157

REV	COMP ASM	ART WORK	ECN	APPVD-DATE
–	5108-02	5107-02	1425-2062	11-13-72

REVISIONS

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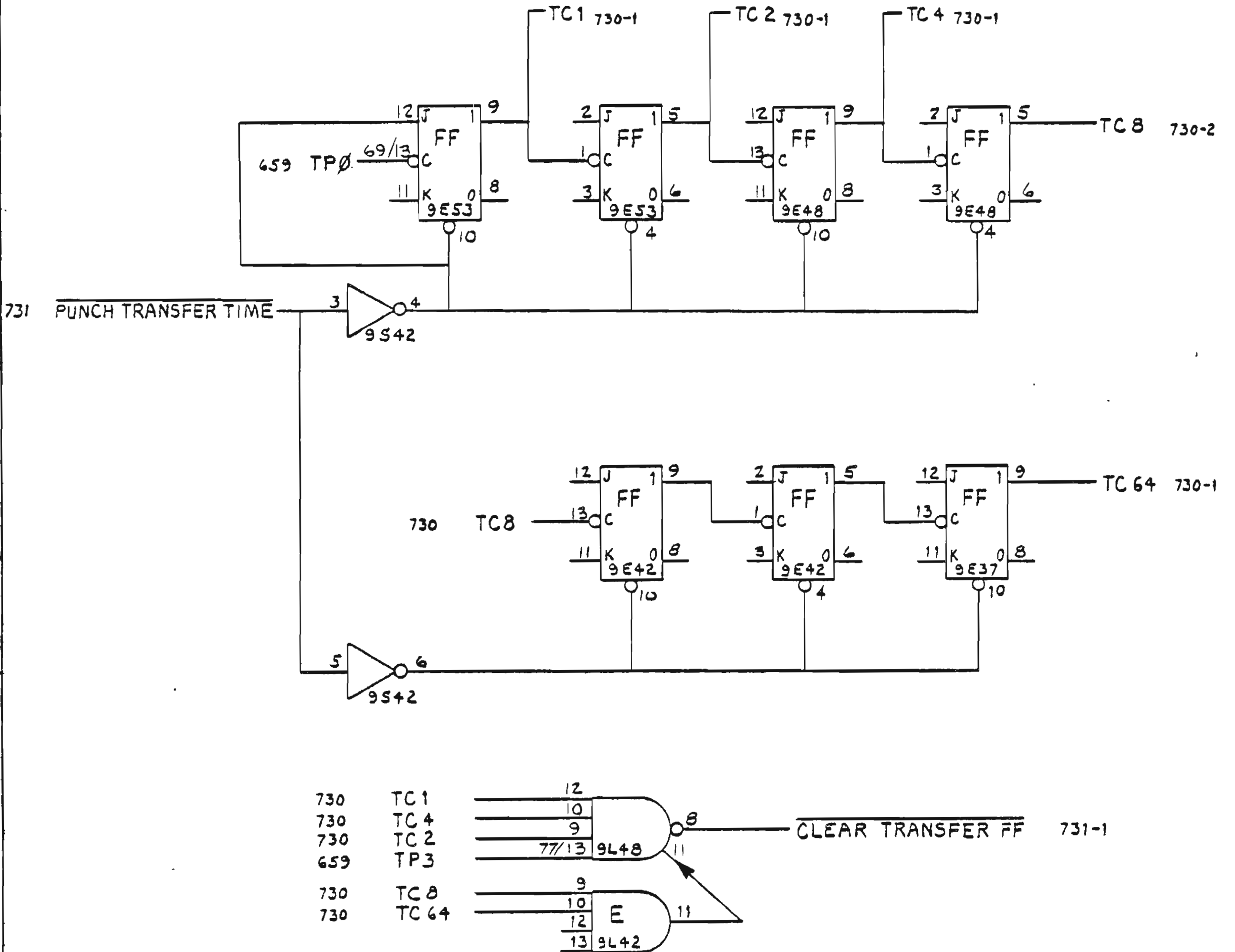
DRN J. ALESI 12/15/71 ENG [signature] 11-13-72
CHK [signature] MFG [signature]

TITLE MEMORY TO BUS, PART 2

DWG NO. C 7729 DASH NO. -00 SH 1 OF 1

C 7730



DR80-B-158

A	5108-05	5107-05	2288-2C	
-	5108-02	5107-02	1425-2C	11-13-72
REV	COMPASM	ART WORK	ECN	APPVD-DATE

REVISIONS

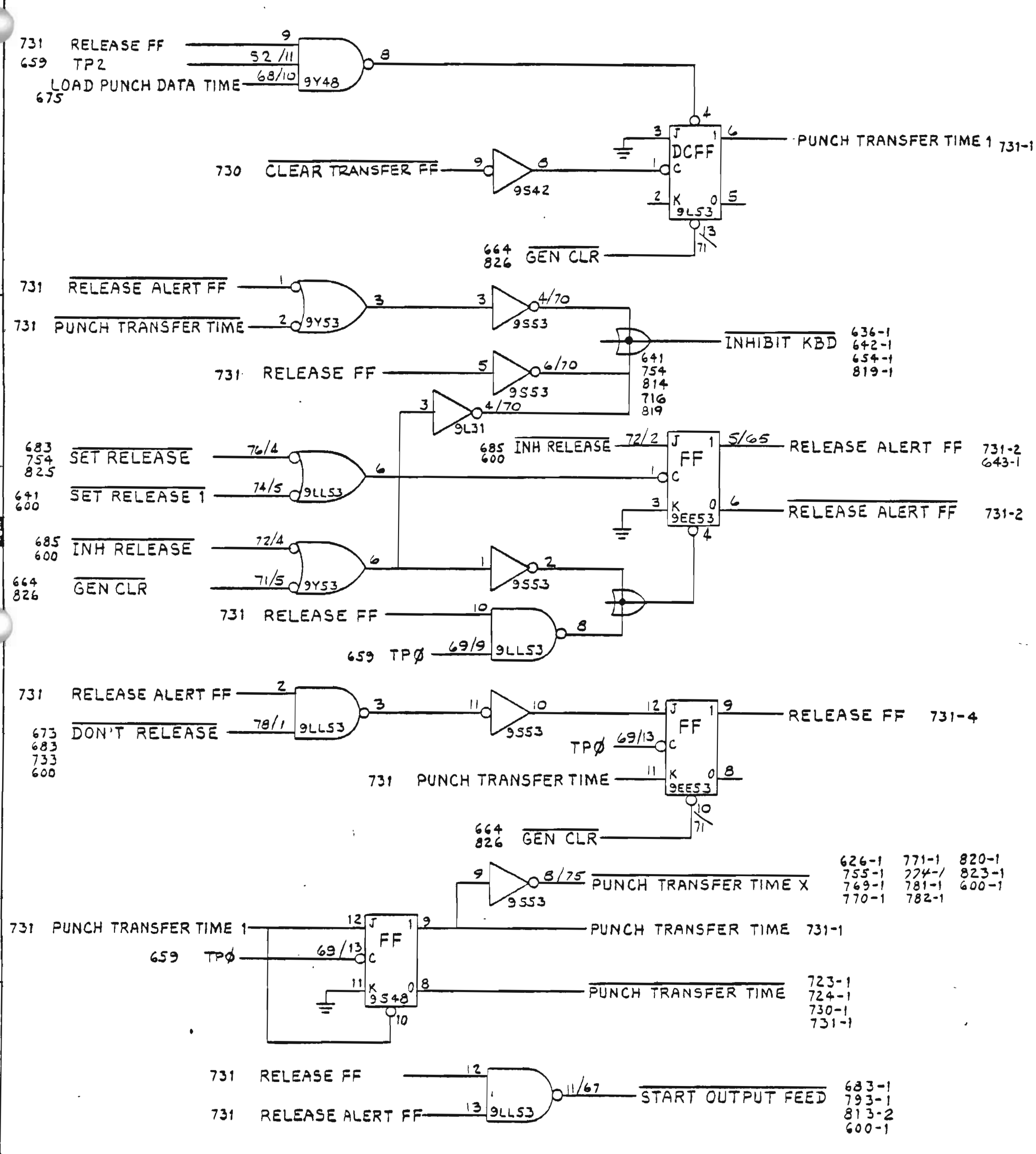
DRN J. ALESI 12/15/71 ENG

CHK MFG

TITLE PUNCH TRANSFER COUNTER

DWG NO. C 7730 DASH NO. -00

SH 1 OF 1



REV	COMPASM	ART WORK	ECN	APPVD-DATE
C	5108-03	5107-03	2019-1P	5-10-73
B	5108-03	5107-03	1818-2C	2-27-73
A	5108-03	5107-03	1818-1P	1-30-73
-	5108-02	5107-02	1425-2C	11-13-72

REVISIONS

E	5108-06	5107-05	4536-4P	
D	5108-05	5107-04	2288-1P	28 JUN 73

DR80- B-159

DRN J. ALESI 12/14/71 ENG 1-13-72
CHK MFG

TITLE RELEASE, TRANSFER CONTROL

DWG NO. C 7731 DASH NO. -00 SH 1 OF 1

REV	DESCRIPTION	APPVD DATE
G	ADDED THIS SHT E/N 2258-2C	[illegible]
H	[illegible]	[illegible]
J	RELEASE -06 E/N 4556-2C	[illegible]

LOGIC DIAGRAM CHART

DWG. NO.	REV
7719	A
7720	-
7721	A
7722	-
7723	A
7724	D
7725	B
7726	A
7727	D
7728	A
7729	-
7730	A
7731	B

*UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS, OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 5108-05 ON ARTWORK 5107-05

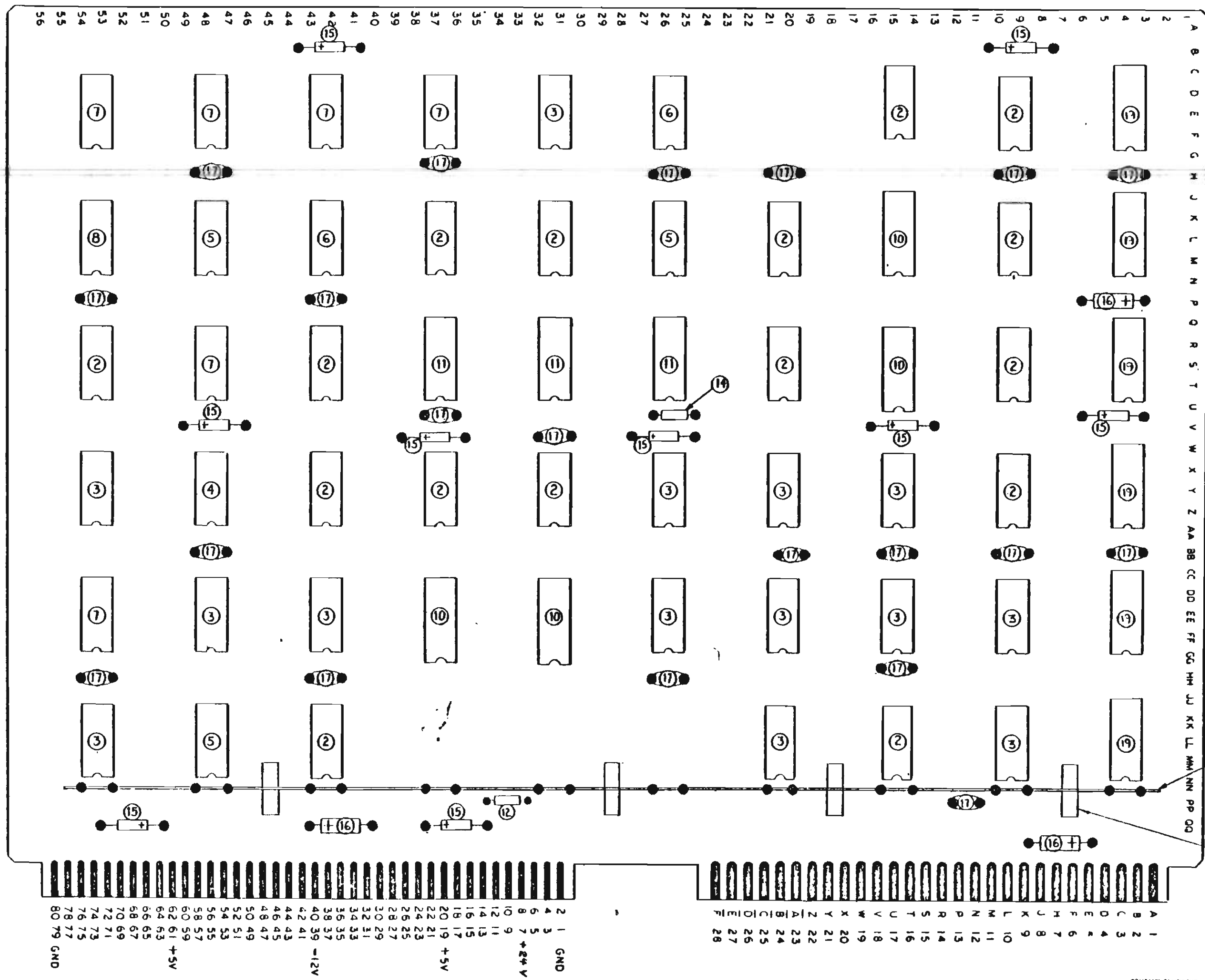
* VARIATION REFERENCE

VAR	ASSEMBLY	ARTWORK
I	5108-02,03,04,4,05	5107-02
II	5108-03,04,4,05	5107-03
III	5108-04,4-05	5107-04
IV	5108-05	5107-05
	5108-06	5107-05
	5108-06	5107-06

9H4, 9H9, 9H20, 9H26, 9G,H37, 9H48, 9BB15, 9P42, 9P53, 9BB4, 9BB9, 9BB20, 9U,V37, 9BB48, 9GG,HH15, 9HH26, 9V,W31, 9HH42, 9HH53, VAR I,II,III 9H31, VAR IV 9PP11 — +5V — .02 µf 25V, +80% -30%

9B9, 9B42, 9V,W37, 9V14, 9V,W26, 9U,V4, 9V47, 9QQ36, 9QQ51 — +5V — 4.7 µf 10V, 20%

9P5, 9QQ41, 9RR7 — -12V — 2.2 µf 25V, 20%



TYPE	LOC	OUTPUT PIN NO.
DUAL FLIP-FLOP	E37	5,6
EXPANDER	L42	4
DCFF	L53	8,9

SPARE GATES

FOR LIST OF PARTS SEE DETACHED PARTS LIST

DECISION DATA COMPUTER CORP.

COMPONENT ASSEMBLY INPUT MEMORY

D 5108 DASH NO. * SH 4

5108 D

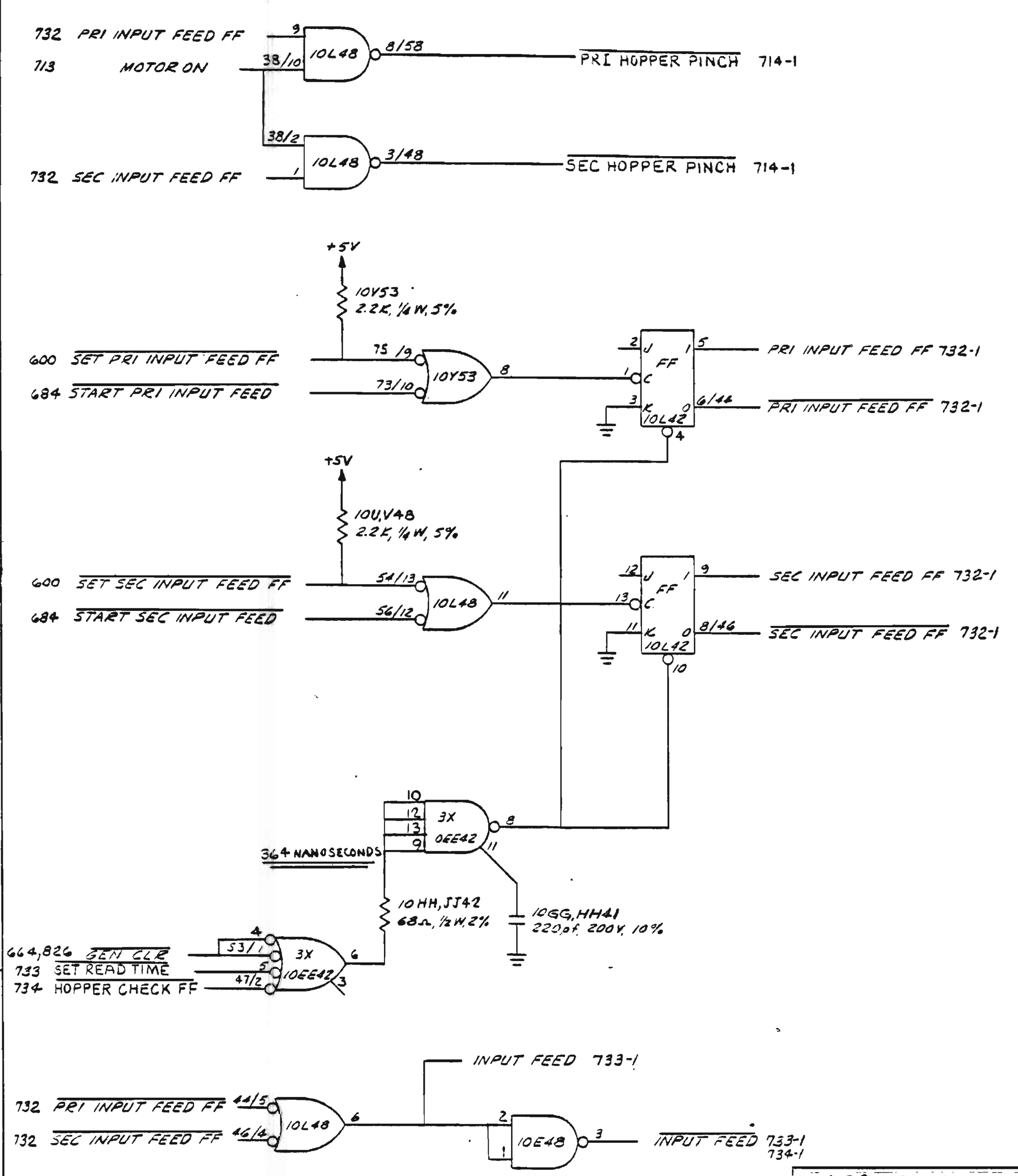
LOGIC/SCHEMATIC INDEX

MANUAL	TYPE/FEATURE		
7545	8001/8010 DR 80 Col	PWB	5109-06
902193	8010-11 CR 360	ASM	5110-06

DOCUMENT	SH	RV	DESCRIPTION
			READ CONTROL
7732		-D	Input Feed Flip Flops
7733		-E	Card in Read Station Controls
7734		-D	Input Error Flip Flops
7735		-G	Read Col/Timing Counters
7736		-C	Read Program Data Recovery
7737		-C	Read Program Register
7738		-C	Read Timing Gates
7739		-B	Read Station Pt-1
7740		-A	Read Station Pt-2
7741		--	Read Register Pt-1
7742		--	Read Register Pt-2
7743		-B	Light/Dark/Strobe Check Gates
7744		-D	Read Data Flip Flop Output Gates
5110	02	-K	Schematic - Read Control

LOCATION: 10

C 7732



DR80-B-162

REV	COMP ASM	ART WORK	ECN	APPVD DATE
D	5110-06	5109-06	5664-1C	
C	5110-06	5109-06	3162-3C	
B	5110-04	5109-04	2163-2C	
A	5110-03	5109-03	1638-2P	
-	5110-0	5109-0	1426-2C	

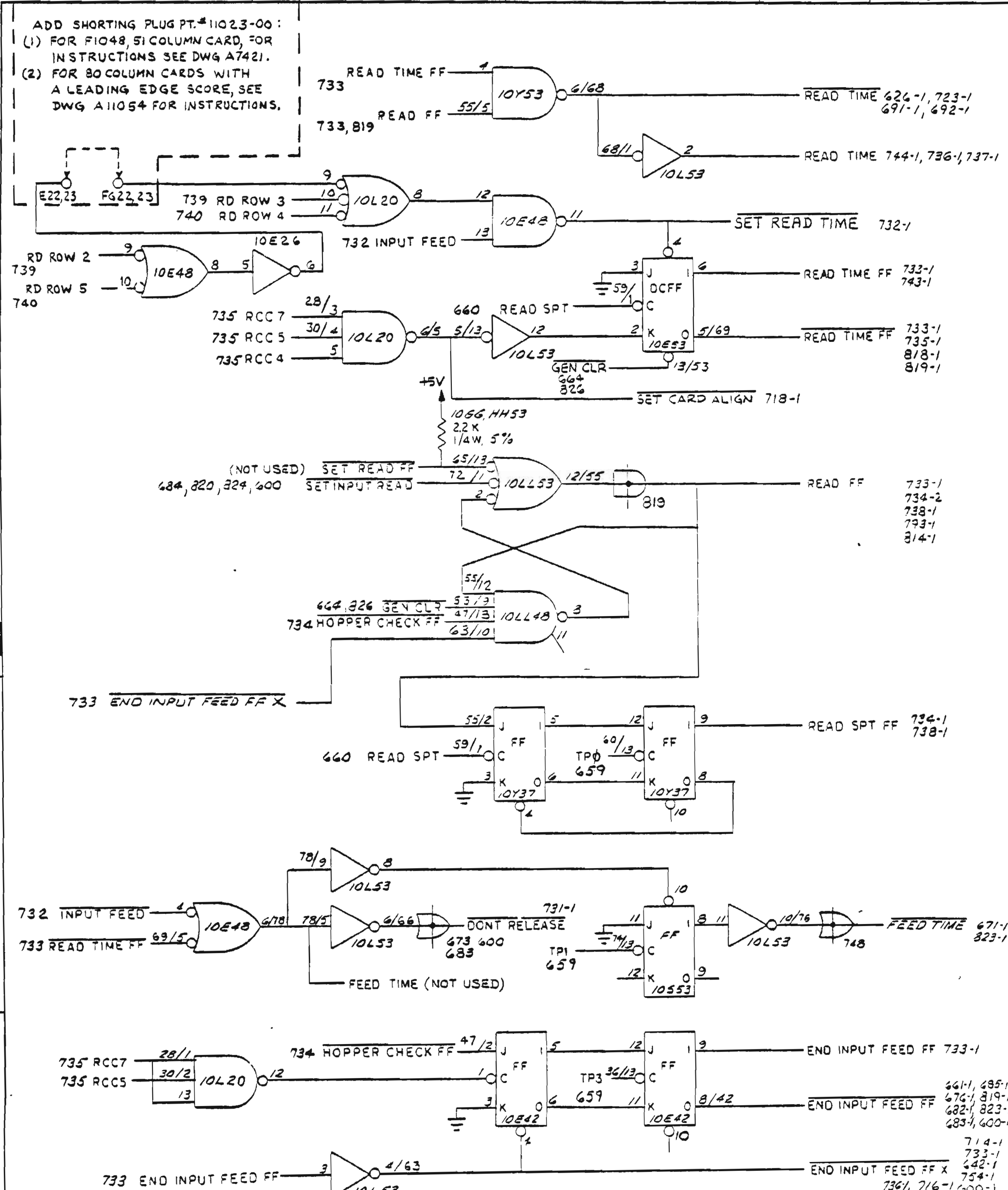
REVISIONS

DRN | CHK | ENGR | MFG

TITLE INPUT FEED FLIP FLOPS

DWG NO. C 7732 DASH NO. -00 SH 1 OF 1

C 7733



C	5110-04	5109-01 5109-03	2163-1P	6-12-73
B	5110-03	5109-01	1688-2P	1-31-73
A	5110-02	5109-01	1688-1P	12-29-72
–	5110-01	5109-01	1426-2C	11-3-72
REV	COMPASM	ART WORK	ECN	APPVD-DATE

REVISIONS

E	5110-06	5109-06	3162-36	6 JAN 74
D	5110-06	5109-04	3162-2P	1 JAN 74

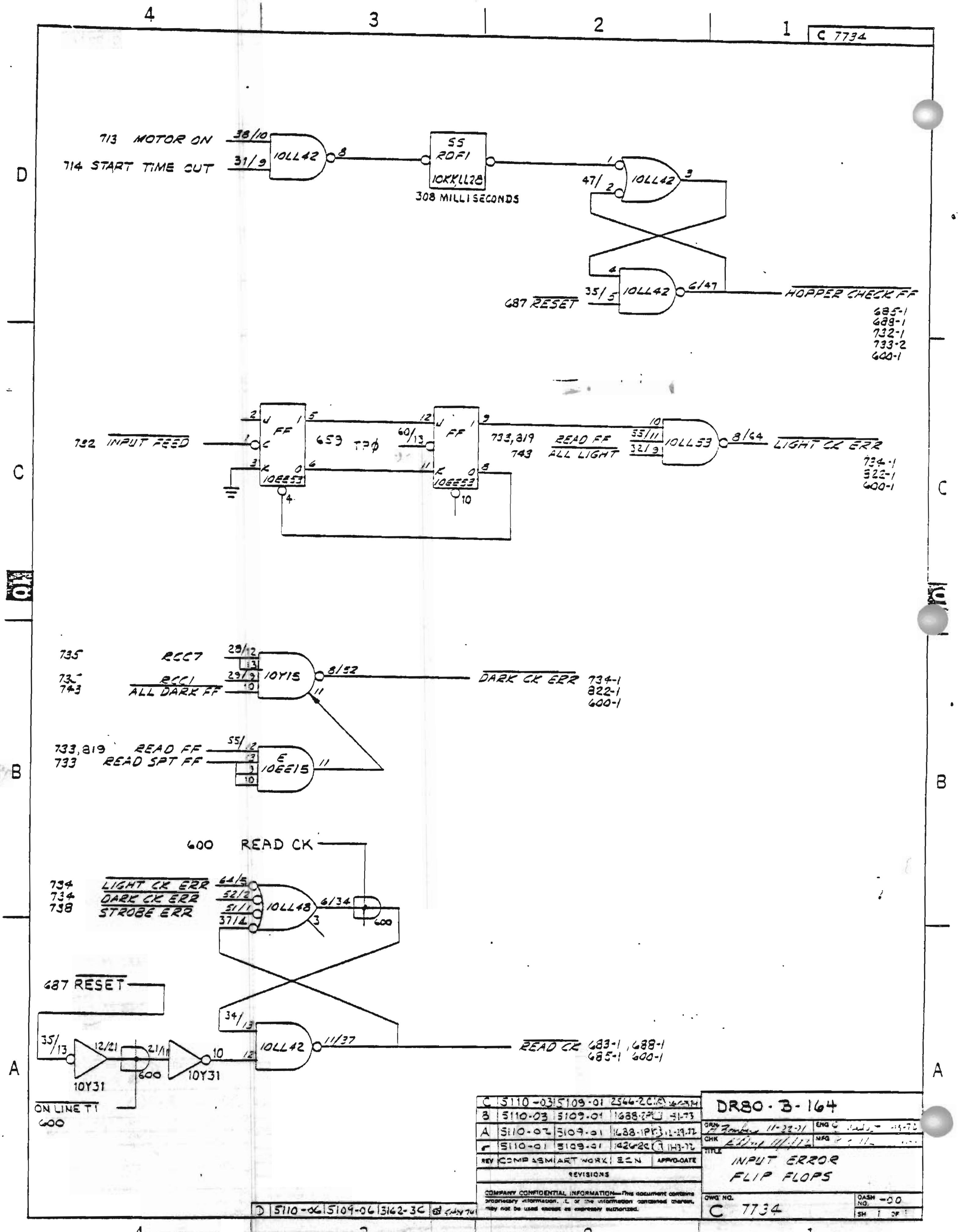
DR 80 - B - 163

DRN 11/5/71 ENG 11-3-72

CHK MFG

TITLE CARD IN READ STATION CONTROLS

DWG NO. C 7733 DASH NO. -00 SH 1 OF 1


713 MOTOR ON
714 START TIME OUT
10LL42
SS 20F1 10KKLL28
308 MILLISECONDS
10LL42
687 RESET
10LL42
HOPPER CHECK FF
685-1 688-1 732-1 733-2 600-1
732 INPUT FEED
FF 10EE53
653 TPφ
FF 10EE53
733,819 743
READ FF
ALL LIGHT
10LL53
LIGHT CK ERR
734-1 822-1 600-1
735 RCC7
733 743 RCC1 ALL DARK FF
10Y15
DARK CK ERR 734-1 822-1 600-1
733,819 READ FF
733 READ SPT FF
E 10EE15
600 READ CK
734 LIGHT CK ERR
734 DARK CK ERR
738 STROBE ERR
10LL48
600
687 RESET
10Y31
600
10Y31
10LL42
READ CK 683-1, 688-1 685-1 600-1
ON LINE T1 600
DR80 - B - 164
INPUT ERROR FLIP FLOPS
REVISIONS
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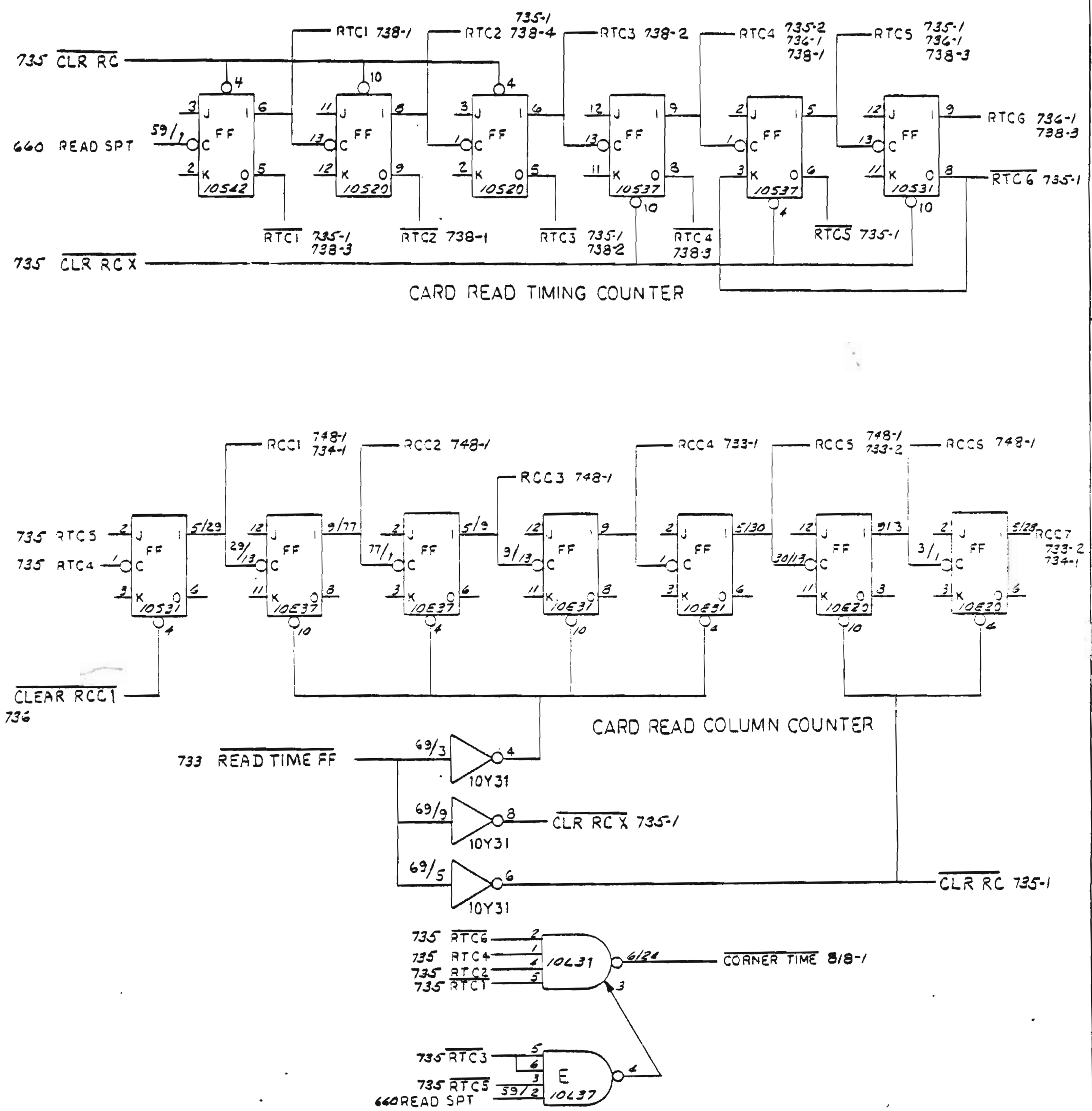
DWG NO. C 7734
DASH NO. -00
C 7734

4	3	2	1

C 7735



C	5110-04	5109-03	2163-1P 6-14-73
B	5110-03	5109-03	1688-3C
A	5110-02	5109-01	1688-1P
–	5110-01	5109-01	1426-2C
REV	COMP ASM	ART WORK	ECN	APPVD-DATE

REVISIONS

G	5110-06	5109-06	3162-3C	
F	5110-06	5109-04	3162-2P	
E	5110-05	5109-04	3162-1P	
D	5110-04	5109-03	3120-1C	

DR80-B-165

DRN 11/5/71
CHK

TITLE
READ COLUMN COUNTER & TIMING COUNTER

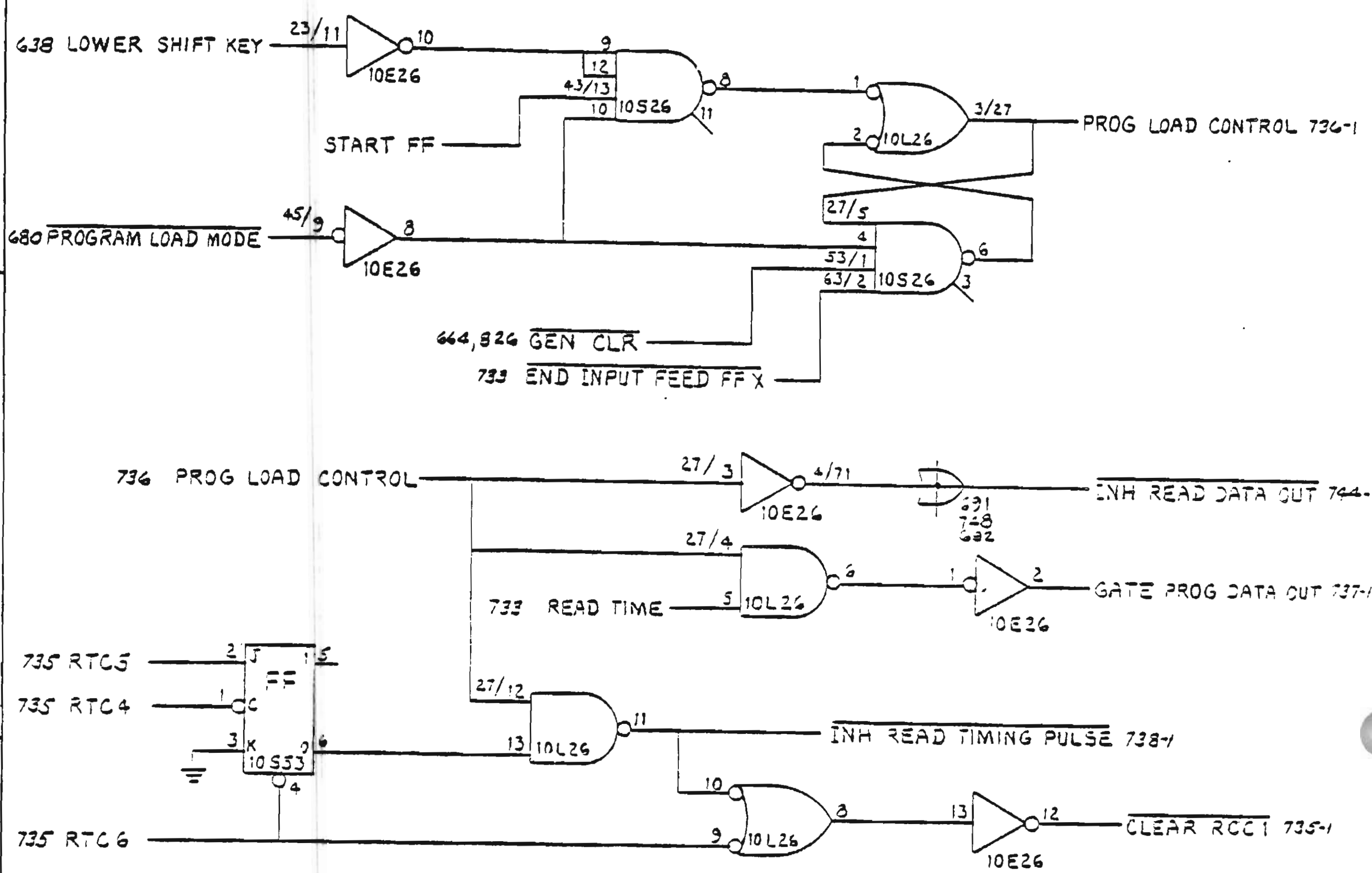
DWG NO. C 7735

DASH NO. -00

SH 1 OF 1

4	3	2	1

C 7736



C	5110-06	5109-06	3162-3C	
3	5110-04	5109-03	3120-1C	
A	5110-04	5109-03	2163-1P	6-4-73
-	5110-01	5109-01	1426-2C	11-13-72
REV	COMP ASM	ARTWORK	ECN	APPD-DATE

REVISIONS

DR80-B-166

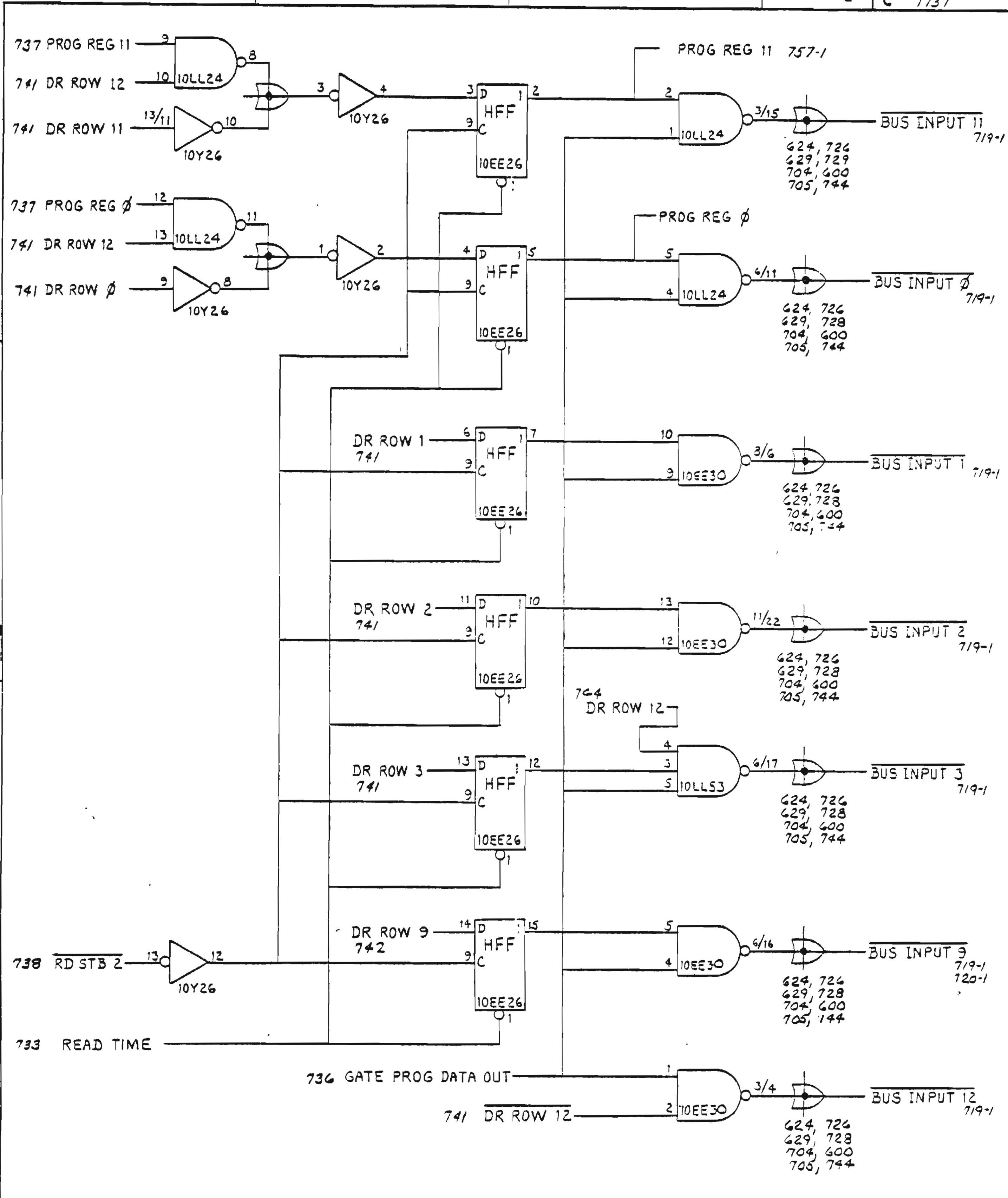
DRN J. ALESI 10/17/72 ENG

CHK MFG

TITLE READ PROGRAM DATA RECOVERY

DWG NO. C 7736 DASH NO. -00 SH 1 OF 1

C 7737



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	5110-06	5109-06	3162-3C	
B	5110-04	5109-04	3120-1C	
A	5110-04	5109-04	2163-2C	AUG 73
-	5110-01	5109-01	1426-2C	

REVISIONS

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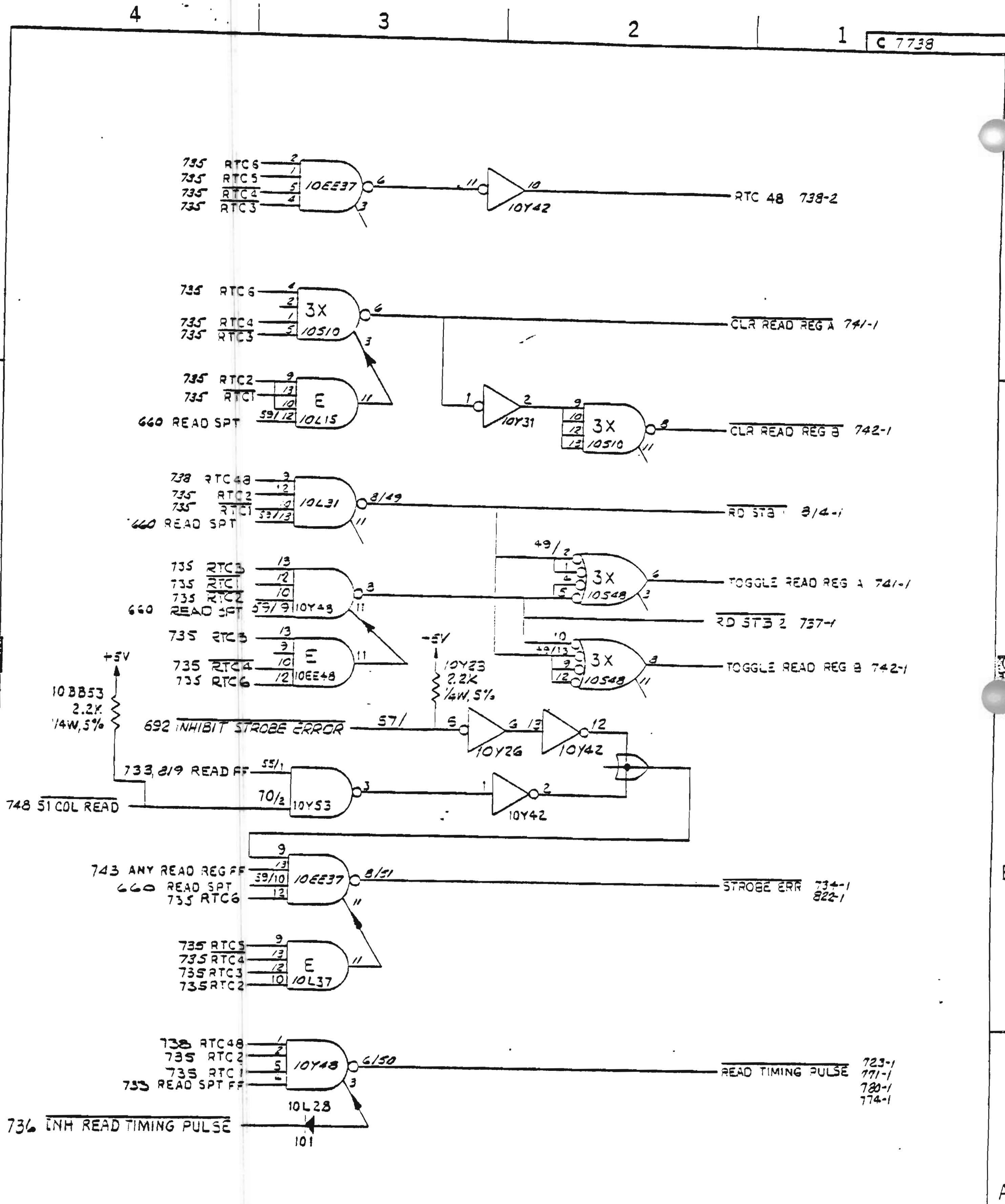
DR80 - B - 167

DRN J. ALESI 7/72

TITLE READ PROGRAM REGISTER

DWG NO. C 7737 DASH NO. -00 SH 1 OF 1



C	5110-06	5109-06	3162-3C	
B	5110-04	5109-04	2163-2C	6 AUG 73
A	5110-04	5109-01 5109-02	2163-4A	6-14-73
—	5110-01	5109-01	1426-2C	11-13-72
REV	COMP ASM	ART WORK	ECN	APPVD-DATE

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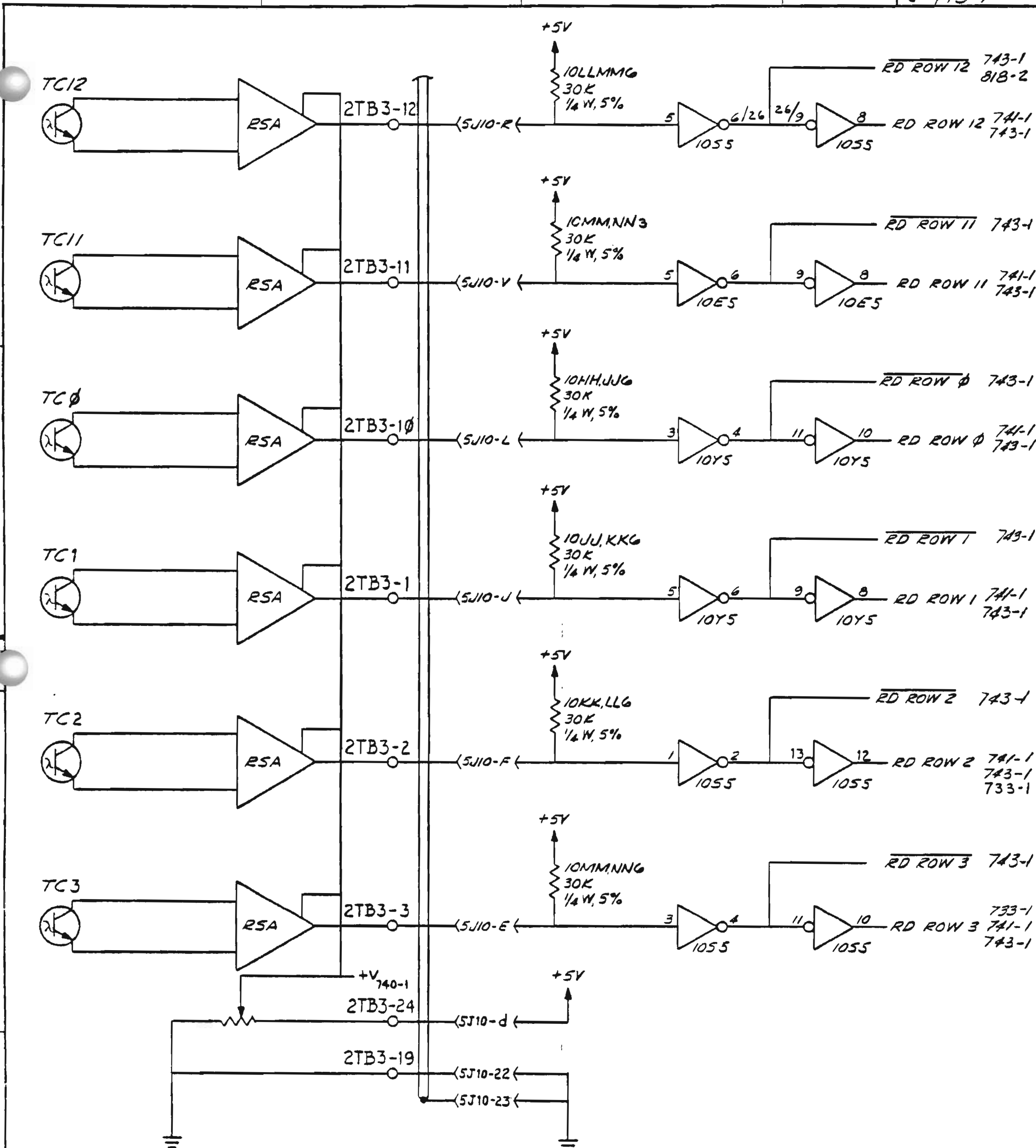
DR80-B-168

DRN 11/5/71 ENGG
CHK MFG

TITLE READ TIMING GATES

DWG NO. C 7738 DASH NO. -00 SH 1 OF 1

C 7739



B	5110-06	5109-04	5444-1C	
A	5110-06	5107-04	3162-2H	
–	5110-01	5109-01	1426-2C	
REV	COMP ASM	ART WORK	ECN	APPVD-DATE

REVISIONS

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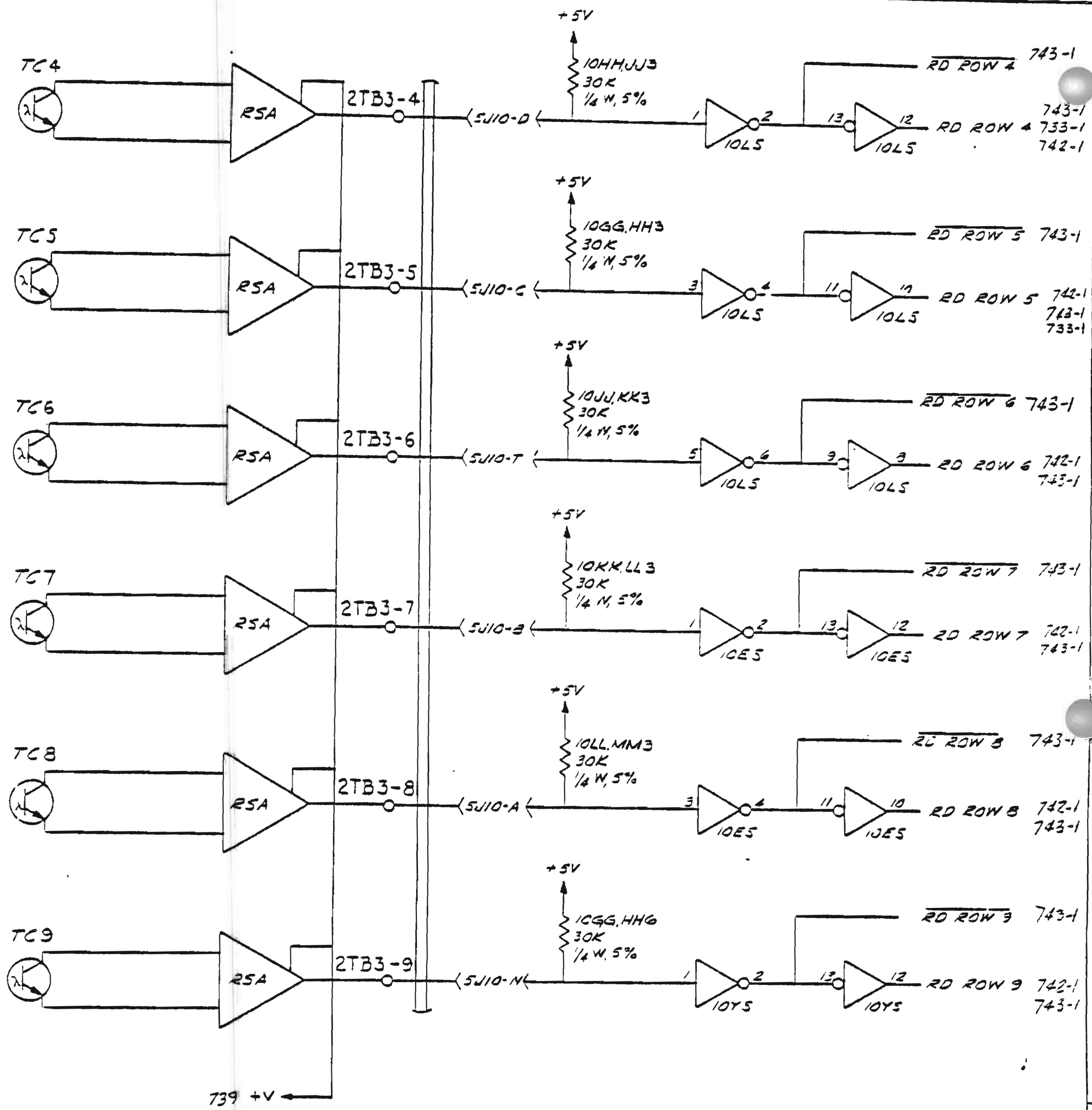
DR80-B 169

DRN 11-22-71 ENG 1-13-72 CHK MFG

TITLE READ STATION PART 1

DWG NO. C 7739 DASH NO. -00 SH 1 OF 1



C 7740

DR80-B-170

REV	COMP ASM	ART WORK	ECN	APPVD-DATE
A	5110-06	5109-04	3162-2A8	
—	5110-01	5109-01	11426-2C	

REVISIONS

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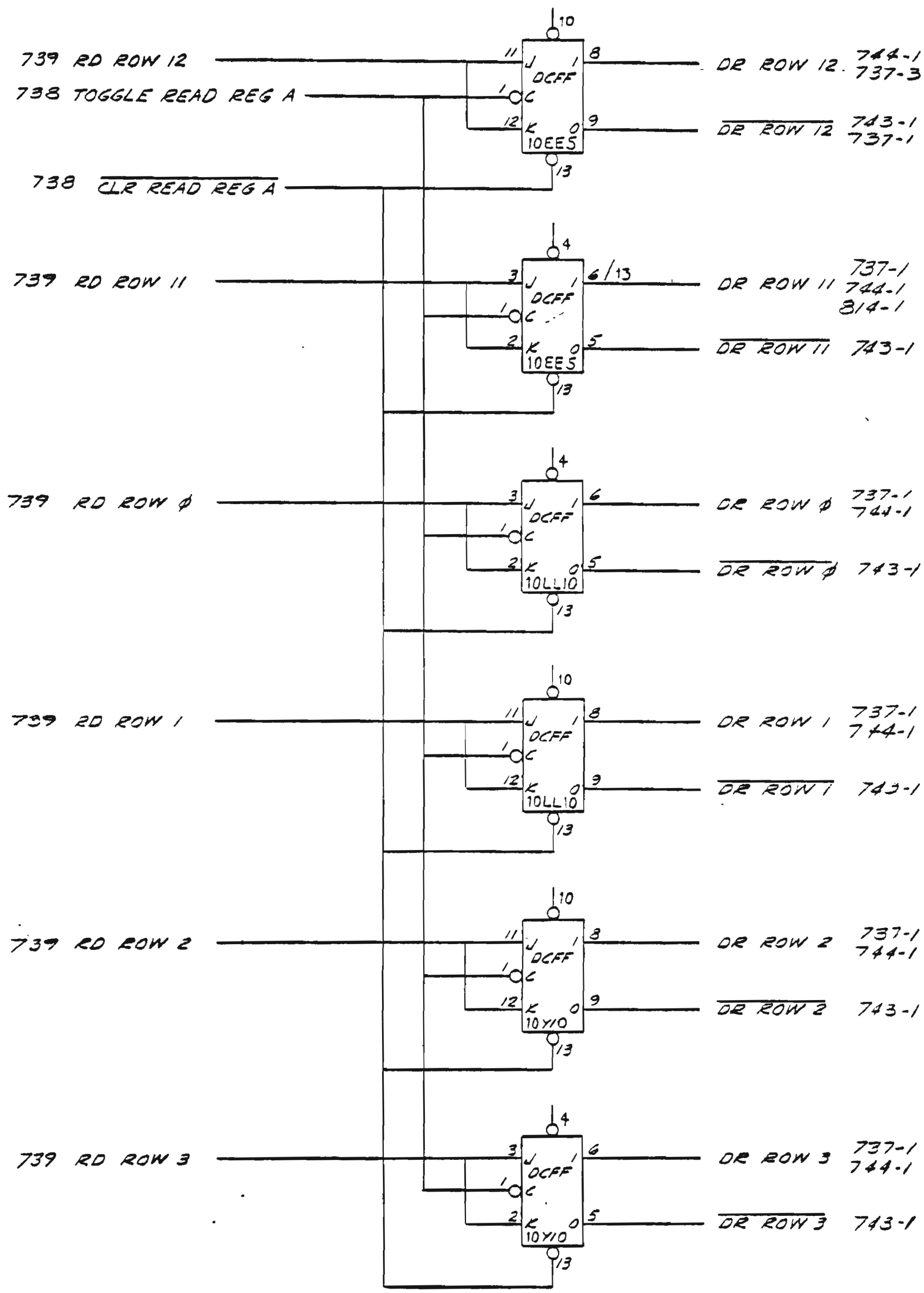
DRN 11-19-71
CHK

TITLE: READ STATION PART 2

DWG NO. C 7740

DASH NO. -00

C 7741



REV	COMP ASM	ART WORK	ECN	APPVD-DATE
-	5110-01	5109-01	426-20	3-1-72

REVISIONS

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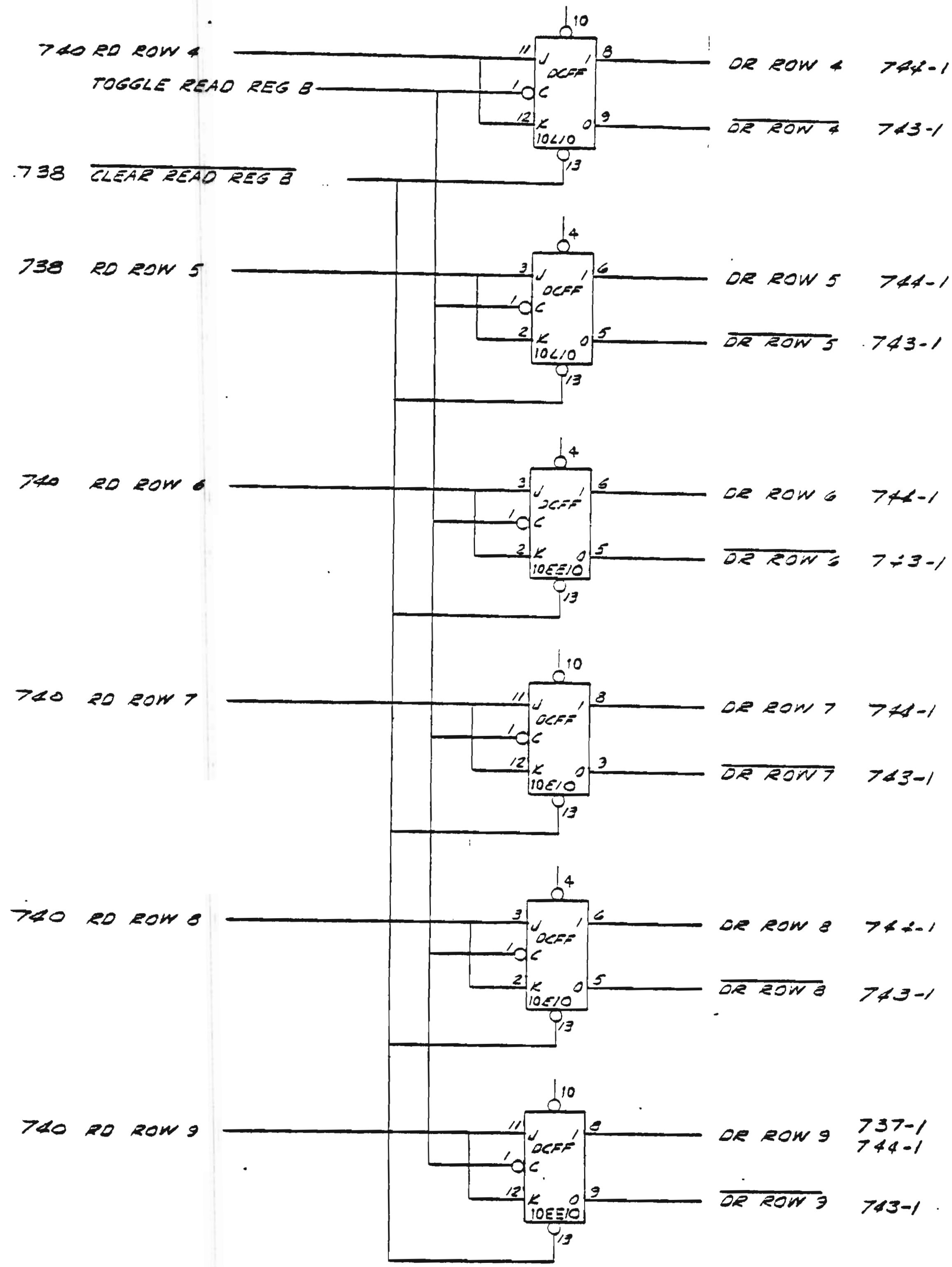
DR80 - B - 171

DRN 11-22-71 CHK ENG MFG

TITLE READ REGISTER, PART I

DWG NO. C 7741 DASH NO. -00 SH 1 OF 1

C 7742



DR80-B-172

REV	COMPASM	ART WORK	ECN	APPVD-DATE
-	5110-01	5109-01	1426-2	11-13-72

REVISIONS

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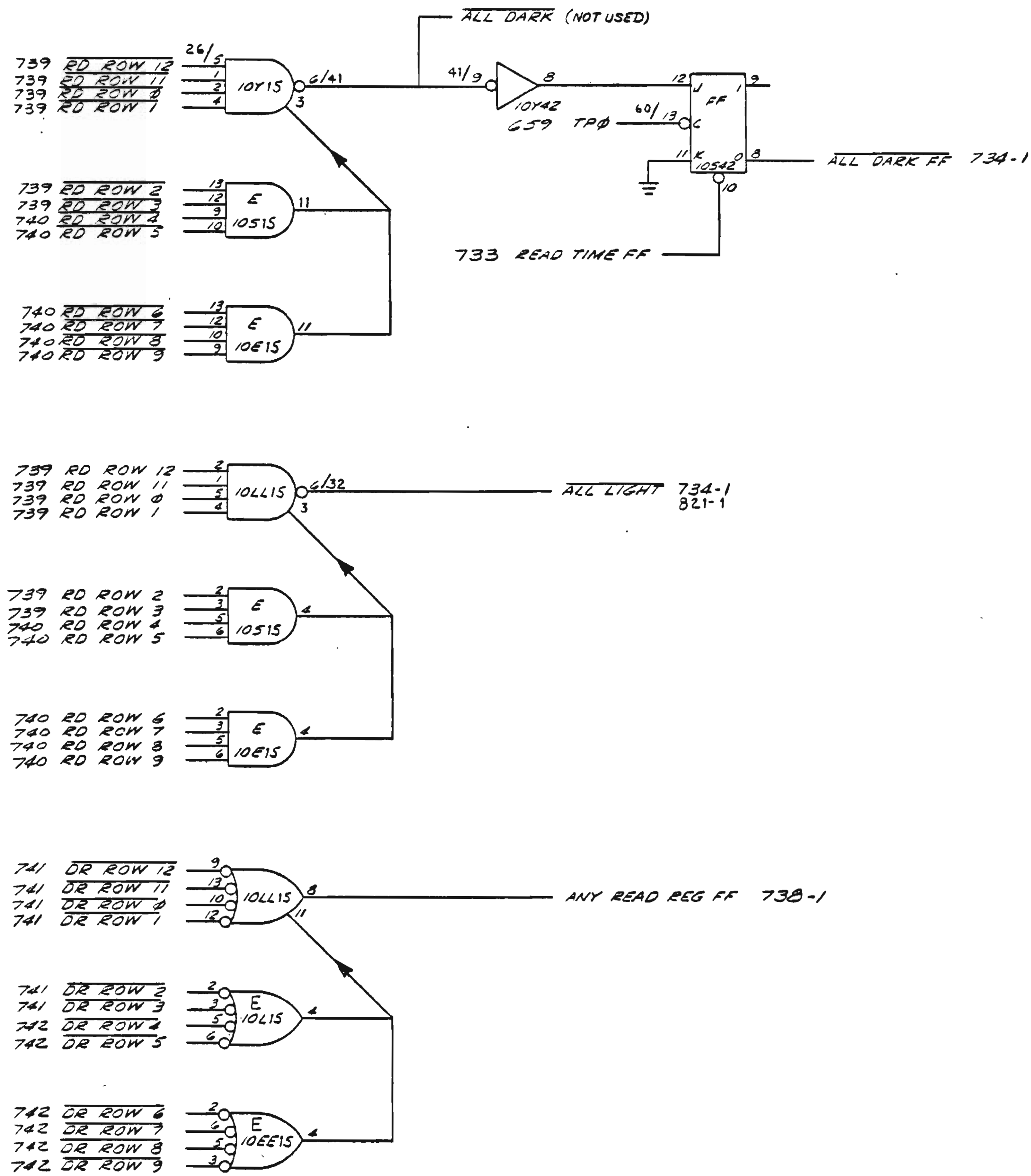
TITLE: READ REGISTER, PART 2

DWG NO. C 7742

DASH NO. -00

SH 1 OF 1

C 7743



REV	COMP ASM	ART WORK	ECN	APPVD-DATE
B	5110-04	5109-03 5109-01	5664-1C	[illegible]
A	5110-04	5103-03 5103-01	5163-1F	[illegible]
-	5110-01	5109-01	1106-2C	[illegible]

REVISIONS

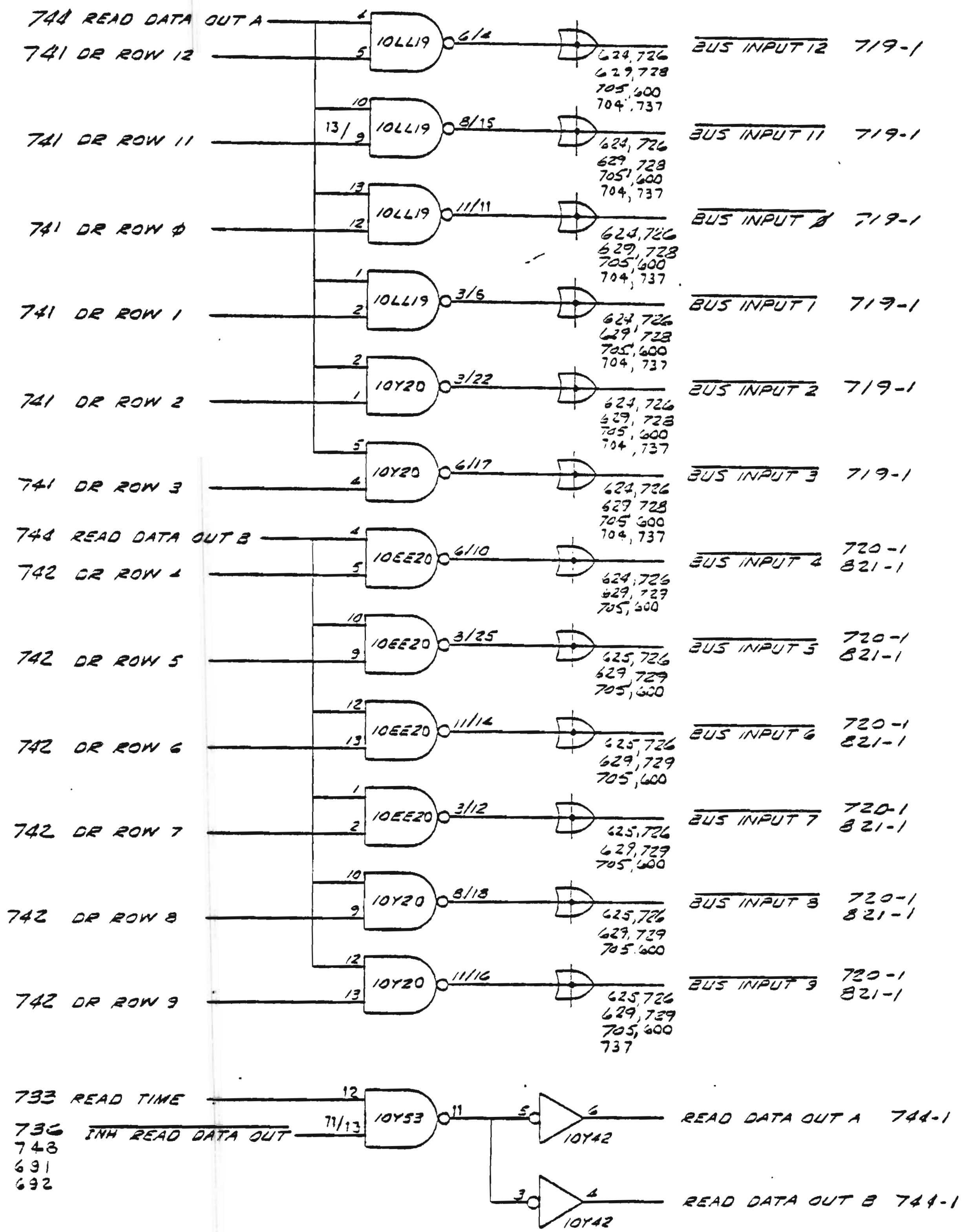
DR80-B 173

DRN [illegible] 11-22-71 ENG [illegible] 11-13-72
CHK [illegible] MFG [illegible]

TITLE LIGHT DARK & STROBE CHECK GATES

DWG NO. C 7743 DASH NO. -00 SH 1 OF 1

C 7744



REV	COMP ASM	ART WORK	ECN	APPVD-DATE
B	5110-04	5109-04	2163-2C	[illegible]
A	5110-04	5109-01 5109-03	2163-1P	[illegible]
–	5110-01	5109-01	1426-24	[illegible]
D	5110-06	5109-06	3162-3C	[illegible]
C	5110-04	5109-04	3330-1C	2 SEPT 74

REVISIONS

DR80-3-174

DRN [illegible] 11-18-71 ENG [illegible]
CHK [illegible] MFG [illegible]

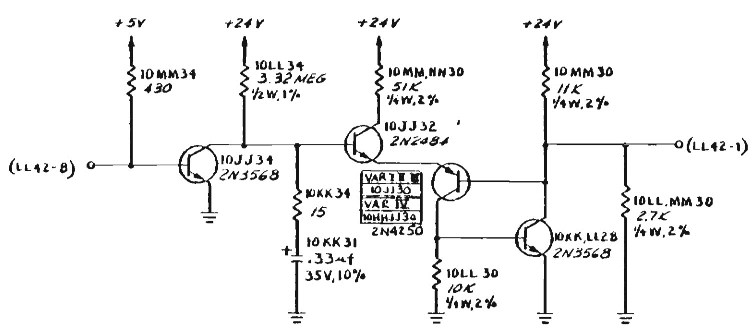
TITLE READ DATA FLIP FLOPS OUTPUT GATES

DWG NO. C 7744 DASH NO. -00 SH 1 OF 1

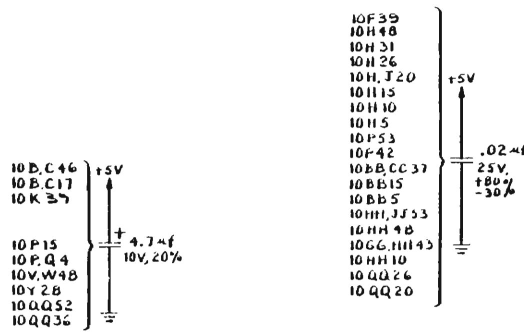
REV	DESCRIPTION	APPVD DATE
A	RELEASE PER ECN 1426-2C	[illegible]
C	REVISED PER ECN 1605-3C	[illegible]
E	REVISED PER ECN 2163-2C	[illegible]
F	REVISED/ECN 2474-1C	[illegible]
K	REVISED/ECN 3162-3C	[illegible]

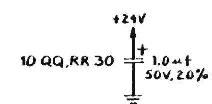
* UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR VARIATIONS LISTED.

* VARIATION REFERENCE		
VAR	ASSEMBLY	ARTWORK
I	5110-01,02,03,04	5109-01
II	5110-03-04	5109-03
III	5110-04,05	5109-04
IV	5110-06	5109-0



RDF1





NOTES:
1. UNLESS OTHERWISE SPECIFIED:
ALL RESISTANCE VALUES ARE IN OHMS, ½W, 2%

DR80- B- 175

COMPONENT ASSEMBLY
READ CONTROL

DWG NO. D 5110

SH 2

0115 a

DR80-B-176

LOGIC / SCHEMATIC CONTENTS

PCB LOC
11

TYPE NUMBER/NAME
BASIC 8001, 8010 INTERFACE

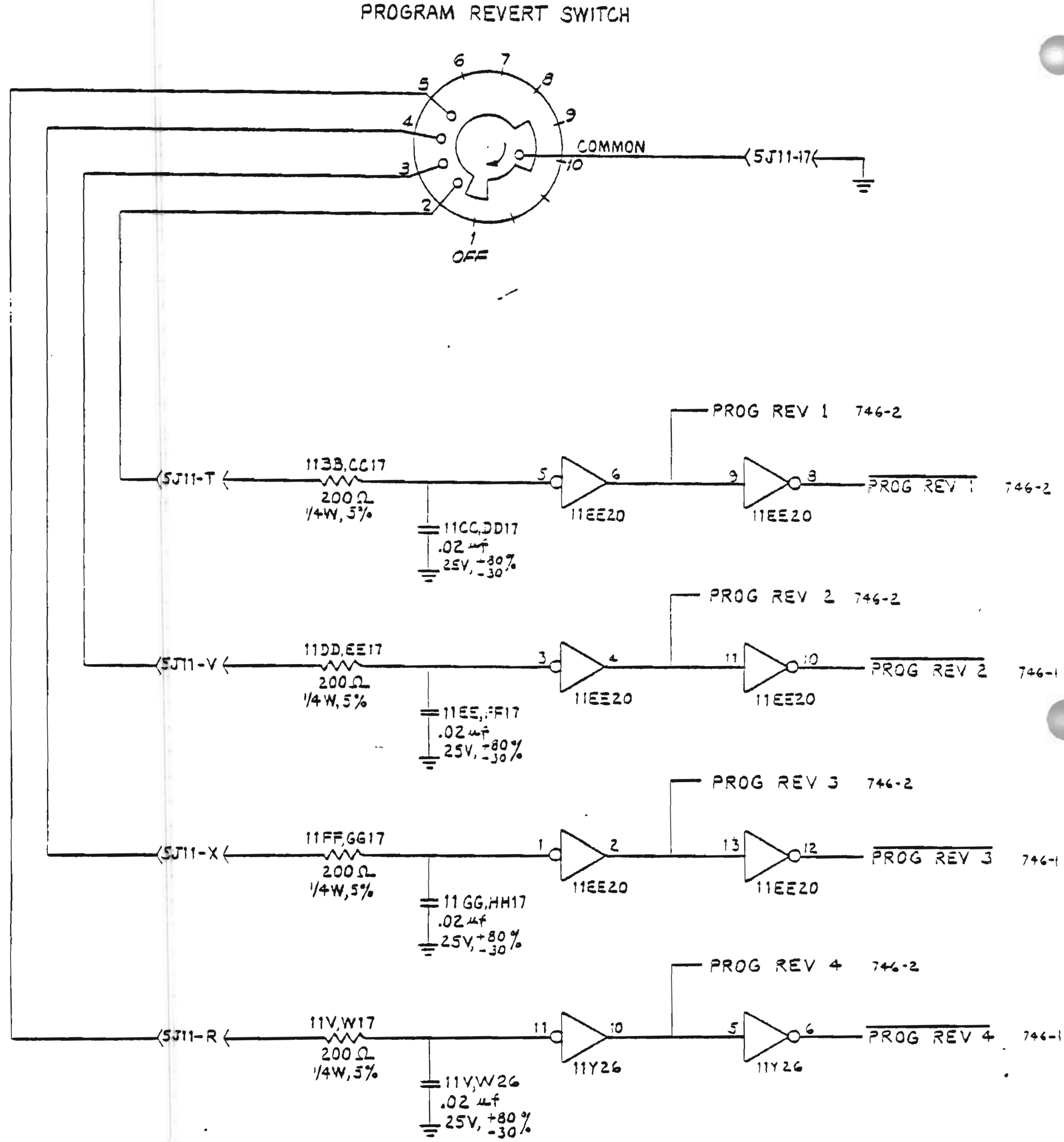
DWG NO	TITLE	PCB/ASM DRAWING DASH NO
7403	PWB	04
7404	PROGRAM REVERT & 51 COL F-1048	04
7440	PROGRAM REVERT	02

LOGIC SCHEMATIC	LOGIC DRAWING TITLE SCHEMATIC SHEET NO	LOGIC/SCHEMATIC DRAWING REVISION
7745	PROGRAM REVERT ROTARY	A
7746	PROGRAM REVERT SELECTION	-
7747	PROGRAM REVERT CONTROL	A
7748	51 COL. READ CONTROL	E
7749	" " KEYING CONTROLS	-
7750	" " PUNCH/PRINT DATA TRANSFER	-
7751	" " CHECK HOLD CONTROL	A
7752	" " PUNCH COLUMN DECODES	-
7753	" " STACKER CONTROL	C
7404	Sheet 2	F
7440	Sheet 3	C

REVISION OF CONTENTS	F	C
DATE OF ECN	5 SEP 73	28 FEB 75
APPROVAL		
ENGINEERING CHANGE NOTICE	2468-IC	3175-IC

DR80-B-177

DWG NO	REV	SHT OF
7404 7440	—	—



C 7745

DR80 - B - 178

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	7440-01 7404-03	7403-03	1689-4C	2-23-73
-	7440-00 7404-00	7403-00	1537-2C	11-9-72

REVISIONS

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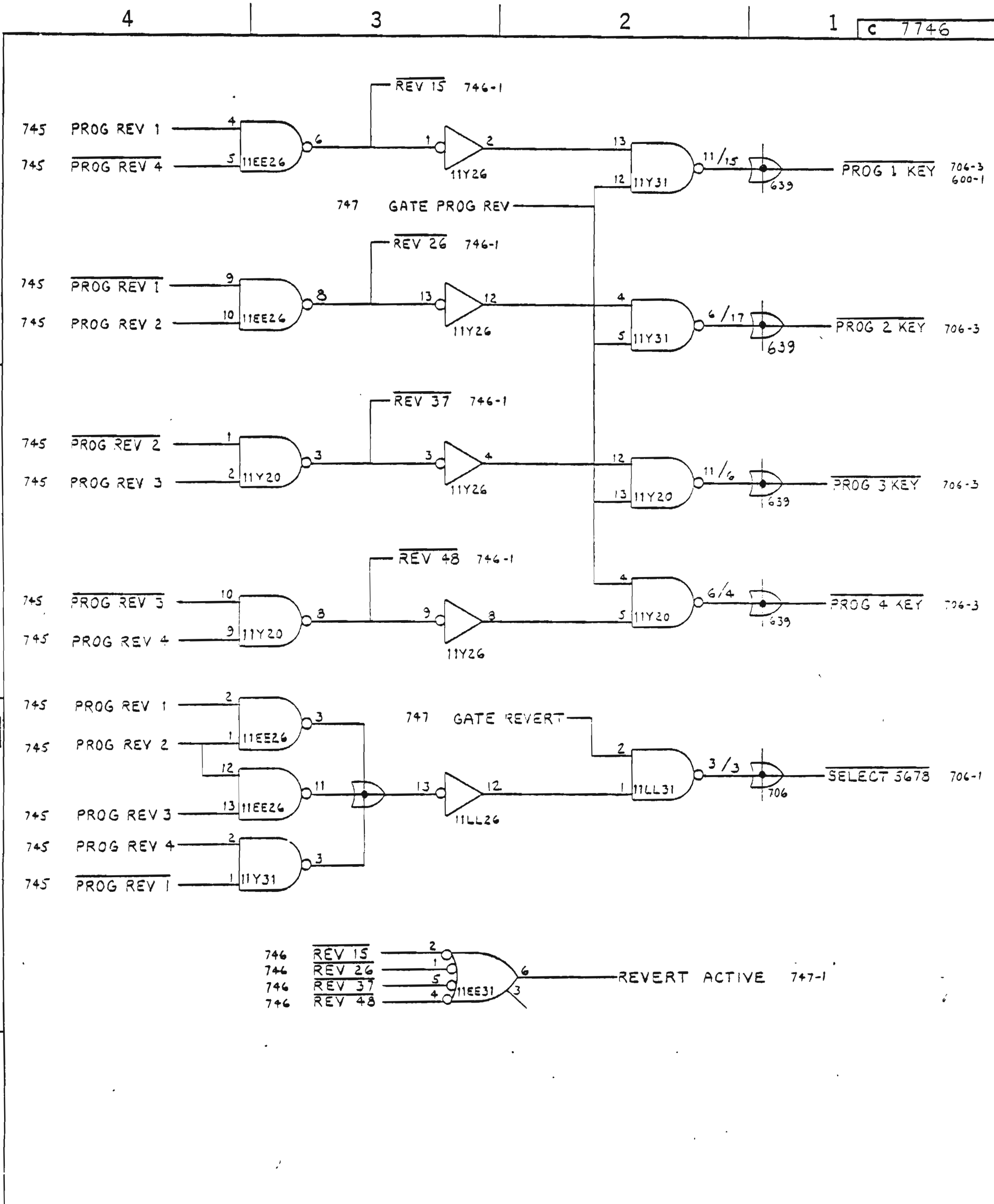
DRN J. ALESI 3/20/72

TITLE PROGRAM REVERT ROTARY

DWG NO. C 7745

DASH NO. -00

SH 1 OF 1



DR80-B-179

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	7440-00 7404-00	7403-00	1537-2C	11-9-72

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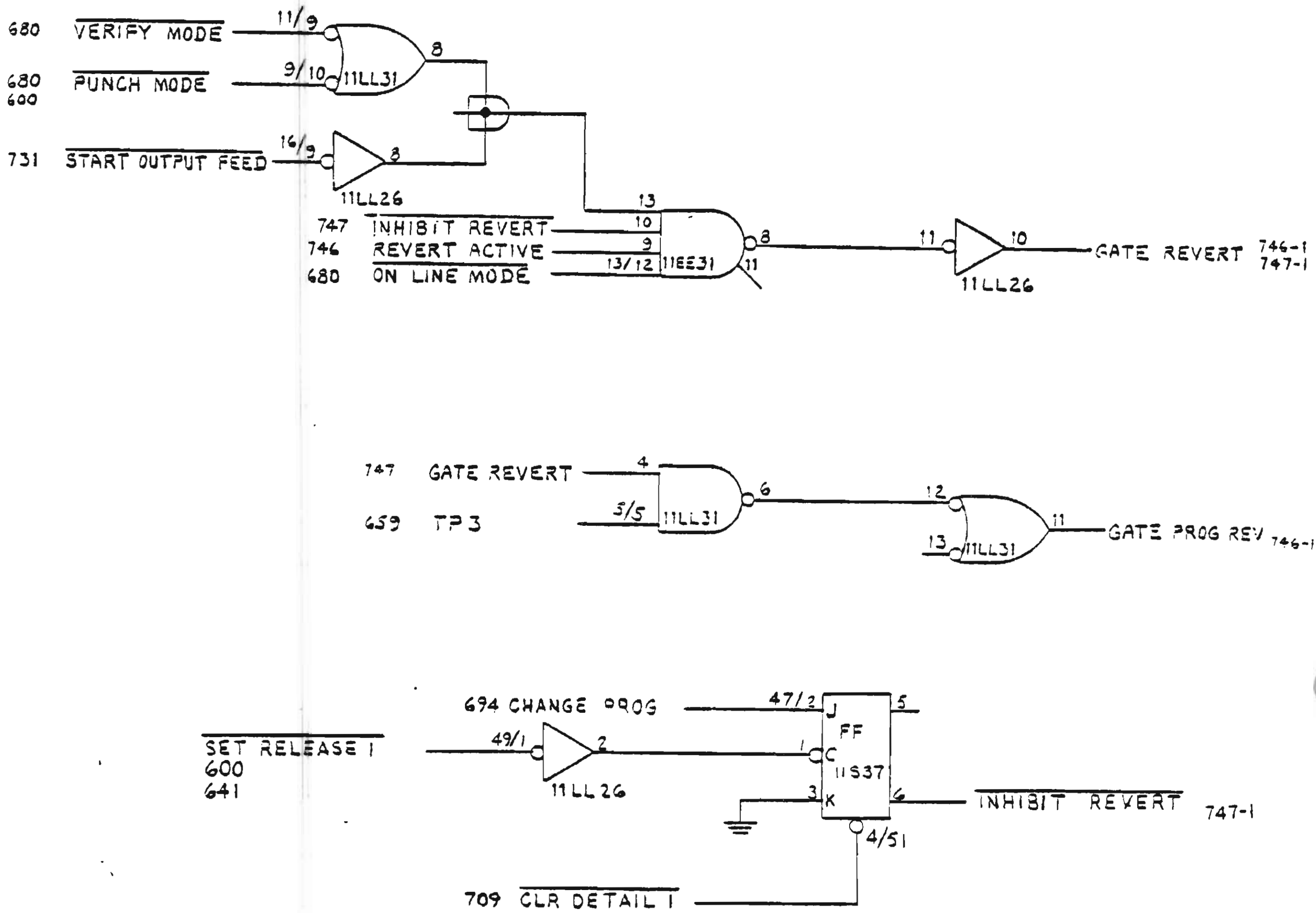
DRN J. ALESI 9/20/72

TITLE PROGRAM REVERT SELECTION

DWG NO. C 7746 DASH NO. -00 SH 1 OF 1

C 7747



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	7440-01 7404-03	7403-03	1689-4C	2-23-73
-	7440-00 7404-00	7403-00	1537-2C	11-9-72

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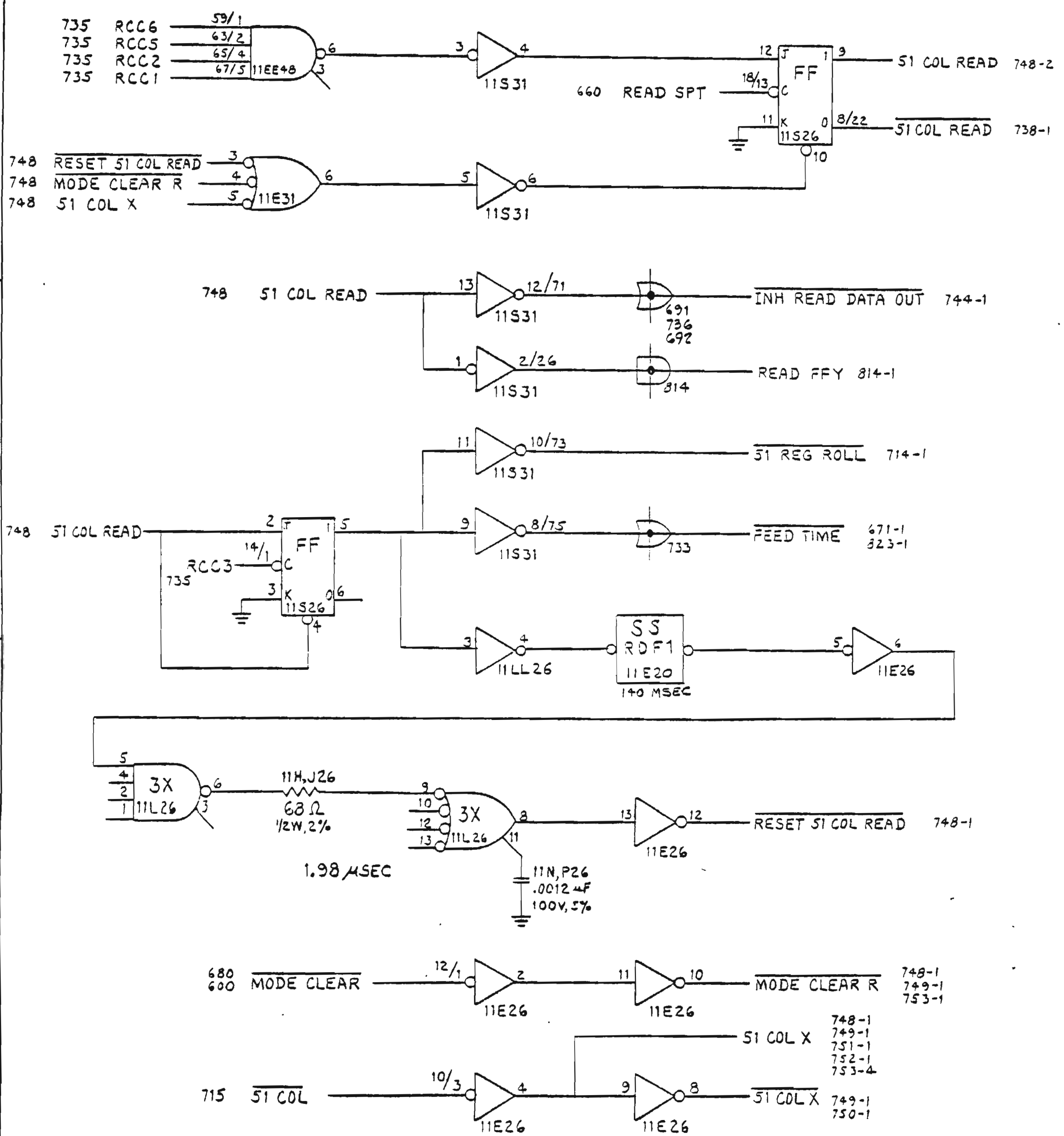
DR80-B-180

DRN J. ALESI 3/20/72

TITLE PROGRAM REVERT CONTROL

DWG NO. C 7747 DASH NO. -00 SH 1 OF 1



LOGIC ON THIS SHEET FOR MACHINES WITH 51 COLUMN CAPABILITY. (7404).

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
E	7404-04	7403-04	2236-2C	
D	7404-04	7403-03	2236-1P	25 JUN 73
C	7404-03	7403-03	1689-4C	2-23-73
B	7404-03	7403-00	1689-3P	1-30-73
A	7404-01	7403-00	1689-1P	12-27-72
–	7404-00	7403-00	1537-2C	11-9-72

REVISIONS

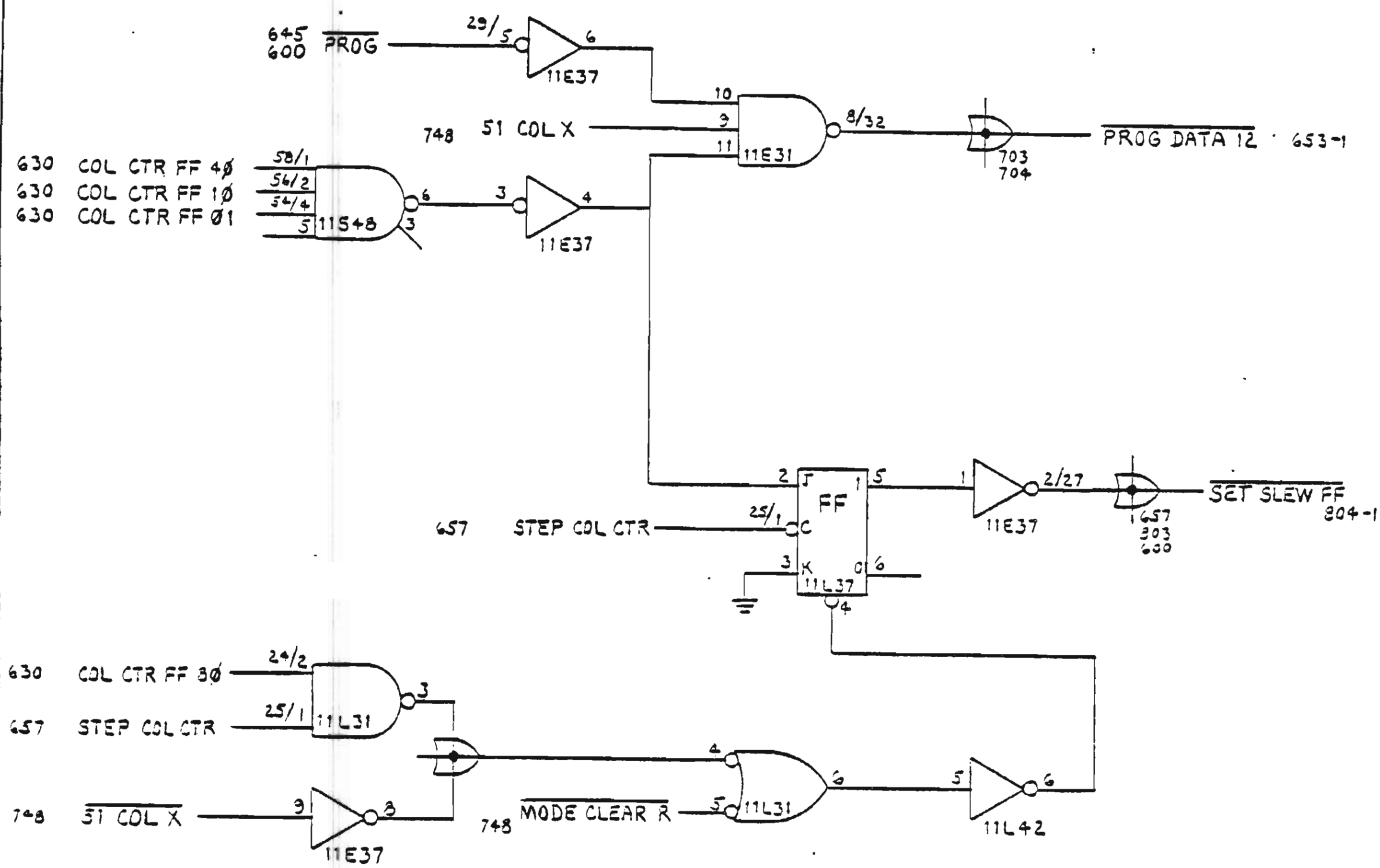
DR80-B-181

DRN J. ALESI 9/21/72 ENG
CHK 9-29-72 MFG

TITLE 51 COLUMN READ CONTROL

DWG NO. C 7748 DASH NO. -00 SH 1 OF 1

C 7749



LOGIC ON THIS SHEET FOR MACHINES
WITH 51 COLUMN CAPABILITY. (7404).

DR80-B-182

DRN J. ALESI 3/21/72 | ENG [illegible]
CHK [illegible] | MFG

- 17404-0017403-0011537-2C1[illegible] 11-4-72

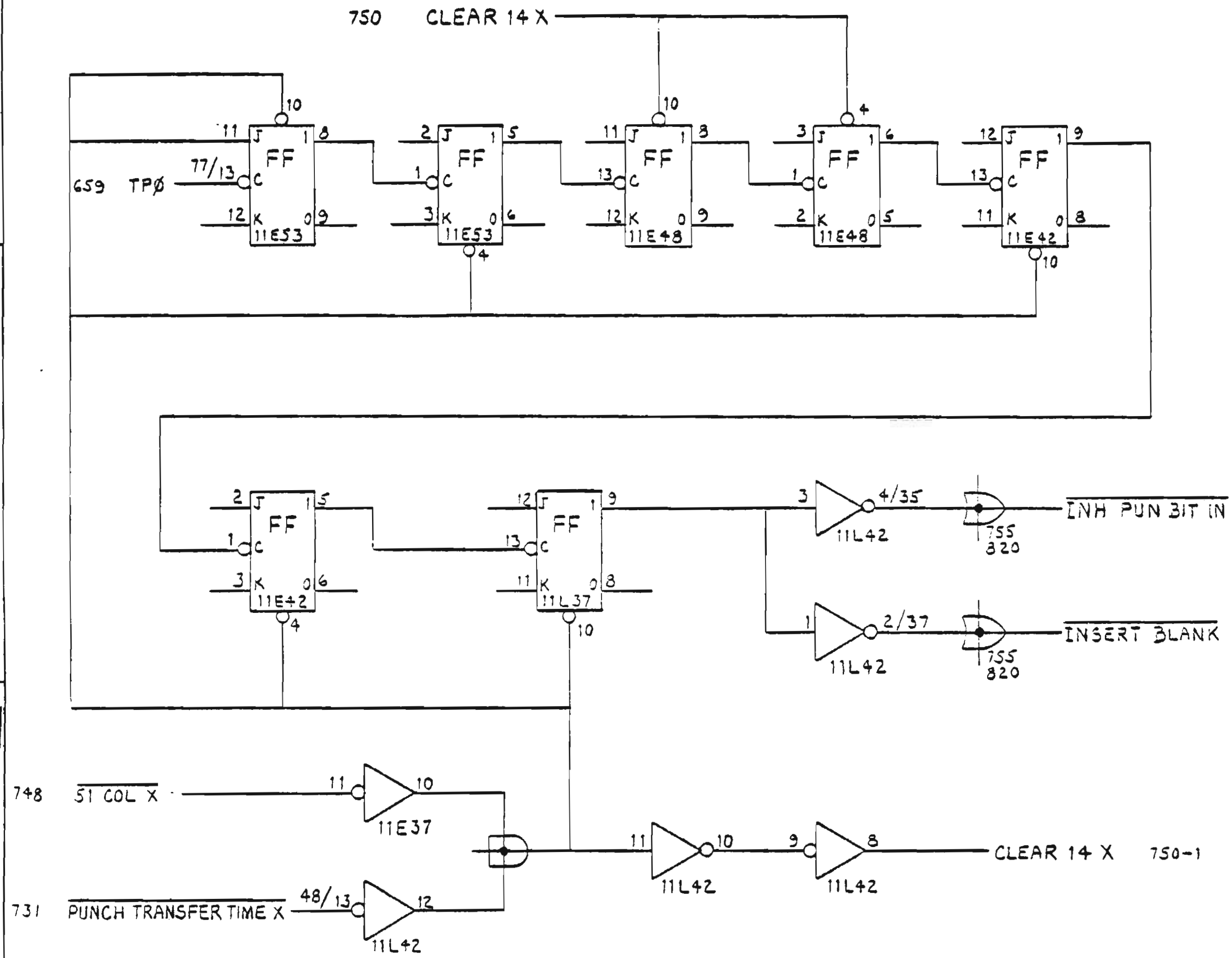
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

TITLE 51 COLUMN
KEYING CONTROLS

DWG NO. C 7749 | DASH NO. -00 | SH 1 OF 1

C 7750



LOGIC ON THIS SHEET FOR MACHINES
WITH 51 COLUMN CAPABILITY. (7404).

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	7404-00	7403-00	1537-2C	1-4-72

REVISIONS

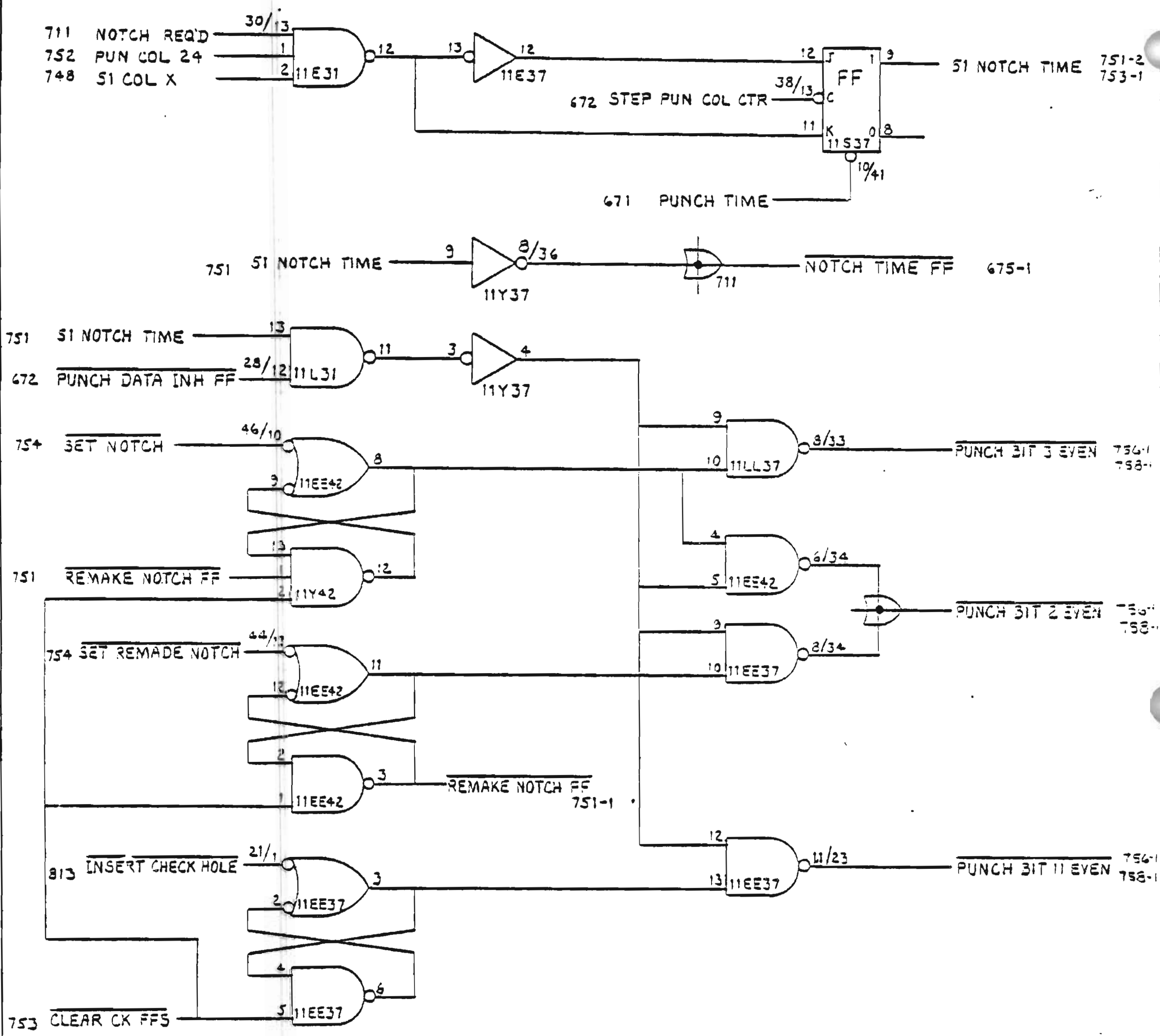
DR80-B-183

DRN J. ALESI 9/21/72

TITLE 51 COLUMN
PUNCH / PRINT DATA TRANSFER

DWG NO. C 7750 DASH NO. -00 SH 1 OF 1

C 7751



LOGIC ON THIS SHEET FOR MACHINES WITH 51 COLUMN CAPABILITY. (7404).

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	7404-0017403-00	3/75-1C		
–	7404-0017403-00	1537-2C		11-6-72

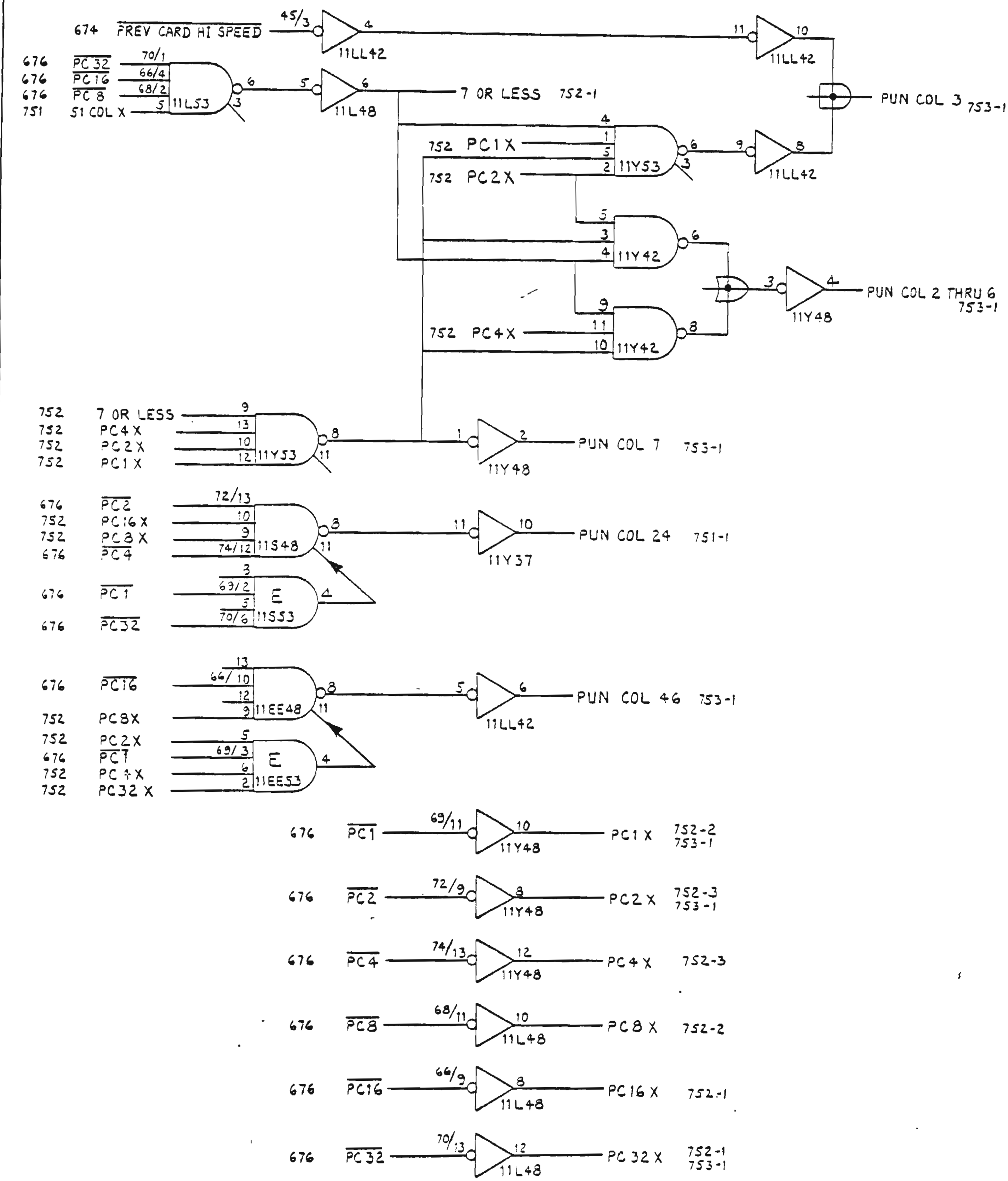
REVISIONS

DR80 - B - 184

DRN J. ALESI 8/21/72
CHK 9-29-72

TITLE 51 COLUMN CHECK HOLE CONTROL

DWG NO. C 7751 DASH NO. -00 SH 1 OF 1



LOGIC ON THIS SHEET FOR MACHINES
WITH 51 COLUMN CAPABILITY. (7404).

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
—	7404-00	7403-00	1537-2C	11-14-72

REVISIONS

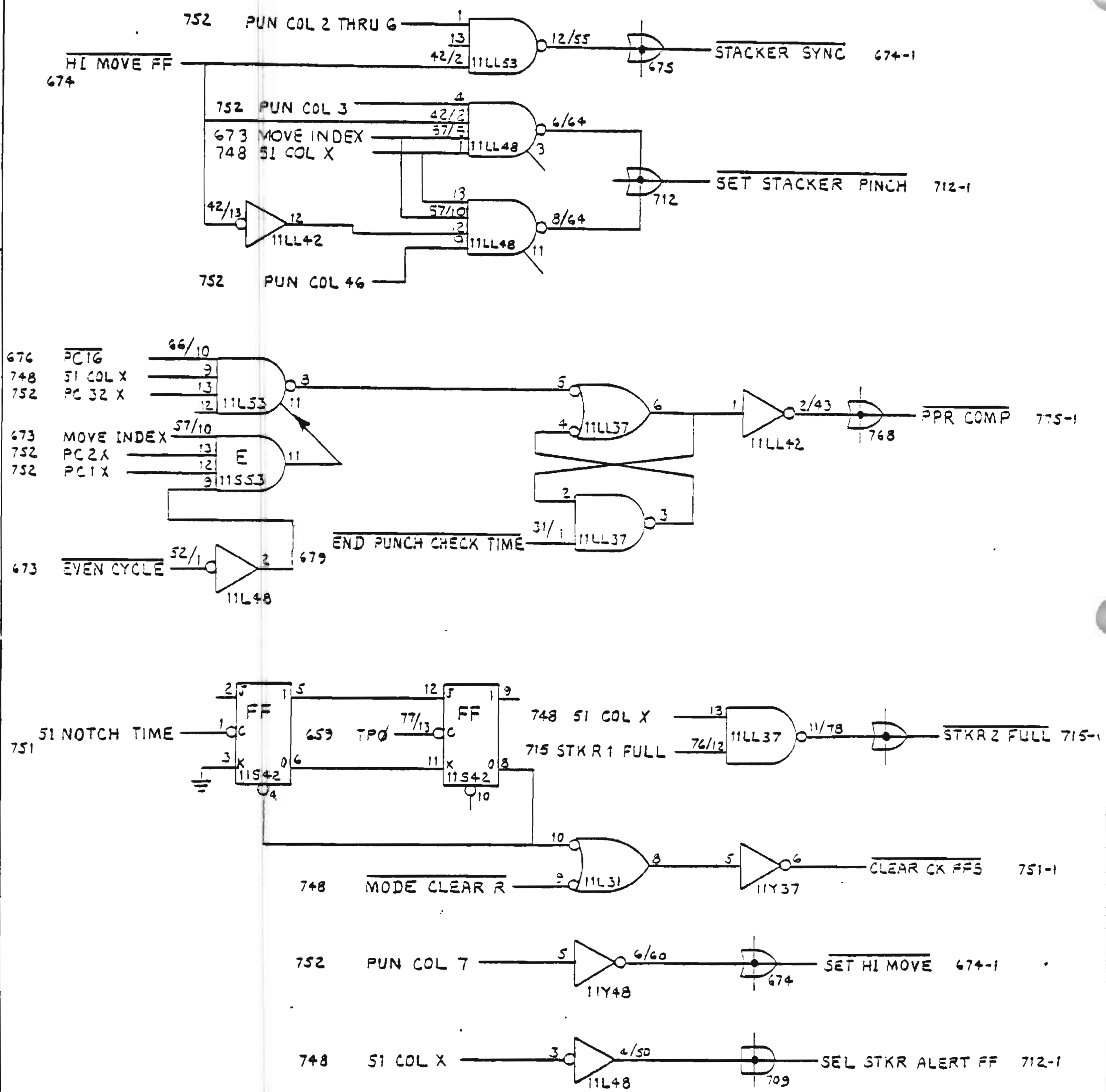
DR80-B-185

DRN J. ALESI 9/21/72 ENG 11-6-72
CHK 9-29-72 MFG

TITLE 51 COLUMN PUN COLUMN DECODES

DWG NO. C 7752 DASH NO -00 SH 1 OF 1

4 3 2 1 C 7753



LOGIC ON THIS SHEET FOR MACHINES WITH 51 COLUMN CAPABILITY. (7404).

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	7404-03	7403-03	1639-4C	2-23-73
B	7404-03	7403-03	1629-3P	1-30-73
A	7404-01	7403-00	1689-1P	12-11-72
-	7404-00	7403-00	1537-2C	11-4-72

REVISIONS

DR80-B-186

DRN J. ALESI 3/22/72 ENG
CHK MFG

TITLE 51 COLUMN STACKER CONTROL

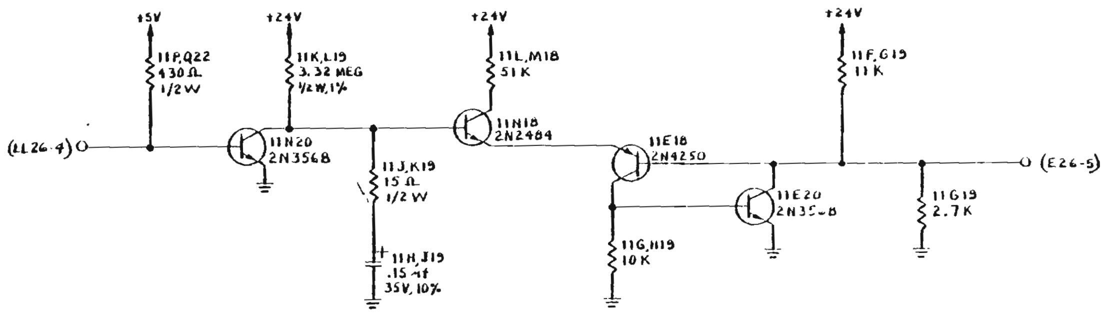
DWG NO. C 7753 DASH NO -00 SH 1 OF 1

A B C D

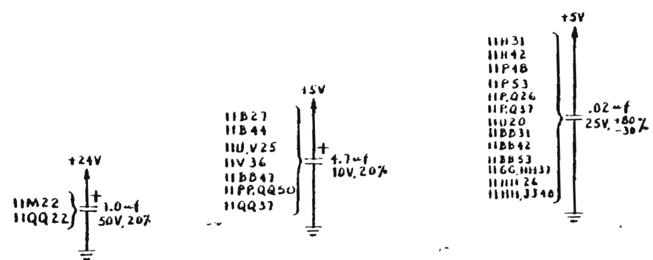
REV	DESCRIPTION	APPVD DATE
–	RELEASE P1-00, ECN #1637 2C	[illegible]
D	REVISED/ECN #1689-4C	[illegible]
E	REVISED/ECN 2236-1P	[illegible]
F	REVISED/ECN 2236-2C	[illegible]

✱ UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS LISTED.

✱ VARIATION REFERENCE		
VAR	ASSEMBLY	ARTWORK
I	7404-00,01,02,03,04	7403-00
II	7404-03 & -04	7403-03
III	7404-04	7403-04



RDF1



NOTES:

1. UNLESS OTHERWISE SPECIFIED: ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%.

DR80-B-187

COMPONENT ASSEMBLY PROGRAM REVERT & SI COLUMN

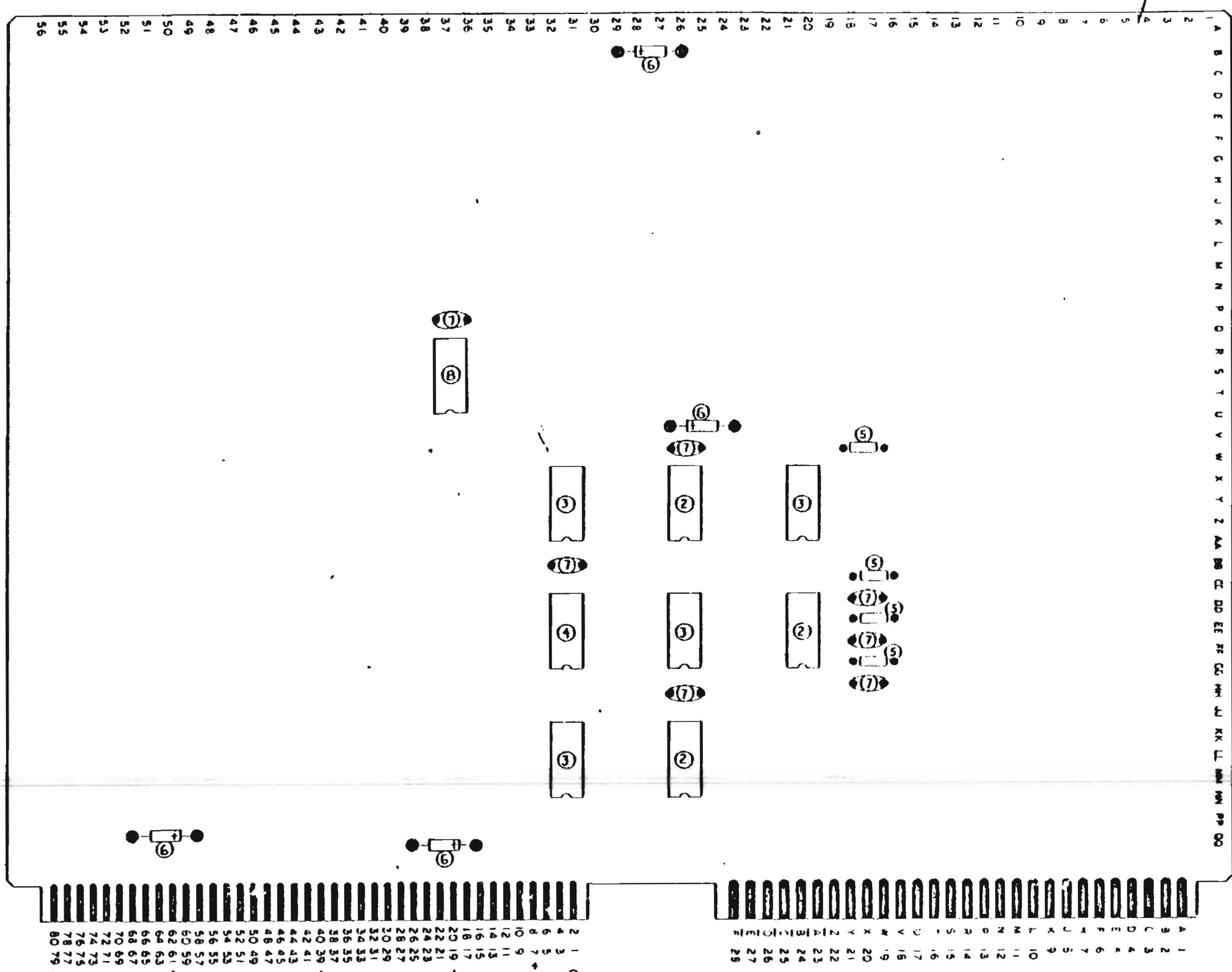
D 7404

DASH NO ✱

SH 2 OF

D 7404

SEE NOTE 1 (SOLDER SIDE)



REV	DESCRIPTION	APPROVAL
C	ADDED SHT 3, ECN 2468-1C	

LOGIC DIAGRAM CHART

DWG NO.	REV
7745	A
7746	-
7747	A

✱ UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR VARIATIONS LISTED; OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 7440-02 ON ARTWORK 7403-04

✱ VARIATION REFERENCE

VAR	ASSEMBLY	ART WORK
I	7440-00, -01	7403-00
II	7440-01	7403-03
III	7440-02	7403-04

NOTES:
1. STAMP ASSEMBLY NO. "7440-02(C)" ON SOLDER SIDE OF BOARD IN APPROXIMATE AREA SHOWN.

VAR I, II, III (11H26, 11B831); VAR I (11P37) — +5V — .02 µf 25V, +80% −30%

VAR I, II, III (11B27, 11U,V25, 11P,Q,Q50, 11Q37) — +5V — 4.7 µf 10V, 20%

TYPE	LOC	OUTPUT PIN NO.
6/1 INPUT	LL26	4,6
4/2 INPUT	Y31	8

SPARE GATES

FOR LIST OF PARTS SEE DETACHED PARTS LIST

SCALE 2:1

DR80-B-188

COMPONENT ASSEMBLY
/ PROGRAM REVERT

D 7440

SH 3

LOGIC/SCHEMATIC CONTENTS

PCB LOC 12

TYPE NUMBER/NAME BASIC 8001, 8010 INTERFACE

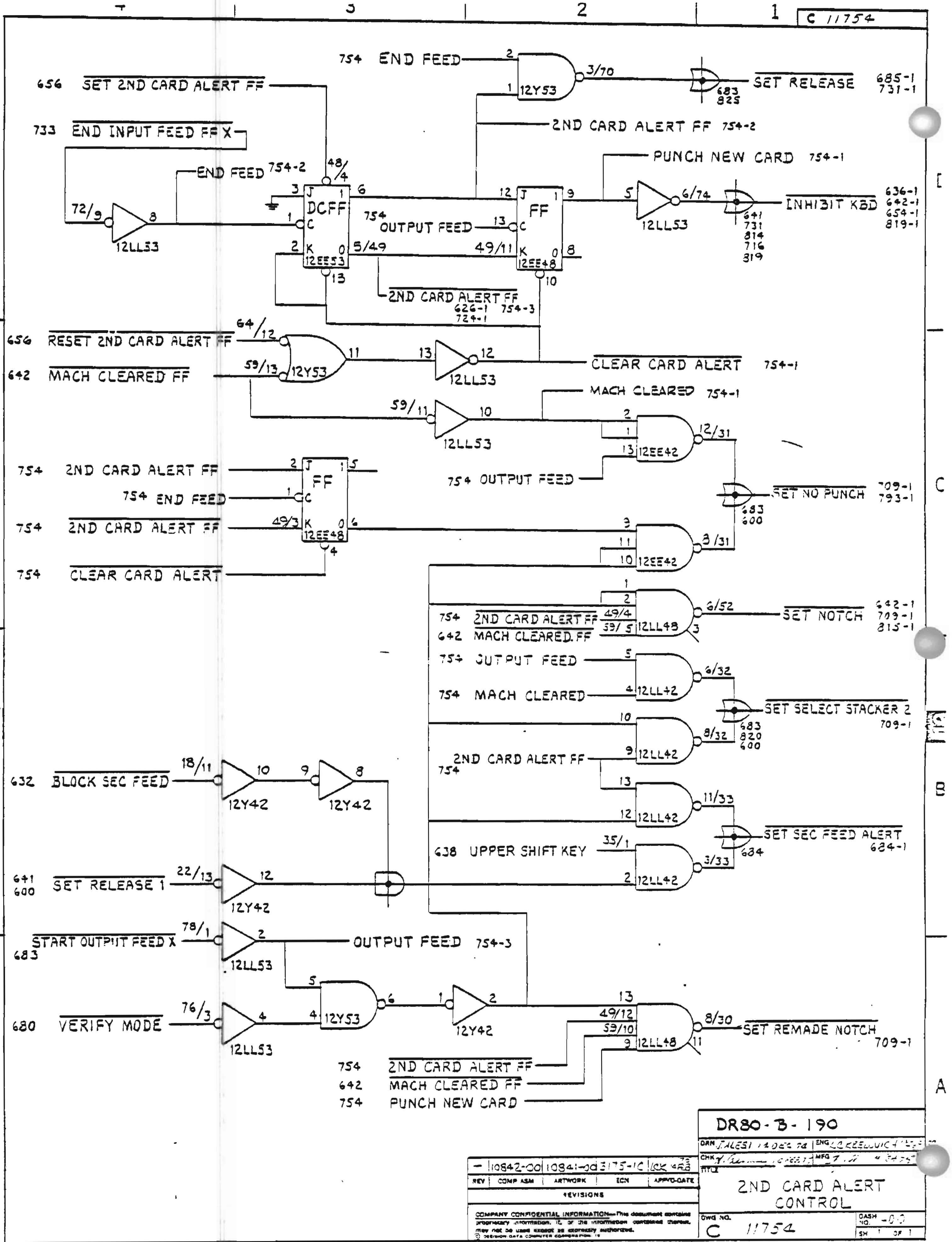
DWG NO	TITLE	PCB/ASM DRAWING DASH NO
10841	PWB	00
10842	PUNCH DATA	00

LOGIC SCHEMATIC	LOGIC DRAWING TITLE SCHEMATIC SHEET NO	LOGIC/SCHEMATIC DRAWING REVISION
11 754	2ND CARD ALERT CONTROL	-
11 755	PUNCH MEMORY CONTROL & DATA PT-1	-
11 756	PUNCH MEMORY CONTROL & DATA PT-2	-
11 757	PUNCH MEMORY	-
11 758	EVEN PUNCH TRANSFER GATES	-
11 759	PUNCH MEMORY ODD COLUMN REGISTER PT-1	-
11 760	PUNCH MEMORY ODD COLUMN REGISTER PT-2	-
11 761	PUNCH DATA BLANK	-
11 762	PUNCH ACTUATORS & DRIVERS PT-1	-
11 763	PUNCH ACTUATORS & DRIVERS PT-2	-
11 764	PUNCH ACTUATORS & DRIVERS PT-3	-
11 765	PUNCH ACTUATORS & DRIVERS PT-4	-
10842	Sheet 2	-

REVISION OF CONTENTS	-
DATE OF ECN	28 FEB 75
APPROVAL	
ENGINEERING CHANGE NOTICE	3175-1C

DR80-B-189

DWG NO 10842 REV - SHT - OF



DR80-B-190

DRN / CHK / MFG

REV	COMP ASM	ARTWORK	ECN	APPD-DATE
—	10842-00	10841-00	3175-1C

REVISIONS

2ND CARD ALERT CONTROL

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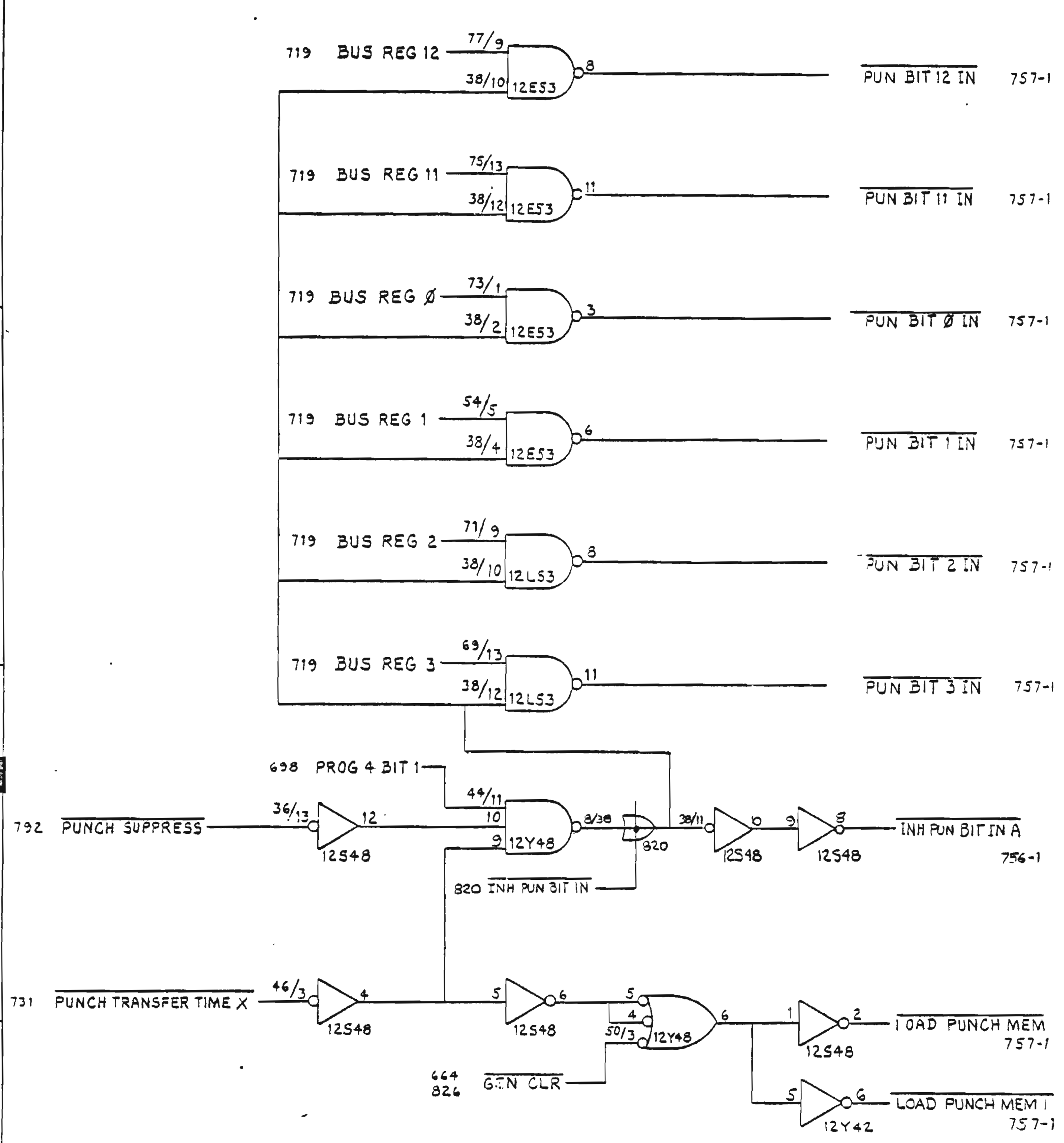
DWG NO. C 11754

DASH NO. -00

SH 1 OF 1



DR80-B-191

-	10842-00	10841-00	3175-IC	LCK 14 FEB 75
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

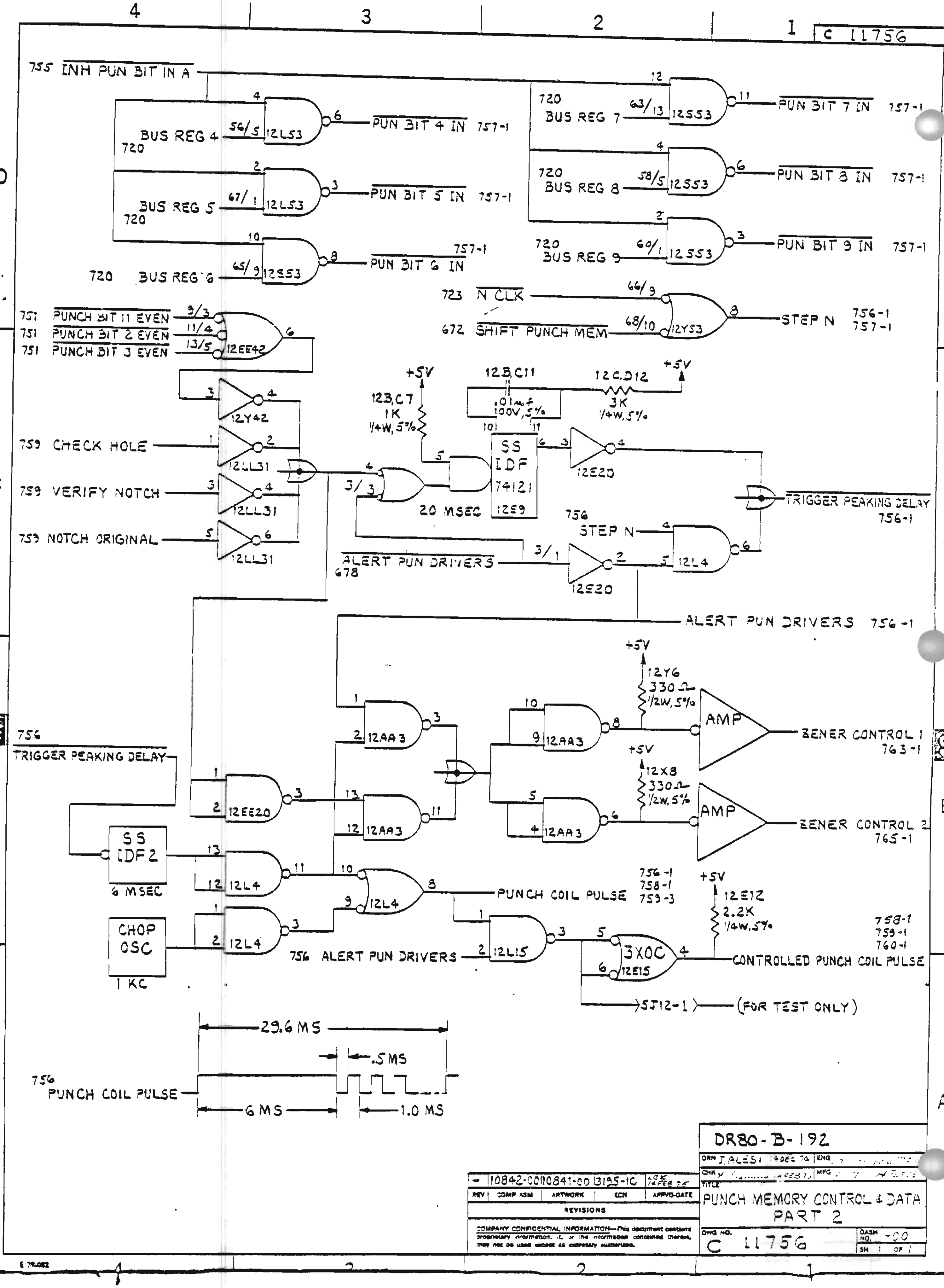
REVISIONS

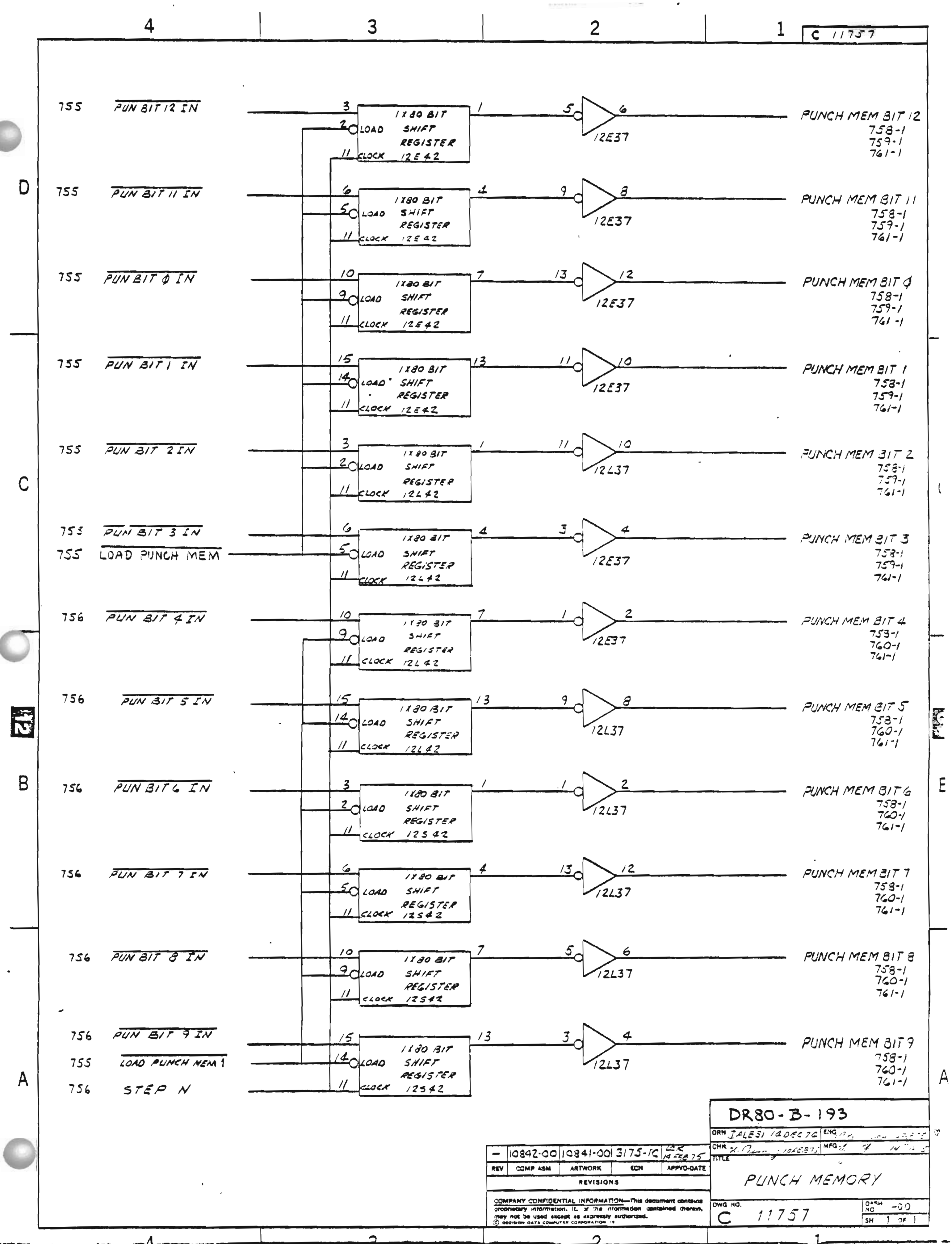
DRN J. ALESI 14 DEC 74 · ENG · CHK · MFG

TITLE PUNCH MEMORY CONTROL & DATA, PART 1

DWG NO. C 11755 · DASH NO. -00 · SH 1 OF 1

C 11755


C 11756
755 INH PUN BIT IN A
720 BUS REG 4
PUN BIT 4 IN 757-1
720 BUS REG 5
PUN BIT 5 IN 757-1
720 BUS REG 6
PUN BIT 6 IN 757-1
720 BUS REG 7
PUN BIT 7 IN 757-1
720 BUS REG 8
PUN BIT 8 IN 757-1
720 BUS REG 9
PUN BIT 9 IN 757-1
723 N CLK
672 SHIFT PUNCH MEM
STEP N 756-1 757-1
751 PUNCH BIT 11 EVEN
751 PUNCH BIT 2 EVEN
751 PUNCH BIT 3 EVEN
12EE42
12Y42
759 CHECK HOLE
759 VERIFY NOTCH
759 NOTCH ORIGINAL
12LL31
12B,C7 1K 1/4W, 5%
12B,C11 .01 uf 100V, 5%
12C,D12 3K 1/4W, 5%
+5V
SS IDF 74121 12S9
20 MSEC
12E20
TRIGGER PEAKING DELAY 756-1
756 STEP N
ALERT PUN DRIVERS 678
12L4
ALERT PUN DRIVERS 756-1
12Y6 330Ω 1/2W, 5%
12X8 330Ω 1/2W, 5%
AMP
ZENER CONTROL 1 763-1
ZENER CONTROL 2 765-1
12AA3
756 TRIGGER PEAKING DELAY
12EE20
SS IDF2
6 MSEC
CHOP OSC
1 KC
PUNCH COIL PULSE 756-1 758-1 759-3
12E12 2.2K 1/4W, 5%
756 ALERT PUN DRIVERS
12L15
3XOC 12E15
CONTROLLED PUNCH COIL PULSE 758-1 759-1 760-1
5J12-1 (FOR TEST ONLY)
29.6 MS
.5 MS
756 PUNCH COIL PULSE
6 MS
1.0 MS
DR80-B-192
DRN J. ALESI
TITLE PUNCH MEMORY CONTROL & DATA PART 2
DWG NO. C 11756
DASH NO. -00
SH 1 OF 1
10842-00110841-00 3195-1C
REV | COMP ASM | ARTWORK | ECN | APPVD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION—This document contains proprietary information. It, or the information contained therein, may not be used except as expressly authorized.



C 11757
755 PUN BIT 12 IN
755 PUN BIT 11 IN
755 PUN BIT Ø IN
755 PUN BIT 1 IN
755 PUN BIT 2 IN
755 PUN BIT 3 IN
755 LOAD PUNCH MEM
756 PUN BIT 4 IN
756 PUN BIT 5 IN
756 PUN BIT 6 IN
756 PUN BIT 7 IN
756 PUN BIT 8 IN
756 PUN BIT 9 IN
755 LOAD PUNCH MEM 1
756 STEP N
1X80 BIT SHIFT REGISTER
LOAD
CLOCK
12E42
12L42
12S42
12E37
12L37
PUNCH MEM BIT 12 758-1 759-1 761-1
PUNCH MEM BIT 11 758-1 759-1 761-1
PUNCH MEM BIT Ø 758-1 759-1 761-1
PUNCH MEM BIT 1 758-1 759-1 761-1
PUNCH MEM BIT 2 758-1 759-1 761-1
PUNCH MEM BIT 3 758-1 759-1 761-1
PUNCH MEM BIT 4 758-1 760-1 761-1
PUNCH MEM BIT 5 758-1 760-1 761-1
PUNCH MEM BIT 6 758-1 760-1 761-1
PUNCH MEM BIT 7 758-1 760-1 761-1
PUNCH MEM BIT 8 758-1 760-1 761-1
PUNCH MEM BIT 9 758-1 760-1 761-1
DR80-B-193
DRN
CHK
ENG
MFG
TITLE
PUNCH MEMORY
— 10842-00 10841-00 3175-IC 14 FEB 75
REV COMP ASM ARTWORK ECN APPVD-DATE
REVISIONS
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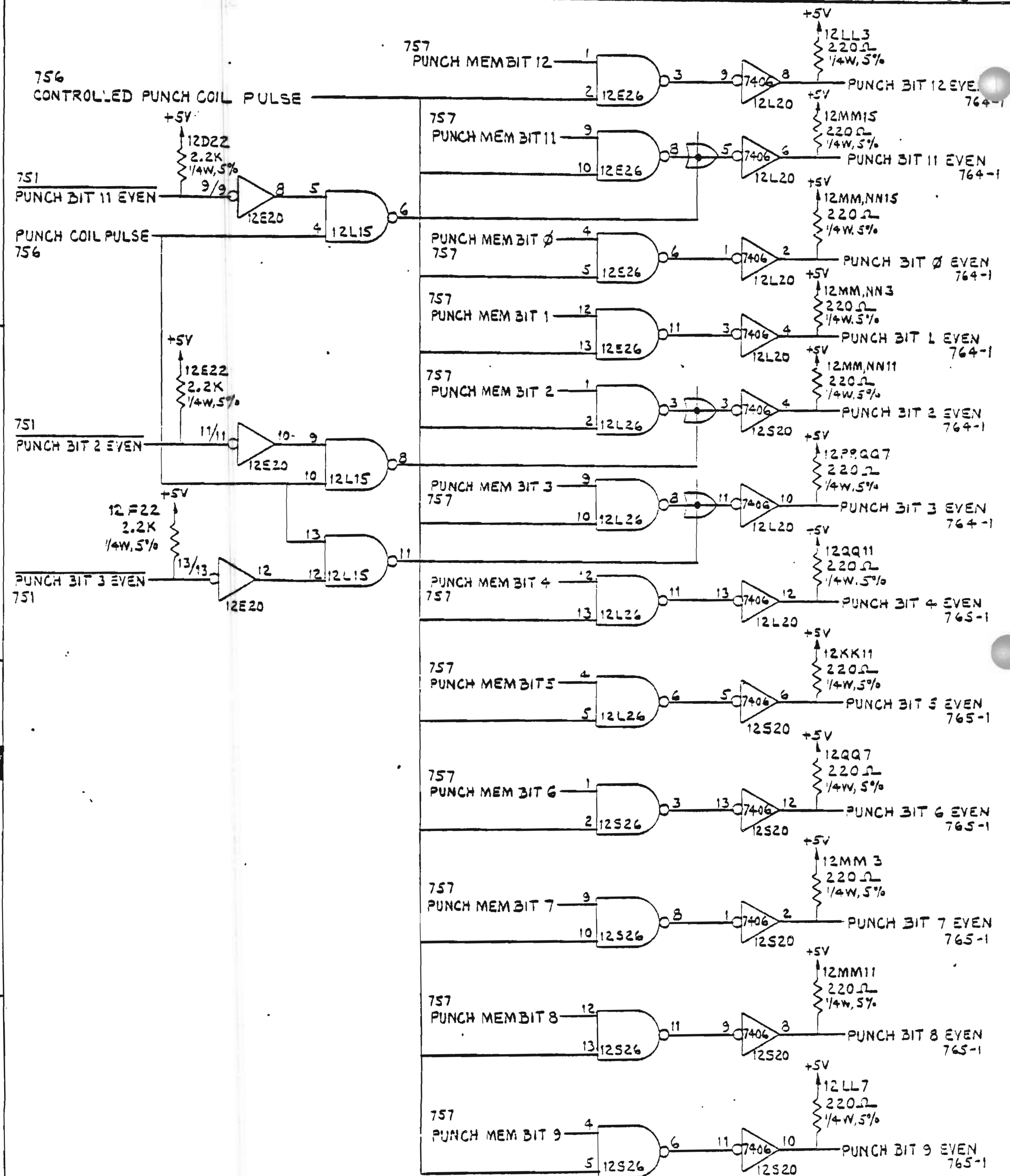
© DECISION DATA COMPUTER CORPORATION
DWG NO. C 11757
DASH NO -00
SH 1 OF 1

4 3 2 1 C 11758



DR80-B-194

DRN J. ALESI 12 DEC 74	ENG	
CHK	MFG	

TITLE EVEN PUNCH TRANSFER GATES

REV	COMP ASM	ARTWORK	ECN	APPD-DATE
-	110842-001	10841-001	3175-1C	

REVISIONS

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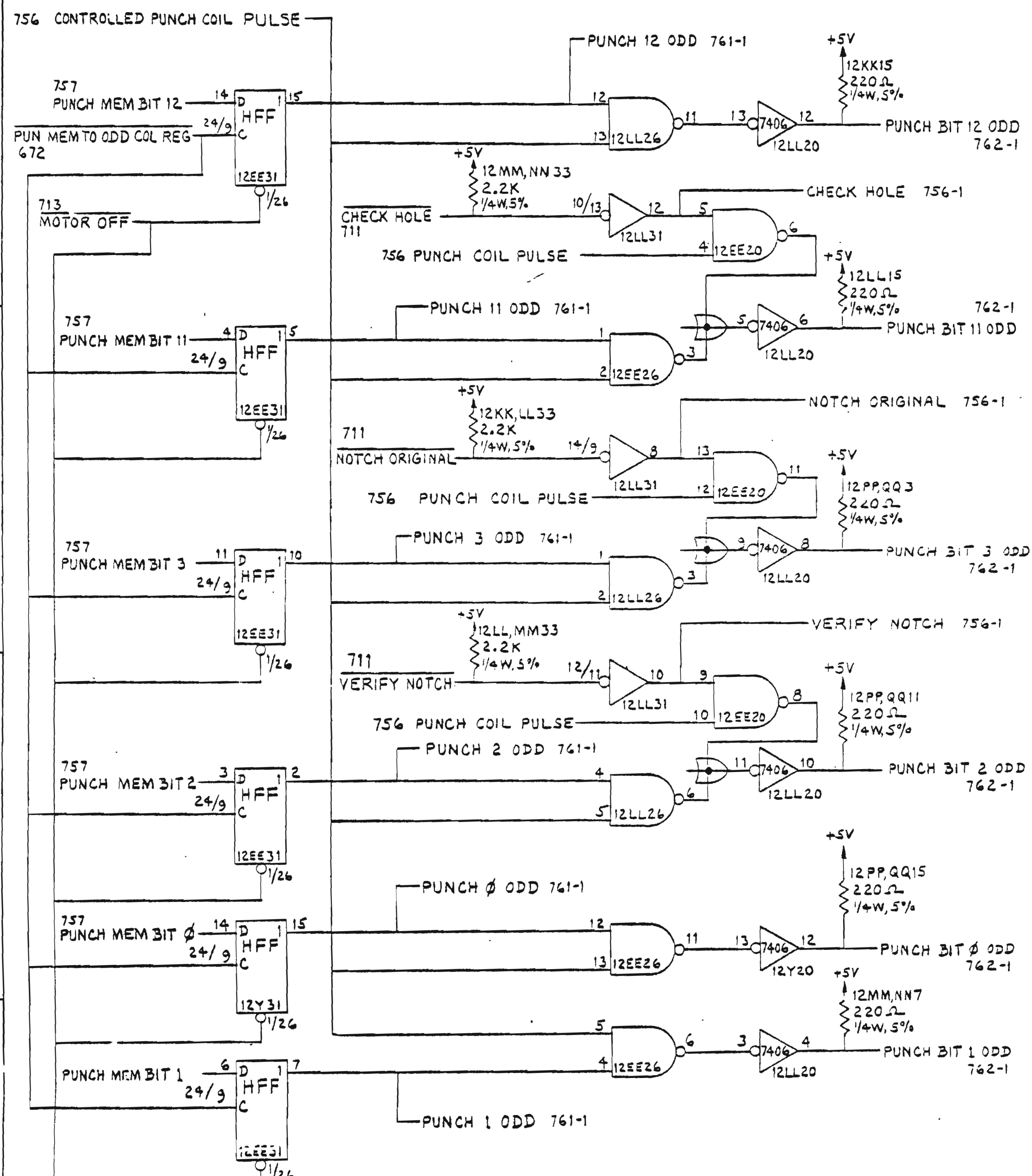
DWG NO. C 11758 DASH NO. -00 SH OF

E 79-082

4 3 2 1



DR80-B-195

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
—	10842-00	10841-00	3175-1C	

REVISIONS

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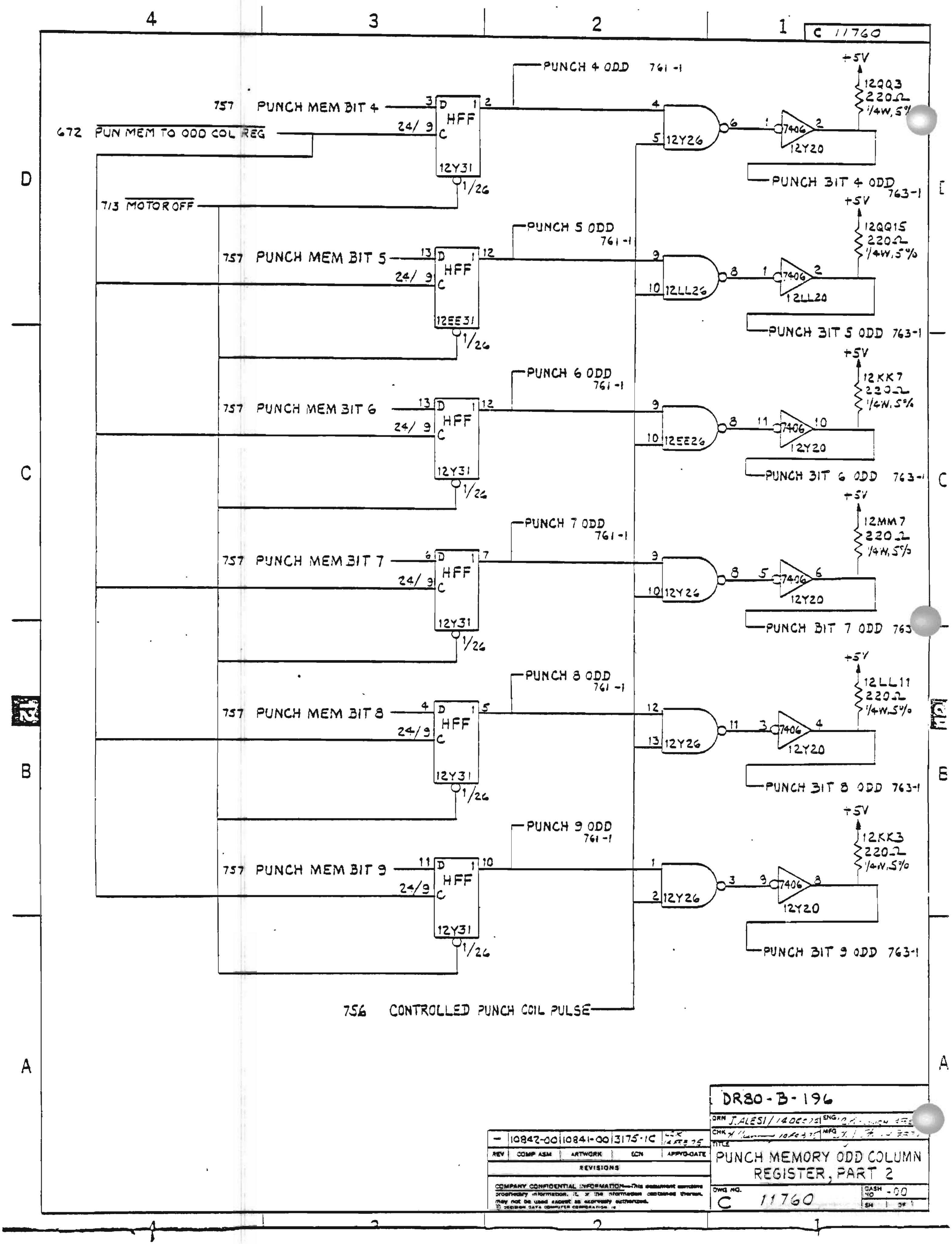
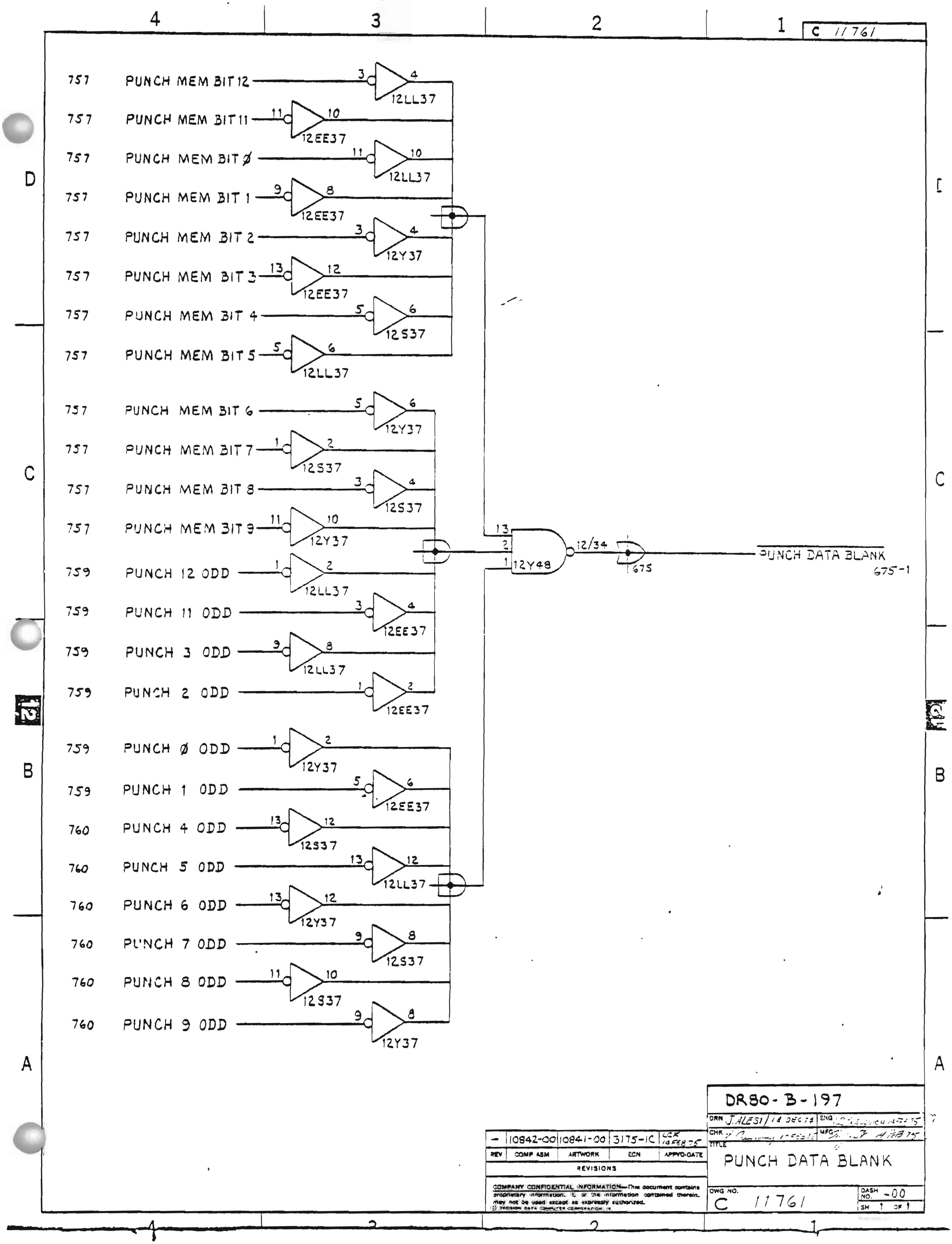
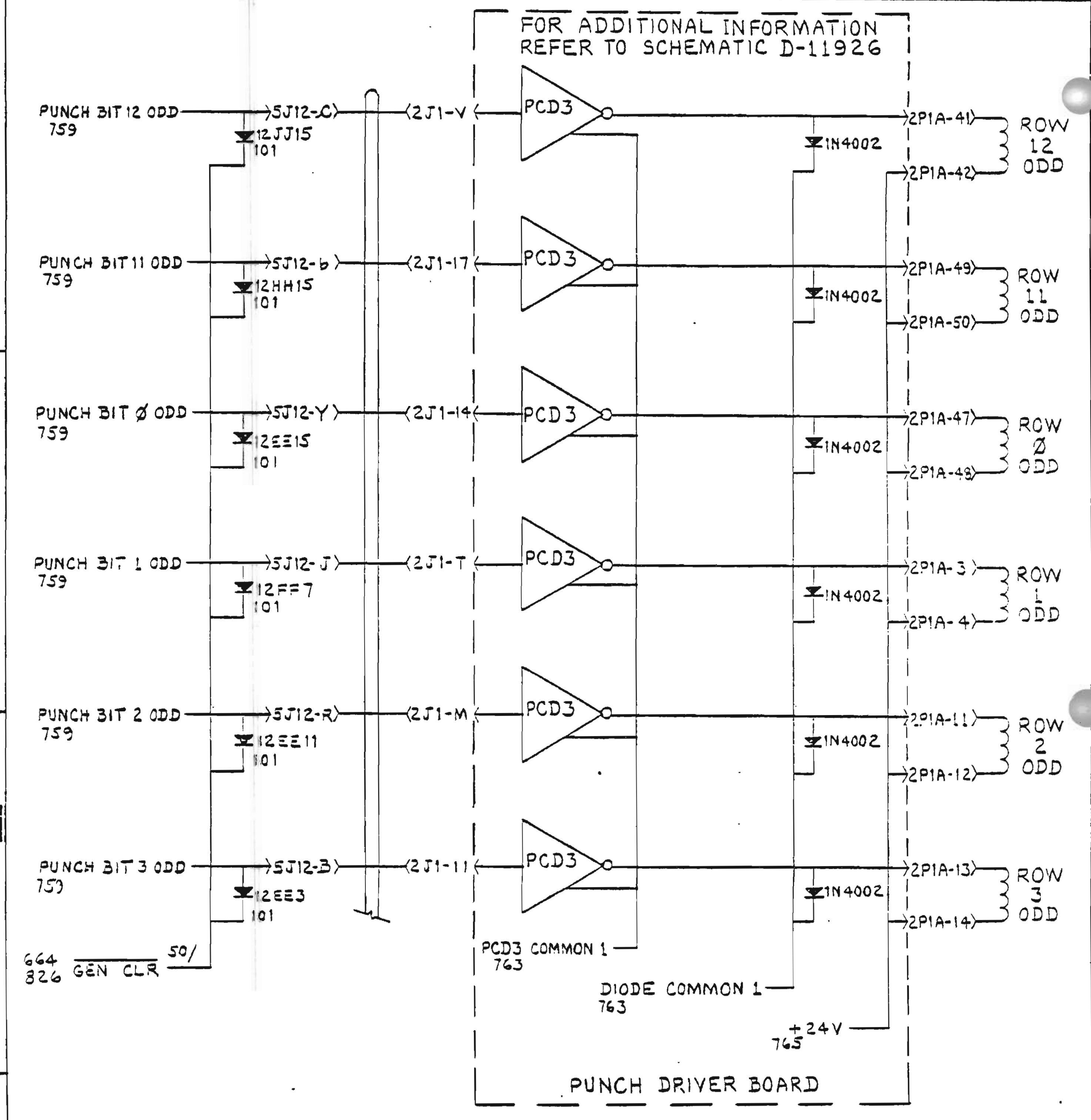
DRN J. ALESI 13 DEC 74

TITLE PUNCH MEMORY ODD COLUMN REGISTER, PART 1

DWG NO. C 11759 DASH NO. -00 SH 1 OF 1


C 11760
757 PUNCH MEM BIT 4
672 PUN MEM TO ODD COL REG
713 MOTOR OFF
PUNCH 4 ODD 761-1
HFF 12Y31
12Y26
7406 12Y20
+5V
12QQ3 220Ω 1/4W, 5%
PUNCH BIT 4 ODD 763-1
757 PUNCH MEM BIT 5
PUNCH 5 ODD 761-1
HFF 12EE31
12LL26
7406 12LL20
12QQ15 220Ω 1/4W, 5%
PUNCH BIT 5 ODD 763-1
757 PUNCH MEM BIT 6
PUNCH 6 ODD 761-1
HFF 12Y31
12EE26
7406 12Y20
12KK7 220Ω 1/4W, 5%
PUNCH BIT 6 ODD 763-1
757 PUNCH MEM BIT 7
PUNCH 7 ODD 761-1
HFF 12Y31
12Y26
7406 12Y20
12MM7 220Ω 1/4W, 5%
PUNCH BIT 7 ODD 763
757 PUNCH MEM BIT 8
PUNCH 8 ODD 761-1
HFF 12Y31
12Y26
7406 12Y20
12LL11 220Ω 1/4W, 5%
PUNCH BIT 8 ODD 763-1
757 PUNCH MEM BIT 9
PUNCH 9 ODD 761-1
HFF 12Y31
12Y26
7406 12Y20
12KK3 220Ω 1/4W, 5%
PUNCH BIT 9 ODD 763-1
756 CONTROLLED PUNCH COIL PULSE
DR80-B-196
DRN J. ALESI 14 DEC 75
– 10842-00 10841-00 3175-1C
REV COMP ASM ARTWORK ECN APPVD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION
PUNCH MEMORY ODD COLUMN REGISTER, PART 2
DWG NO. C 11760
DASH NO. -00
SH 1 OF 1


C 11761
757 PUNCH MEM BIT 12
757 PUNCH MEM BIT 11
757 PUNCH MEM BIT Ø
757 PUNCH MEM BIT 1
757 PUNCH MEM BIT 2
757 PUNCH MEM BIT 3
757 PUNCH MEM BIT 4
757 PUNCH MEM BIT 5
757 PUNCH MEM BIT 6
757 PUNCH MEM BIT 7
757 PUNCH MEM BIT 8
757 PUNCH MEM BIT 9
759 PUNCH 12 ODD
759 PUNCH 11 ODD
759 PUNCH 3 ODD
759 PUNCH 2 ODD
759 PUNCH Ø ODD
759 PUNCH 1 ODD
760 PUNCH 4 ODD
760 PUNCH 5 ODD
760 PUNCH 6 ODD
760 PUNCH 7 ODD
760 PUNCH 8 ODD
760 PUNCH 9 ODD
12LL37
12EE37
12Y37
12S37
12Y48
12/34
675
PUNCH DATA BLANK
675-1
DR80-B-197
DRN J. ALESI 14 DEC 74
- 10842-00 10841-00 3175-1C
REV COMP ASM ARTWORK ECN APPVD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION—This document contains proprietary information. It, or the information contained therein, may not be used except as expressly authorized.
TITLE PUNCH DATA BLANK
DWG NO. C 11761
DASH NO. -00
SH 1 OF 1



DR80-B-198

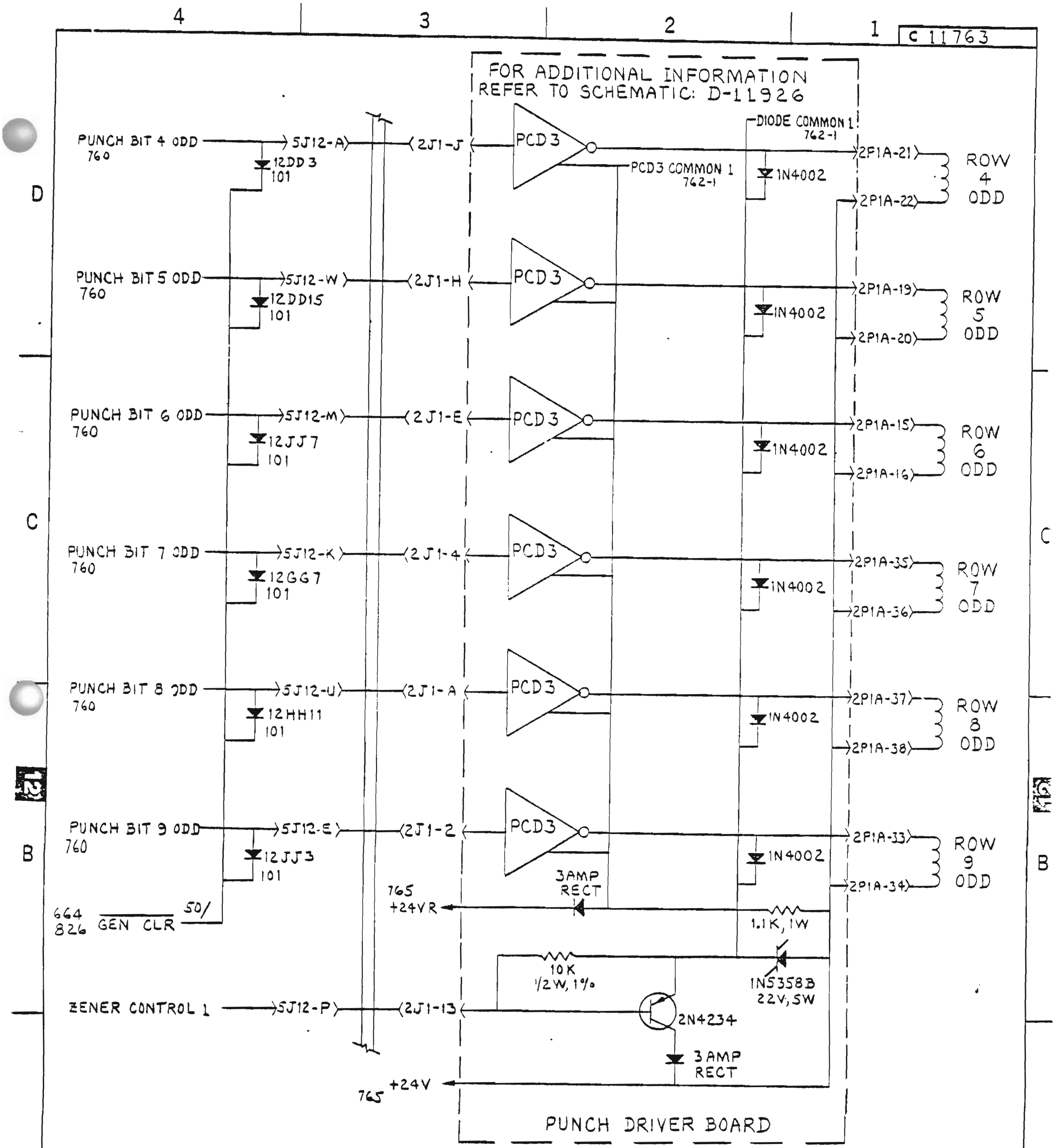
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	10842-00	10841-00	3175-1C	

REVISIONS

DRN J. ALESI 16 DEC 74

TITLE PUNCH ACTUATORS & DRIVERS PART 1

DWG NO. C 11762 DASH NO. -00



—	10842-00	10841-00	3175-1C	2 FEB 75
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

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DR80-B-199

DRN J. ALESI 14 DEC 74

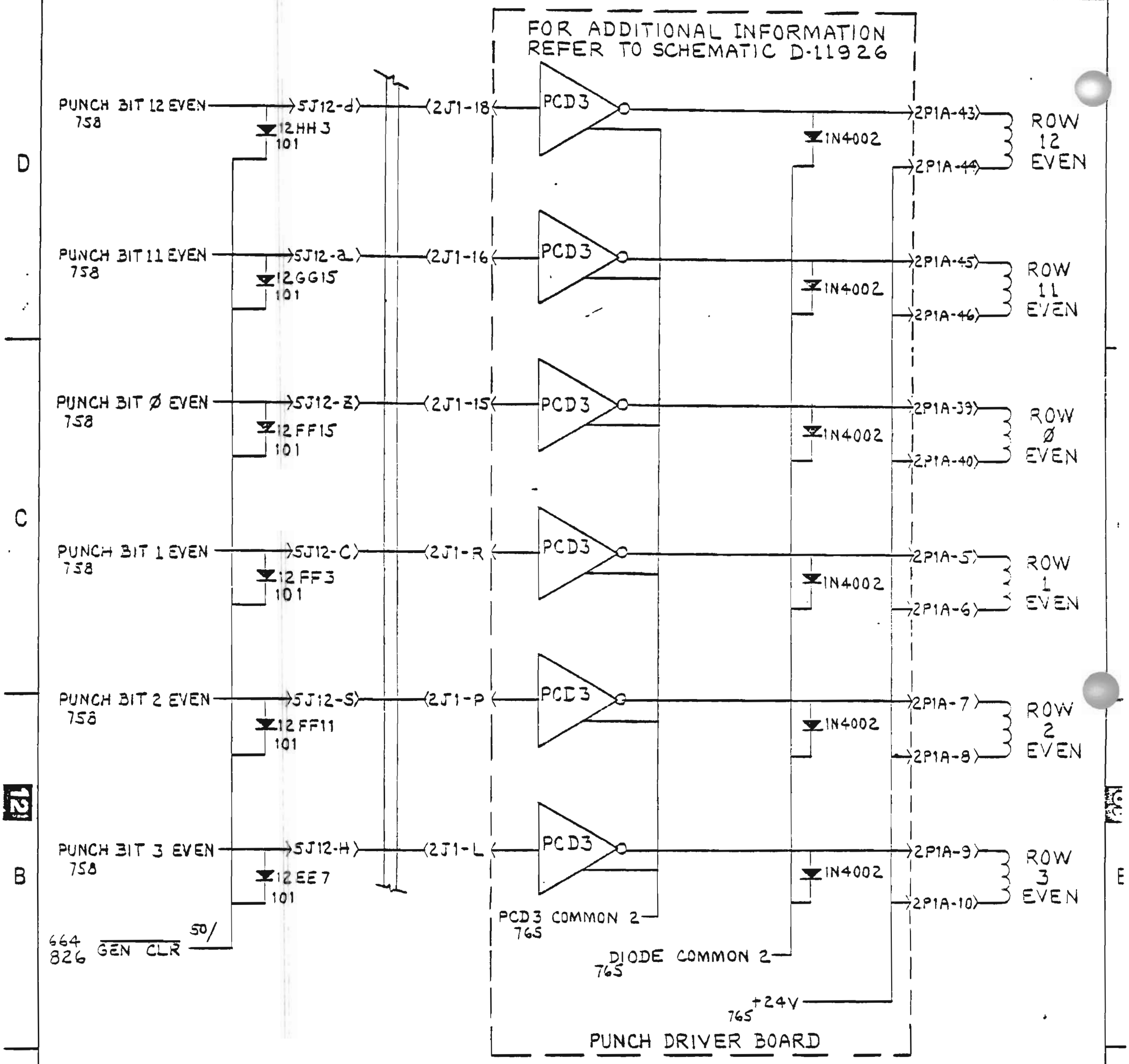
TITLE PUNCH ACTUATORS & DRIVERS, PART 2

DWG NO. C 11763

DASH NO. -00

SH 1 OF 1

E 79-082



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
—	10842-00	10841-00	3175-1C	

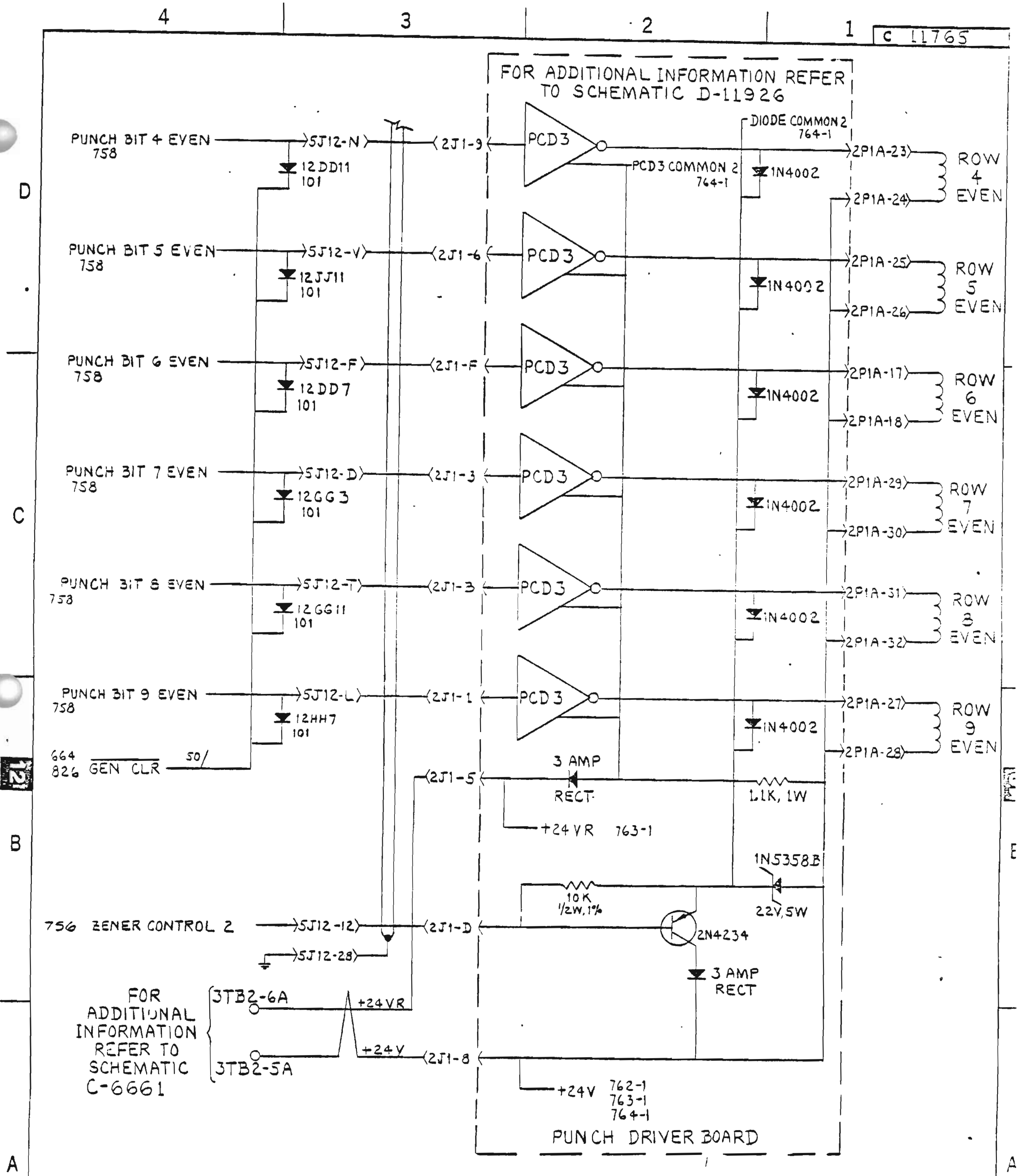
REVISIONS

DR80-B-200

DRN J. ALESI 6 DEC 74

TITLE PUNCH ACTUATORS & DRIVERS, PART 3

DWG NO. C 11764 DASH NO. -00 SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	10842-00	10841-00	3175-1C	

REVISIONS

DR80 - B - 201

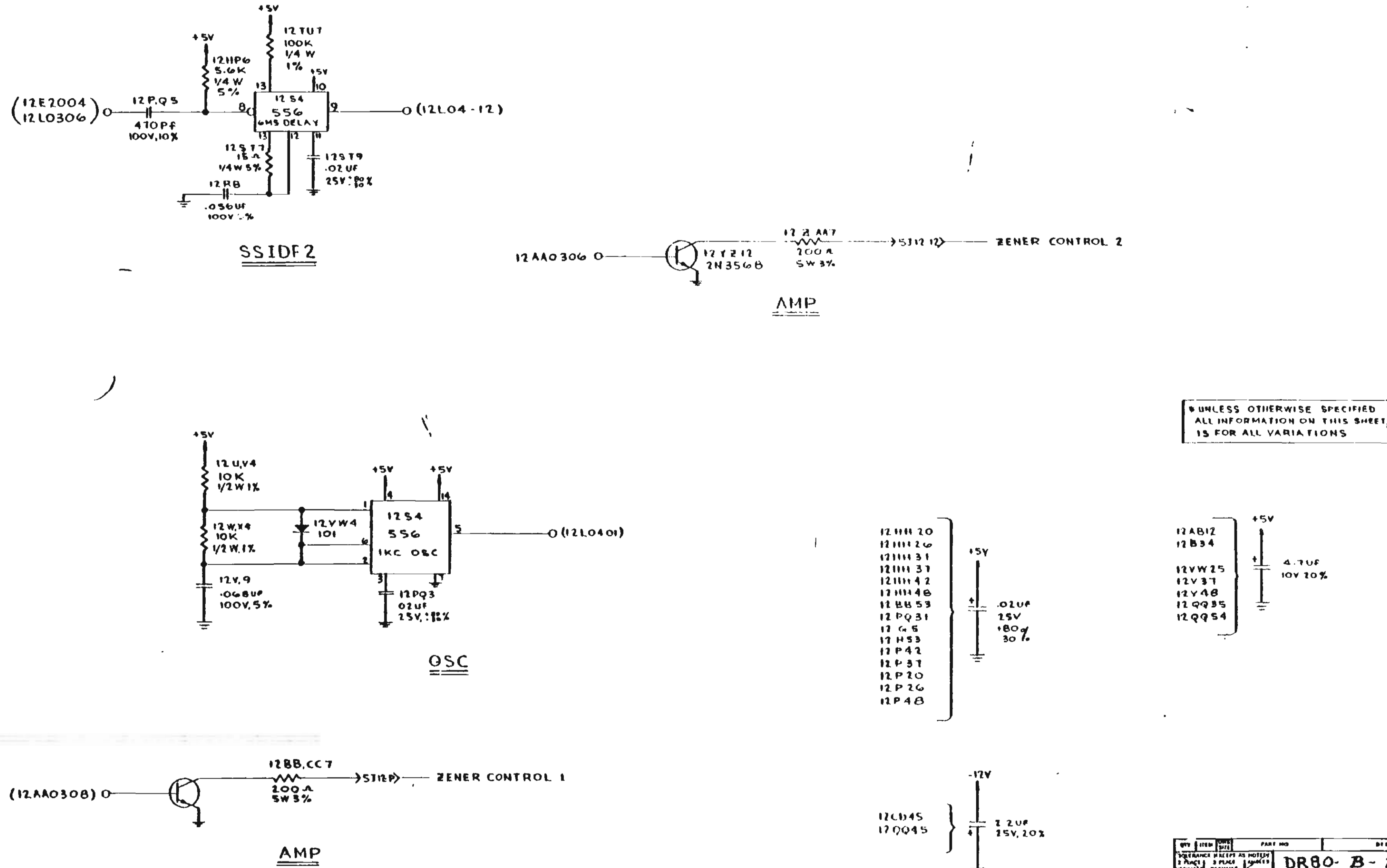
DRN J. ALESI | ENG
CHK | MFG

TITLE PUNCH ACTUATORS & DRIVERS PART 4

DWG NO. C 11765

E 79-082

REV	DESCRIPTION
	RELEASE/ECN 3175-IC



*UNLESS OTHERWISE SPECIFIED ALL INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS

COMPANY CONFIDENTIAL INFORMATION

DR80-B-202

COMPONENT ASSEMBLY PUNCH DATA

D 10842

D10842

LOGIC/SCHEMATIC CONTENTS

PCB LOC 20

TYPE NUMBER/NAME BASIC 8001,8010 INTERFACE

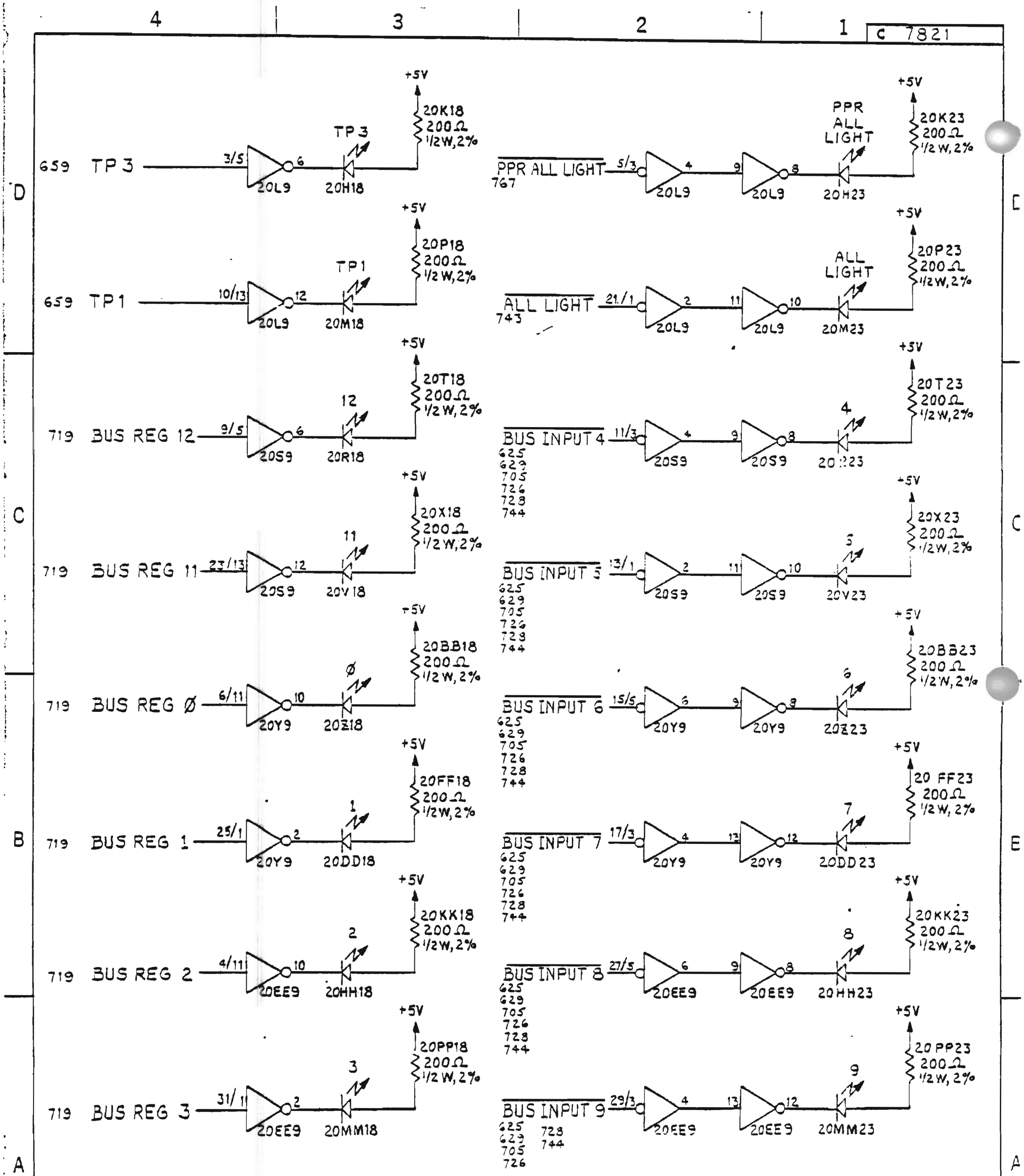
DWG NO	TITLE	PCB/ASM DRAWING DASH NO
2011	PWB	05
2012	MAINTENANCE BOARD	05

LOGIC SCHEMATIC	LOGIC DRAWING TITLE SCHEMATIC SHEET NO	LOGIC/SCHEMATIC DRAWING REVISION
7821	MAINTENANCE CARD PT-1	–
7822	" " PT-2	–
7823	" " PT-3	–
7824	" " PT-4	A
7825	" " PT-5	–
7826	" " PT-6	A
2012	Sheet 5	P

REVISION OF CONTENTS	P
DATE OF ECN	6 DEC 74
APPROVAL	
ENGINEERING CHANGE NOTICE	3913-1C

DR80-B-203

DWG NO 2012 REV – SHT – OF



DR80-B-204

DRN J. ALESI 2/3/71	ENG
CHK 4/7/72	MFG

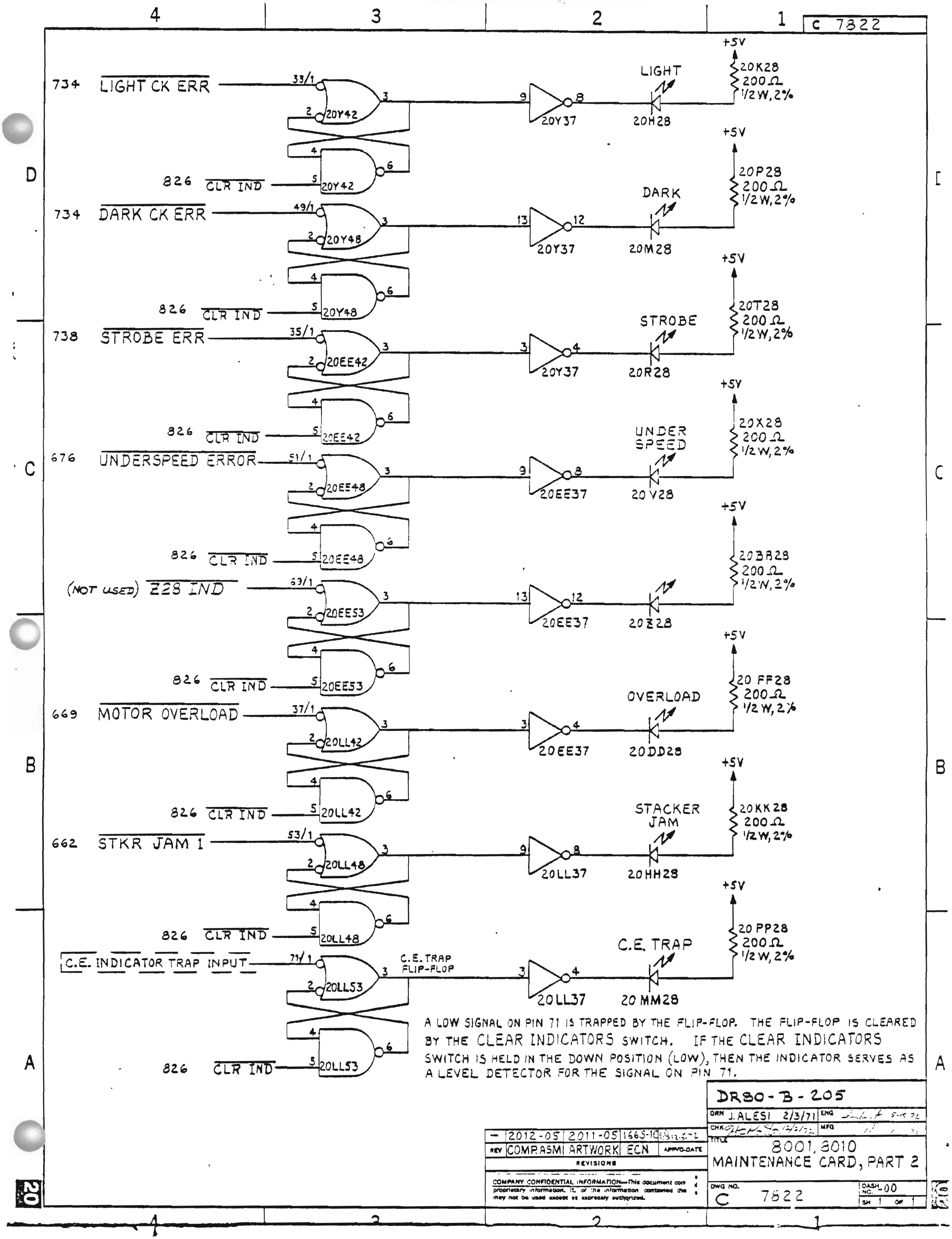
-	2012-05	2011-05	1665-1013 12-6-72
REV	COMP. ASM	ARTWORK	ECN	APPVD-DATE

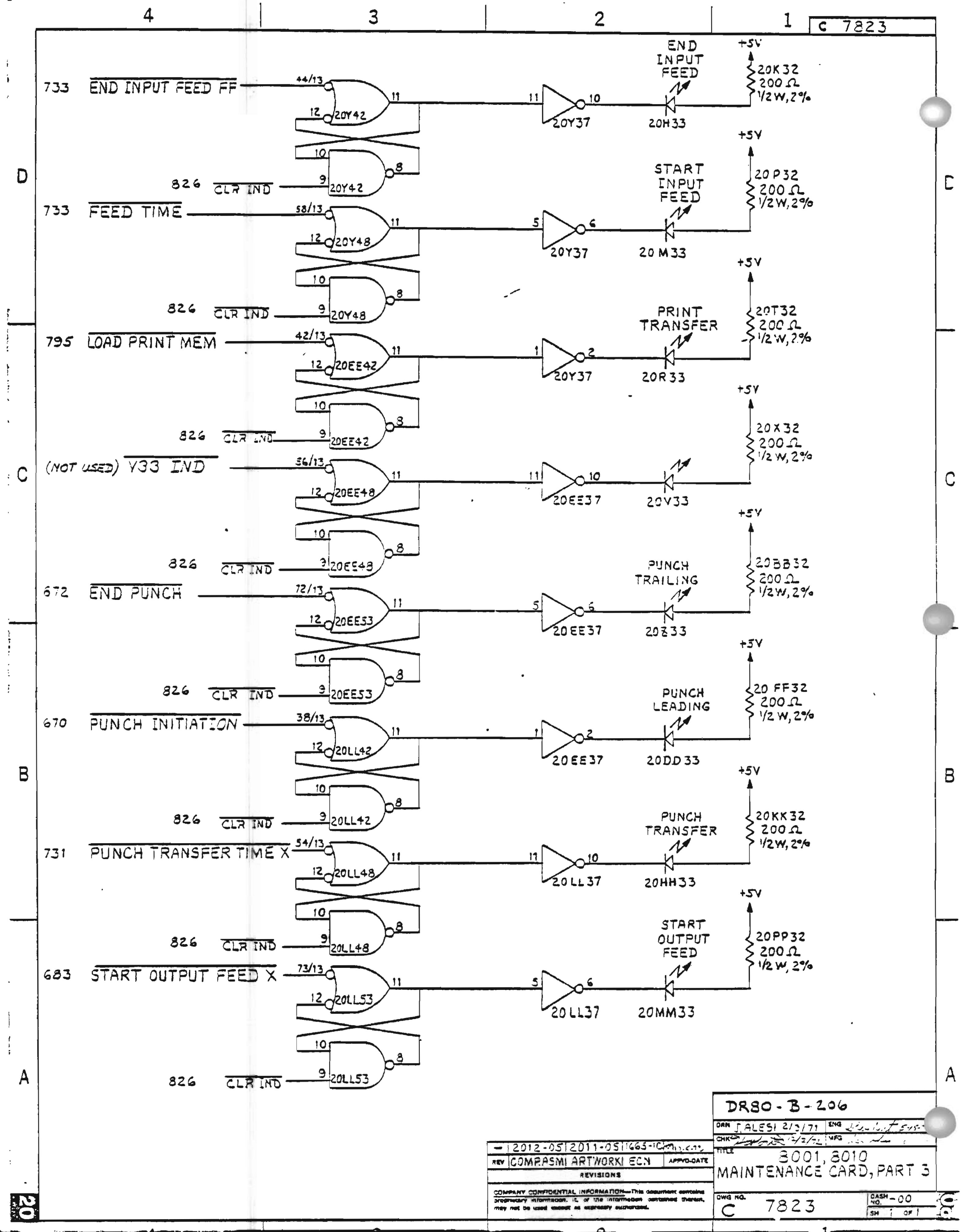
REVISIONS

TITLE 8001, 8010 MAINTENANCE CARD, PART 1

DWG NO. C 7821 DASH NO. -00 SH 1 OF 1

20


C 7822
734 LIGHT CK ERR
826 CLR IND
734 DARK CK ERR
826 CLR IND
738 STROBE ERR
826 CLR IND
676 UNDERSPEED ERROR
826 CLR IND
(NOT USED) Z28 IND
826 CLR IND
669 MOTOR OVERLOAD
826 CLR IND
662 STKR JAM 1
826 CLR IND
C.E. INDICATOR TRAP INPUT
826 CLR IND
20Y42
20Y48
20EE42
20EE48
20EE53
20LL42
20LL48
20LL53
C.E. TRAP FLIP-FLOP
20Y37
20EE37
20LL37
LIGHT 20H28
DARK 20M28
STROBE 20R28
UNDER SPEED 20V28
20Z28
OVERLOAD 20DD28
STACKER JAM 20HH28
C.E. TRAP 20MM28
+5V
20K28 200Ω 1/2W, 2%
20P28 200Ω 1/2W, 2%
20T28 200Ω 1/2W, 2%
20X28 200Ω 1/2W, 2%
20BB28 200Ω 1/2W, 2%
20FF28 200Ω 1/2W, 2%
20KK28 200Ω 1/2W, 2%
20PP28 200Ω 1/2W, 2%
A LOW SIGNAL ON PIN 71 IS TRAPPED BY THE FLIP-FLOP. THE FLIP-FLOP IS CLEARED BY THE CLEAR INDICATORS SWITCH. IF THE CLEAR INDICATORS SWITCH IS HELD IN THE DOWN POSITION (LOW), THEN THE INDICATOR SERVES AS A LEVEL DETECTOR FOR THE SIGNAL ON PIN 71.
DR80-B-205
DRN J. ALESI 2/3/71
TITLE 8001, 8010 MAINTENANCE CARD, PART 2
DWG NO. C 7822
DASH NO. -00
SH 1 OF 1
2012-05 2011-05 1665-10
REV COMP. ASM ARTWORK ECN APPVD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION—This document contains proprietary information. It, or the information contained therein, may not be used except as expressly authorized.
20


C 7823
733 END INPUT FEED FF
826 CLR IND
733 FEED TIME
826 CLR IND
795 LOAD PRINT MEM
826 CLR IND
(NOT USED) Y33 IND
826 CLR IND
672 END PUNCH
826 CLR IND
670 PUNCH INITIATION
826 CLR IND
731 PUNCH TRANSFER TIME X
826 CLR IND
683 START OUTPUT FEED X
826 CLR IND
20Y42
20Y48
20EE42
20EE48
20EE53
20LL42
20LL48
20LL53
20Y37
20EE37
20LL37
END INPUT FEED
START INPUT FEED
PRINT TRANSFER
PUNCH TRAILING
PUNCH LEADING
PUNCH TRANSFER
START OUTPUT FEED
20H33
20M33
20R33
20V33
20Z33
20DD33
20HH33
20MM33
+5V
20K32 200Ω 1/2W, 2%
20P32 200Ω 1/2W, 2%
20T32 200Ω 1/2W, 2%
20X32 200Ω 1/2W, 2%
20BB32 200Ω 1/2W, 2%
20FF32 200Ω 1/2W, 2%
20KK32 200Ω 1/2W, 2%
20PP32 200Ω 1/2W, 2%
DR80-B-206
DRN ALESI 2/3/71
CHK
ENG
MFG
TITLE 3001, 8010 MAINTENANCE CARD, PART 3
DWG NO. C 7823
DASH NO. -00
SH 1 OF 1
— 2012-05 2011-05 1663-1
REV COMPASMI ARTWORK ECN APPVD-DATE
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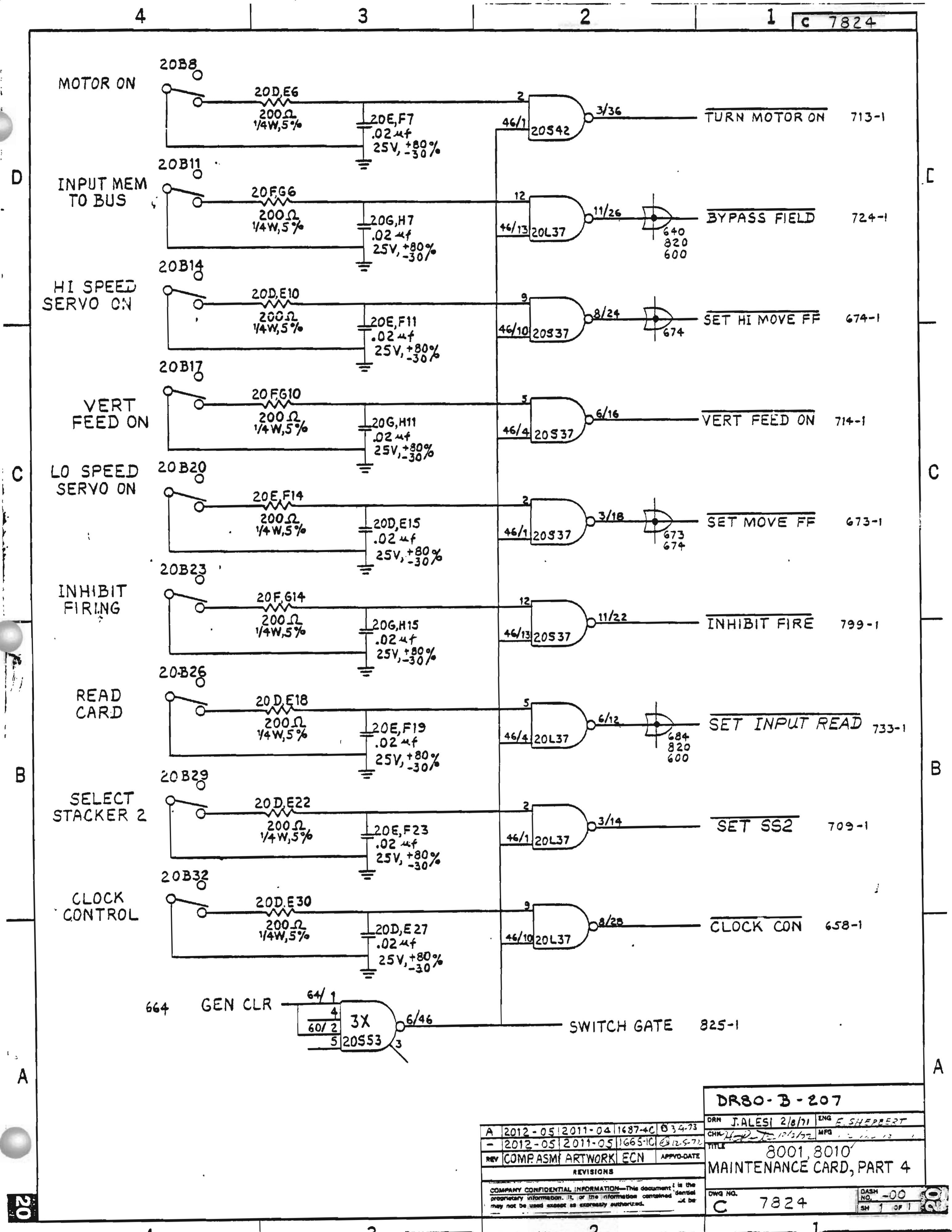
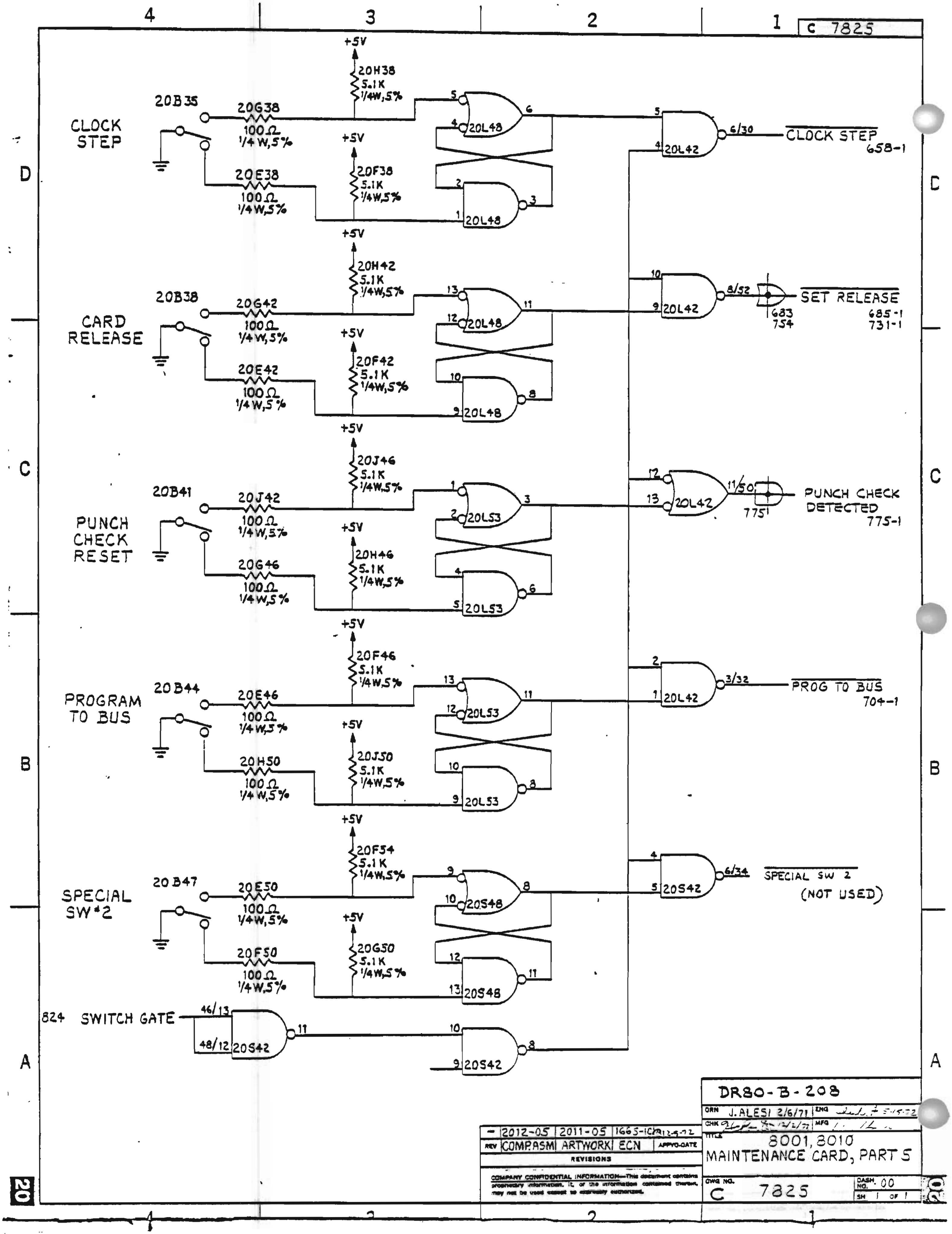


C 7824
MOTOR ON
20B8
20D,E6
200Ω 1/4W,5%
20E,F7 .02 uf 25V, +80% -30%
20S42
TURN MOTOR ON 713-1
INPUT MEM TO BUS
20B11
20F,G6
20G,H7
20L37
BYPASS FIELD 724-1
640 820 600
HI SPEED SERVO ON
20B14
20D,E10
20E,F11
20S37
SET HI MOVE FF 674-1
674
VERT FEED ON
20B17
20F,G10
20G,H11
VERT FEED ON 714-1
LO SPEED SERVO ON
20B20
20E,F14
20D,E15
SET MOVE FF 673-1
673 674
INHIBIT FIRING
20B23
20F,G14
20G,H15
INHIBIT FIRE 799-1
READ CARD
20B26
20D,E18
20E,F19
SET INPUT READ 733-1
684 820 600
SELECT STACKER 2
20B29
20D,E22
20E,F23
SET SS2 709-1
CLOCK CONTROL
20B32
20D,E30
20D,E27
CLOCK CON 658-1
664 GEN CLR
3X 20S53
SWITCH GATE 825-1
DR80-B-207
DRN J. ALESI 2/8/71
ENG E. SHEPPERT
CHK
MFG
TITLE 8001, 8010 MAINTENANCE CARD, PART 4
A 2012-05 2011-04 1687-4C
- 2012-05 2011-05 1665-1C
REV COMP. ASM ARTWORK ECN APPVD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION—This document is the proprietary information... may not be used except as expressly authorized.
DWG NO. C 7824
DASH NO. -00
SH 1 OF 1
20

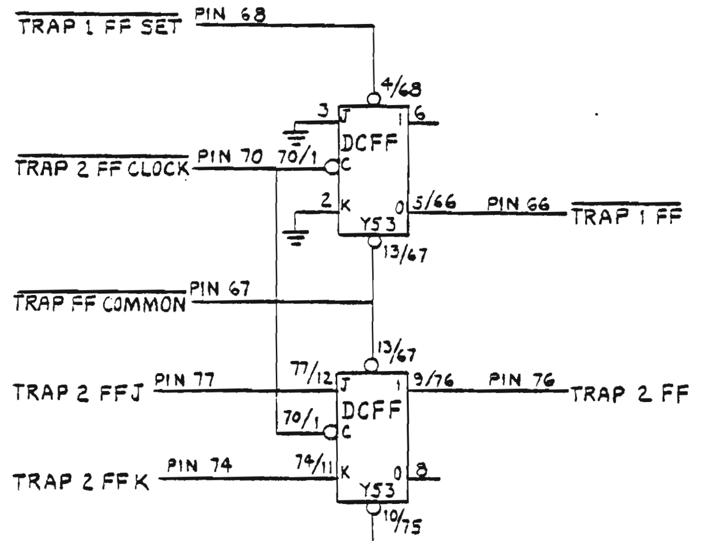

C 7825
CLOCK STEP
20B35
20G38 100Ω 1/4W,5%
20E38 100Ω 1/4W,5%
+5V
20H38 5.1K 1/4W,5%
20F38 5.1K 1/4W,5%
20L48
20L42
6/30
CLOCK STEP
658-1
CARD RELEASE
20B38
20G42 100Ω 1/4W,5%
20E42 100Ω 1/4W,5%
20H42 5.1K 1/4W,5%
20F42 5.1K 1/4W,5%
8/52
683
754
SET RELEASE
685-1
731-1
PUNCH CHECK RESET
20B41
20J42 100Ω 1/4W,5%
20G46 100Ω 1/4W,5%
20J46 5.1K 1/4W,5%
20H46 5.1K 1/4W,5%
20L53
11/50
775
PUNCH CHECK DETECTED
775-1
PROGRAM TO BUS
20B44
20E46 100Ω 1/4W,5%
20H50 100Ω 1/4W,5%
20F46 5.1K 1/4W,5%
20J50 5.1K 1/4W,5%
3/32
PROG TO BUS
704-1
SPECIAL SW#2
20B47
20E50 100Ω 1/4W,5%
20F50 100Ω 1/4W,5%
20F54 5.1K 1/4W,5%
20G50 5.1K 1/4W,5%
20S48
20S42
6/34
SPECIAL SW 2
(NOT USED)
824 SWITCH GATE
46/13
48/12
DR80-B-208
DRN J. ALESI 2/6/71
TITLE 8001, 8010 MAINTENANCE CARD, PART 5
DWG NO. C 7825
DASH NO. 00
SH 1 OF 1
2012-05 2011-05 1665-1C
COMPASM ARTWORK ECN
REVISIONS
COMPANY CONFIDENTIAL INFORMATION
20

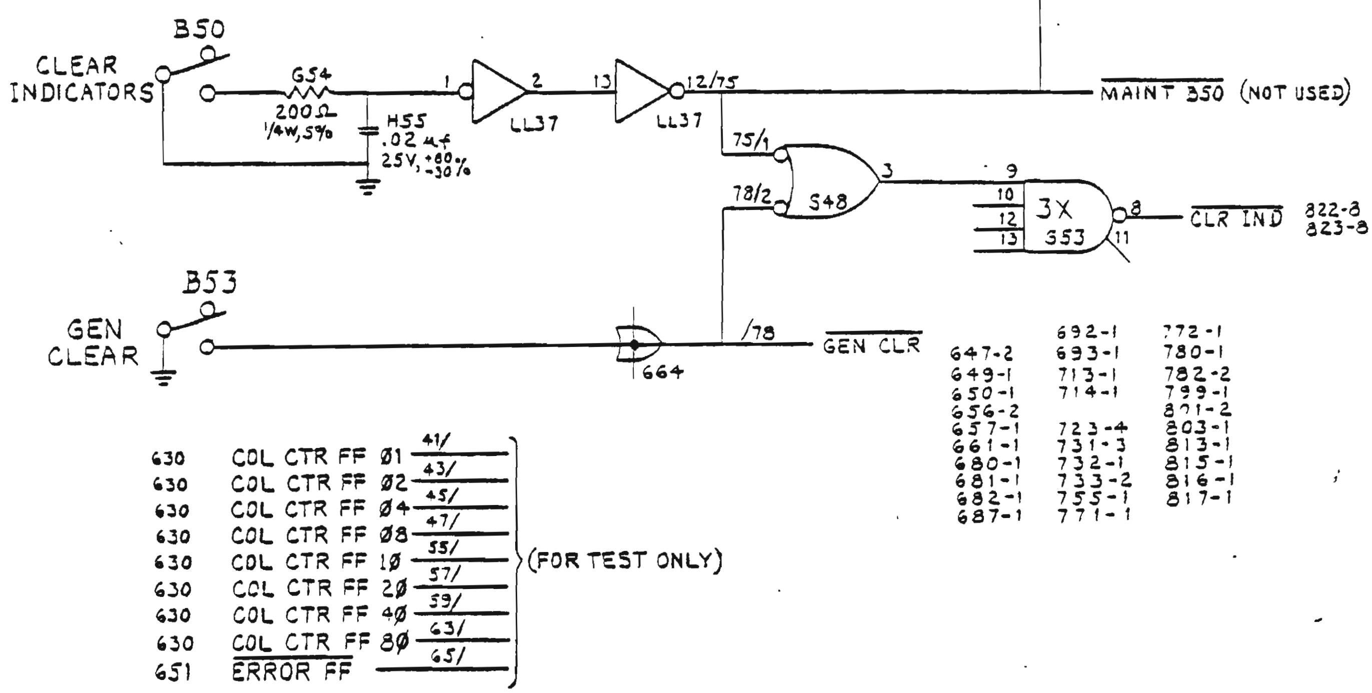
C 7826

THESE TWO FLIP-FLOPS CAN BE USED FOR EMERGENCY TROUBLE SHOOTING. THE OUTPUT OF TRAP 1 FF (PIN 66) OR TRAP 2 FF (PIN 76) CAN BE WIRED TO THE C.E. TRAP FLIP-FLOP (PIN 71) FOR VISUAL DISPLAY

TRAP 1 FF	
ACTION	RESULT
LOW ON PIN 68.	JAM FF SET = LOW ON PIN 66.
LOW ON PIN 67.	JAM FF CLEAR = HIGH ON PIN 66
LOW ON PIN 70.	NO EFFECT.

TRAP 2 FF	
ACTION	RESULT
LOW ON PIN 67.	JAM FF SET = HIGH ON PIN 76.
CLEAR INDICATORS SWITCH	MAN CLEAR = LOW ON PIN 76.
LOW EDGE ON PIN 70.	(1) TOGGLES IF PINS 77 & 74 ARE HIGH. (2) SETS PIN 76 HIGH IF PIN 77 IS HIGH AND PIN 74 IS LOW. (3) CLEARS PIN 76 LOW IF PIN 77 IS LOW AND PIN 74 IS HIGH. (4) NO EFFECT IF PINS 77 & 74 ARE LOW.





DR80-3-209

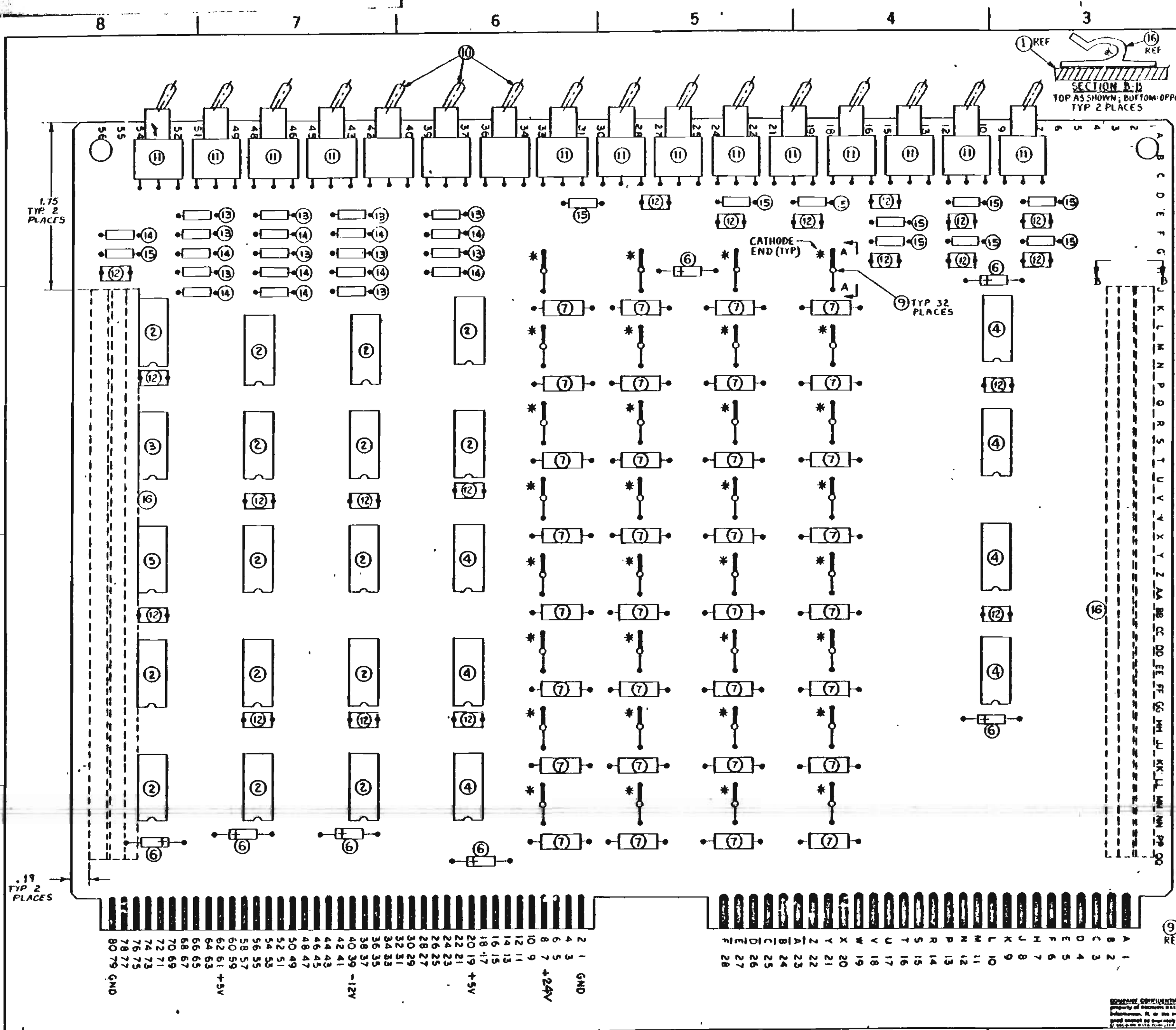
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	2012-05	2011-05	1687-4C	3-6-73
–	2012-05	2011-05	1665-1C	12-7-72

REVISIONS

DRN J. ALESI 12/6/72

TITLE 8001, 8010 MAINTENANCE CARD, PART 6

DWG NO. C 7826 DASH NO. -00 SH 1 OF 1



P9
BB9
NP53
U,V37
V42
V47
BB53
GG,HH37
GG,HH42
GG,HH47
+5V .02 μf 25V, +80% -30%

H,J9
H26
GG,HH9
NN,PP43
NN,PP48
PP53
QQ36
+5V 4.7 μf 10V, 20%

REV	DESCRIPTION	APP'D DATE
J	ADDED THIS SHEET	[illegible]
K	REVISED PER ECN 1663-1C	[illegible]
L	REVISED PER ECN 2118-1C	[illegible]
M	REVISED PER ECN 2503-1C	[illegible]
N	REVISED PER ECN 2754-1C	[illegible]
P	REVISED /ECN 3913-1C	[illegible]

*UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 2012-05 ON ARTWORK 2011-05

LOGIC DIAGRAM CHARTS

8001, 8010		8035, 8045	
DWG. NO.	REV.	DWG. NO.	REV.
7821	-	9393	-
7822	-	9394	-
7823	-	9395	-
7824	-	9396	-
7825	-	9397	-
7826	-	9398	-

*VARIATION REFERENCE

VAR	ASSEMBLY	ARTWORK
I	2012-03, -04 -05	2011-03
II	2012-05	2011-05

9640

DIAGNOSTIC		TYPE DD		TYPE 1		9601,10,60-03		9635/45-04	
DWG. NO.	REV.	DWG. NO.	REV.	DWG. NO.	REV.	DWG. NO.	REV	DWG. NO.	REV
7152	A	7183	A	7220	A	11600	-	11308	-
7153	B	7184	-	7221	-	11601	-	11309	-
7154	A	7185	A	7222	A	11602	-	11310	-
7155	A	7186	A	7223	A	11603	-	11311	-
7156	A	7187	-	7224	A	11604	-	11312	-
7157	A	7188	A	7225	A	11605	-	11313	-

9650

DIAGNOSTIC		TYPE DD		TYPE 1		TYPE 6 DIAGNOSTIC		TYPE 6 INTERFACE	
DWG. NO.	REV.	DWG. NO.	REV.	DWG. NO.	REV.	DWG. NO.	REV.	DWG. NO.	REV
5477	C	5483	B	5765	C	10217	-	10223	-
5478	C	5484	A	5766	B	10218	-	10224	-
5479	B	5485	B	5767	B	10219	-	10225	-
5480	D	5486	B	5768	B	10220	-	10226	-
5481	B	5487	A	5769	C	10221	-	10227	-
5482	B	5488	C	5770	B	10222	-	10228	-

9601,10,60-01		9635,9645-03		9620		9630	
DWG. NO.	REV.	DWG. NO.	REV	DWG. NO.	REV.	DWG. NO.	REV.
8170	D	4857	B	5678	A	7351	A
8171	D	4858	C	5679	B	7352	A
8172	C	4859	B	5680	A	7353	A
8173	D	4860	B	5681	B	7354	A
8174	C	4861	B	5682	A	7355	A
8175	C	4862	B	5683	A	7356	A

** INDICATES SPARE NOT TO BE USED UNLESS APPROVED BY ENGINEERING.

SPARE GATES

TYPE	LOC	OUTPUT PIN NO.
~~(2/4) INPUT BUFFER (3X)~~	~~553~~	~~8~~
** 4/2 INPUT	S49	3, 6
** 6/1 HEX	LL37	2, 12

FOR LIST OF PARTS SEE DETAILED PARTS LIST

QTY	ITEM	DWG SIZE	PART NO.	DESCRIPTION

DR80-B-210

COMPONENT ASSEMBLY MAINTENANCE BOARD

DWG NO D 2012 | DASH NO. * | SH 5 OF

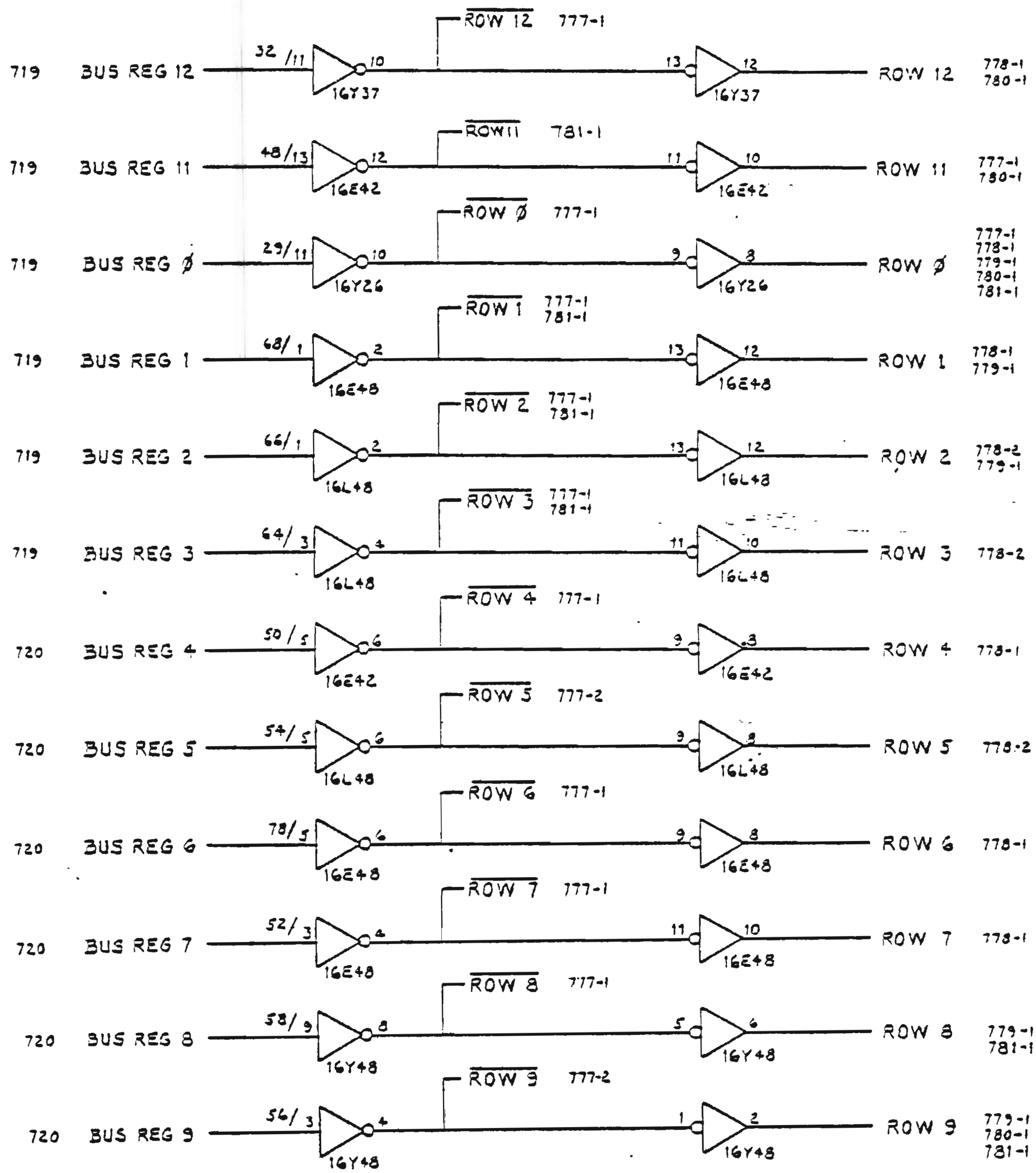
D 2012

LOGIC/SCHEMATIC INDEX

MANUAL	TYPE/FEATURE		
7545	8001/8010 DR 80 Col	PWB	5121-06
902193	8010-11 CR 360	ASM	5122-06

DOCUMENT	SH	RV	DESCRIPTION
			LOCATION: 16
			PRINTER MEMORY
7776		-C	Printer Bus Register Data
7777		-C	Print 12/6 Bit Translate
7778		-B	Non-print Detection Pt-1
7779		-B	Non-print Detection Pt-1
7780		-B	Print Edit
7781		-E	Print Leading Zero Suppress
7782		-D	Print Memory Clocking
7783		-A	Print Memories
7784		-B	Print Data Outputs
7785		--	Odd Column Registers
7786		--	Even Column Register Pt-1
7787		--	Even Column Register Pt-2
7788		-A	Hammer Alert Control
7789		--	Print Blank Control
7790		--	Print Hammer 1 & 2 Comparison
7791		--	Print Hammer 3 & 4 Comparison
5122	04	-K	Schematic - Printer Memory

C 7776



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
C	5122-05	5121-05	2020-4C	
B	5122-04	5121-02	2020-2	
A	5122-03	5121-02	2434-2C	
–	5122-01	5121-01	1432-2C	

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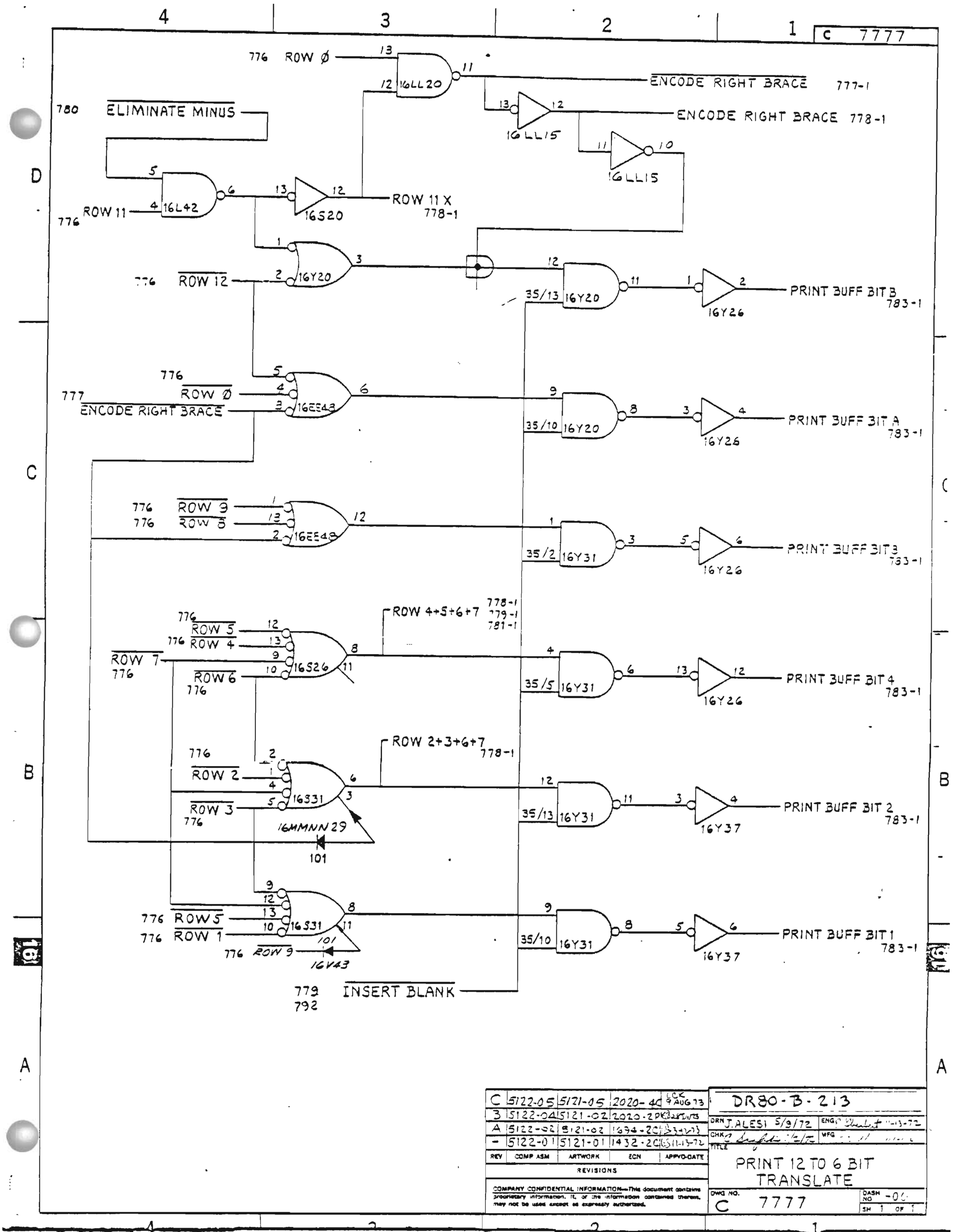
COMPANY CONFIDENTIAL INFORMATION

DR80-3-212

DRN J. ALESI 3/3/72

TITLE PRINTER BUS REGISTER DATA

DWG NO. C 7776


C 7777
776 ROW Ø
16LL20
ENCODE RIGHT BRACE 777-1
ENCODE RIGHT BRACE 778-1
16LL15
780 ELIMINATE MINUS
776 ROW 11
16L42
16S20
ROW 11 X 778-1
776 ROW 12
16Y20
16Y26
PRINT BUFF BIT B 783-1
776 ROW Ø
777 ENCODE RIGHT BRACE
16EE48
PRINT BUFF BIT A 783-1
776 ROW 9
776 ROW 8
PRINT BUFF BIT 3 783-1
16Y31
776 ROW 5
776 ROW 4
ROW 7 776
ROW 6 776
16S26
ROW 4+5+6+7 778-1 779-1 781-1
PRINT BUFF BIT 4 783-1
776 ROW 2
ROW 3 776
16S31
ROW 2+3+6+7 778-1
16MMNN29
101
16Y37
PRINT BUFF BIT 2 783-1
776 ROWS
776 ROW 1
776 ROW 9
16V43
PRINT BUFF BIT 1 783-1
779 792 INSERT BLANK
C 5122-05 5121-05 2020-40 9 AUG 73
3 5122-04 5121-02 2020-2PK
A 5122-02 5121-02 1634-2C
- 5122-01 5121-01 1432-2C 11-13-72
REV COMP ASM ARTWORK ECN APPVD-DATE
REVISIONS
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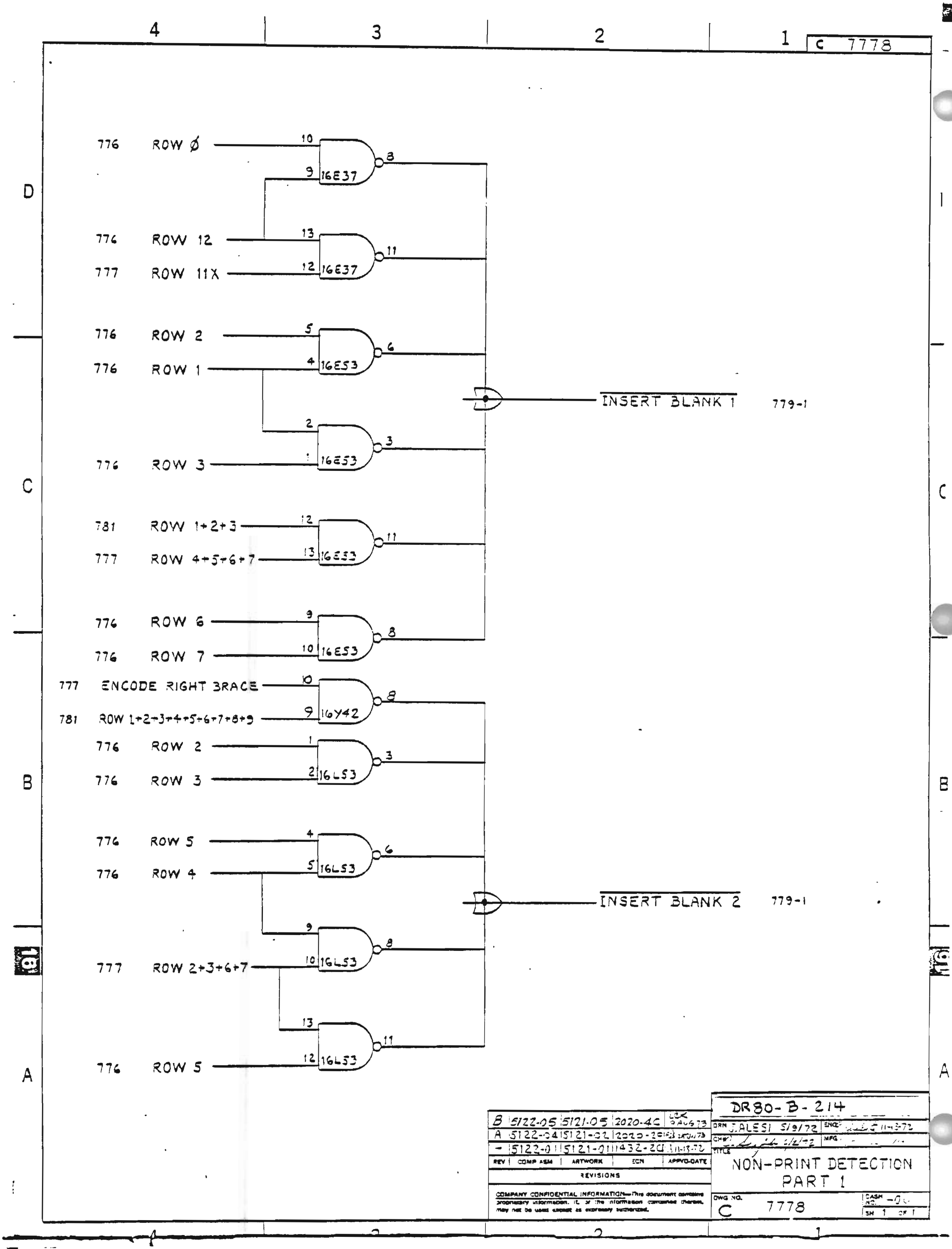
DR80-B-213
DRN J. ALESI 5/9/72
TITLE PRINT 12 TO 6 BIT TRANSLATE
DWG NO. C 7777
SH 1 OF 1


C 7778
776 ROW Ø
776 ROW 12
777 ROW 11X
776 ROW 2
776 ROW 1
776 ROW 3
781 ROW 1+2+3
777 ROW 4+5+6+7
776 ROW 6
776 ROW 7
777 ENCODE RIGHT BRACE
781 ROW 1+2+3+4+5+6+7+8+9
776 ROW 2
776 ROW 3
776 ROW 5
776 ROW 4
777 ROW 2+3+6+7
776 ROW 5
16E37
16E53
16Y42
16L53
INSERT BLANK 1 779-1
INSERT BLANK 2 779-1
DR80-B-214
B 5122-05 5121-05 2020-4C
A 5122-04 5121-02 2020-2
- 5122-01 5121-01 1432-2C
REV COMP ASM ARTWORK ECN APPVD-DATE
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DRN J.ALESI 5/9/72
NON-PRINT DETECTION PART 1
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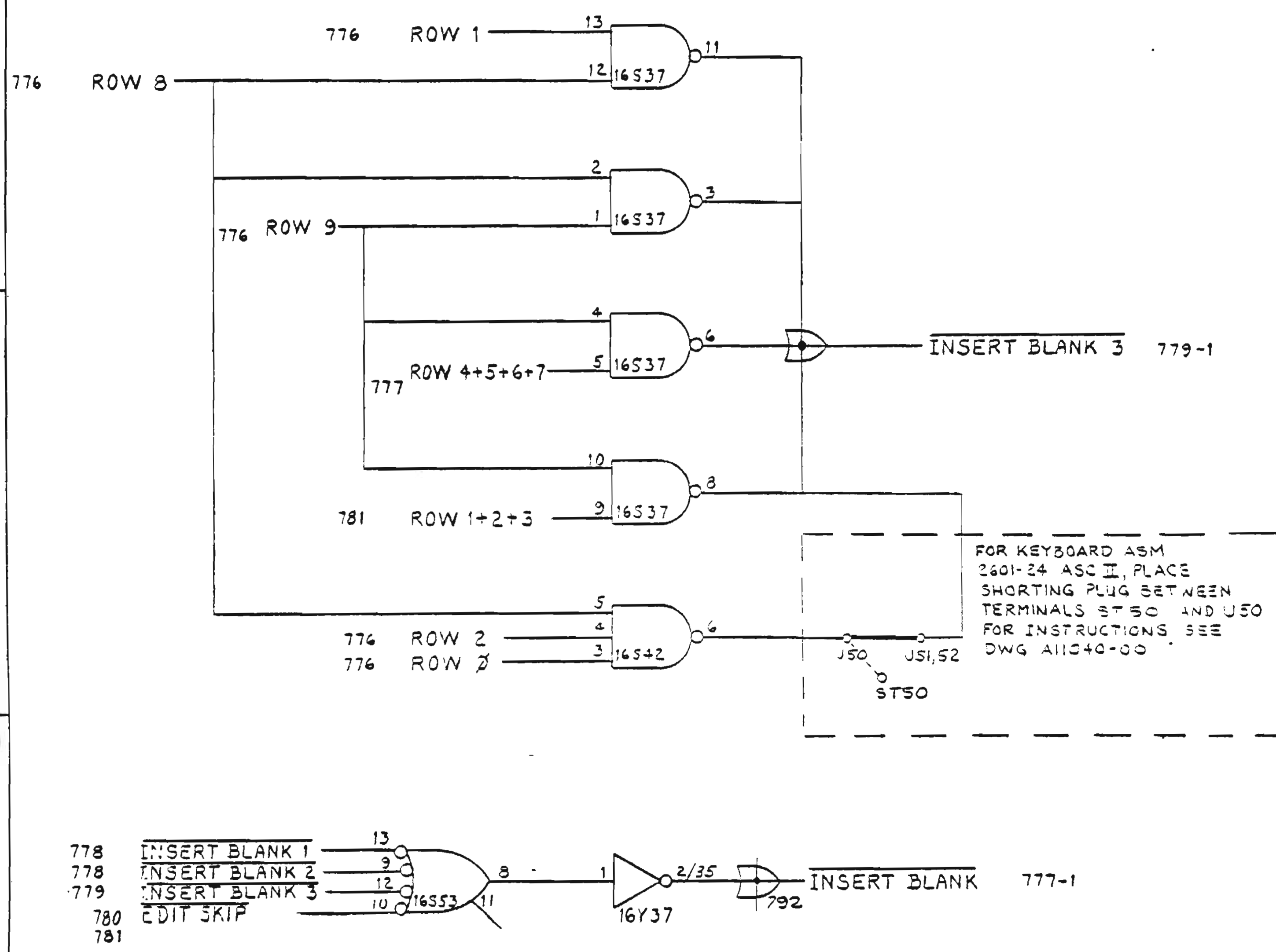
DWG NO. C 7778
SH 1 OF 1

C 7779



DR80 - B - 215

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	5122-06	5121-06	3555-2C	
A	5122-06	5121-05	3555-1P	
-	5122-01	5121-01	1432-2C	3-11-13-72

REVISIONS

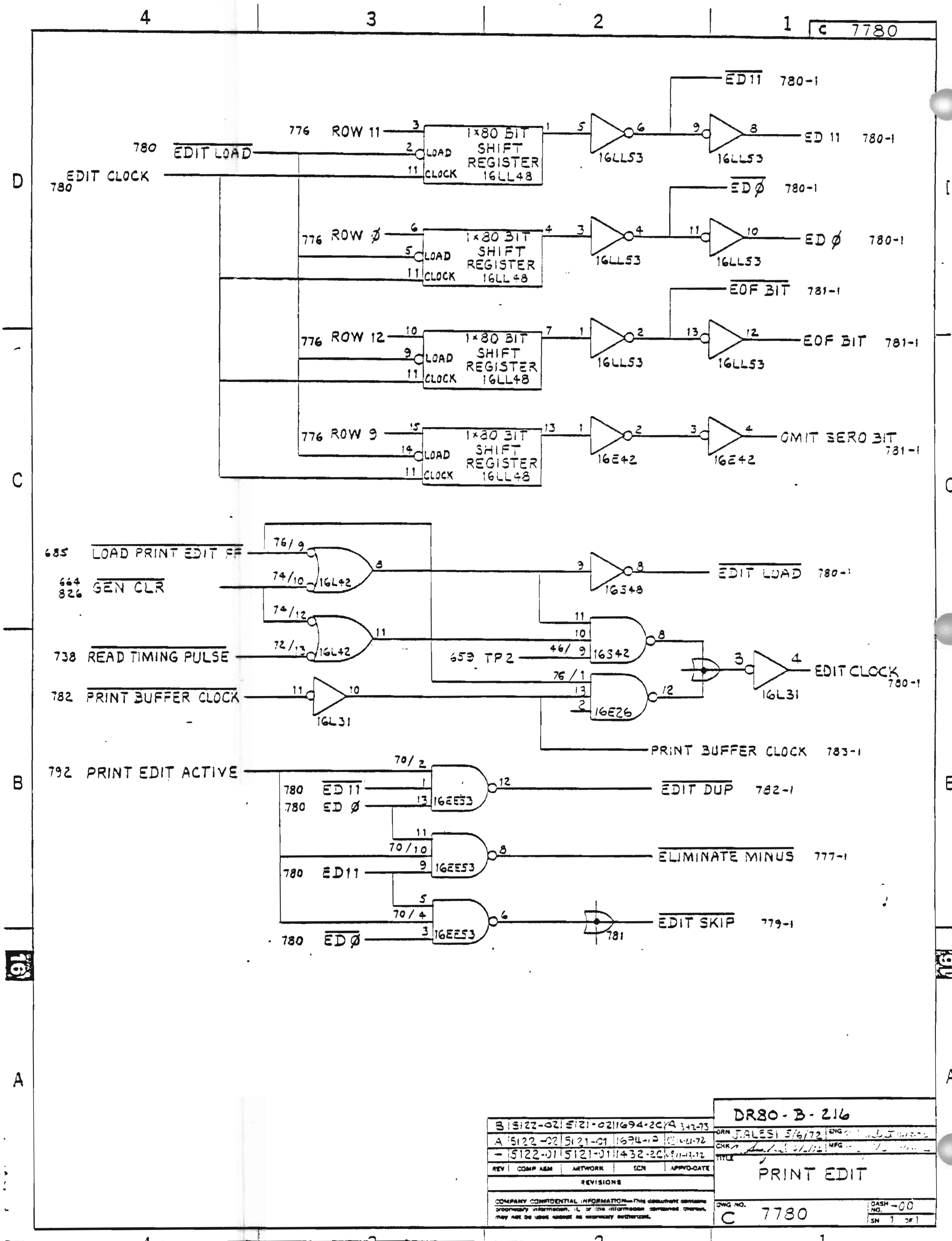
DRN J. ALESI 5/8/72

TITLE NON-PRINT DETECTION, PART 2

DWG NO. C 7779

DASH NO. -00

SH 1 OF 1



C 7780
780 EDIT LOAD
780 EDIT CLOCK
776 ROW 11
776 ROW Ø
776 ROW 12
776 ROW 9
1×80 BIT SHIFT REGISTER 16LL48
LOAD
CLOCK
16LL53
16E42
ED11 780-1
ED 11 780-1
EDØ 780-1
ED Ø 780-1
EOF BIT 781-1
EOF BIT 781-1
OMIT ZERO BIT 781-1
685 LOAD PRINT EDIT FF
664 826 GEN CLR
738 READ TIMING PULSE
782 PRINT BUFFER CLOCK
16L42
16L31
16S48
16S42
16E26
659 TP2
EDIT LOAD 780-1
EDIT CLOCK 780-1
PRINT BUFFER CLOCK 783-1
792 PRINT EDIT ACTIVE
780 ED 11
780 ED Ø
780 ED11
780 ED Ø
16EE53
EDIT DUP 782-1
ELIMINATE MINUS 777-1
EDIT SKIP 779-1
781
DR80-B-216
B 5122-02 5121-02 11694-2C 4-3-73
A 5122-02 5121-01 1694-1P 1-21-72
– 5122-01 5121-01 1432-2C 11-12-72
REV COMP ASM ARTWORK ECN APPVD-DATE
REVISIONS
DRN J. ALESI 5/6/72
CHK
ENG
MFG
TITLE PRINT EDIT
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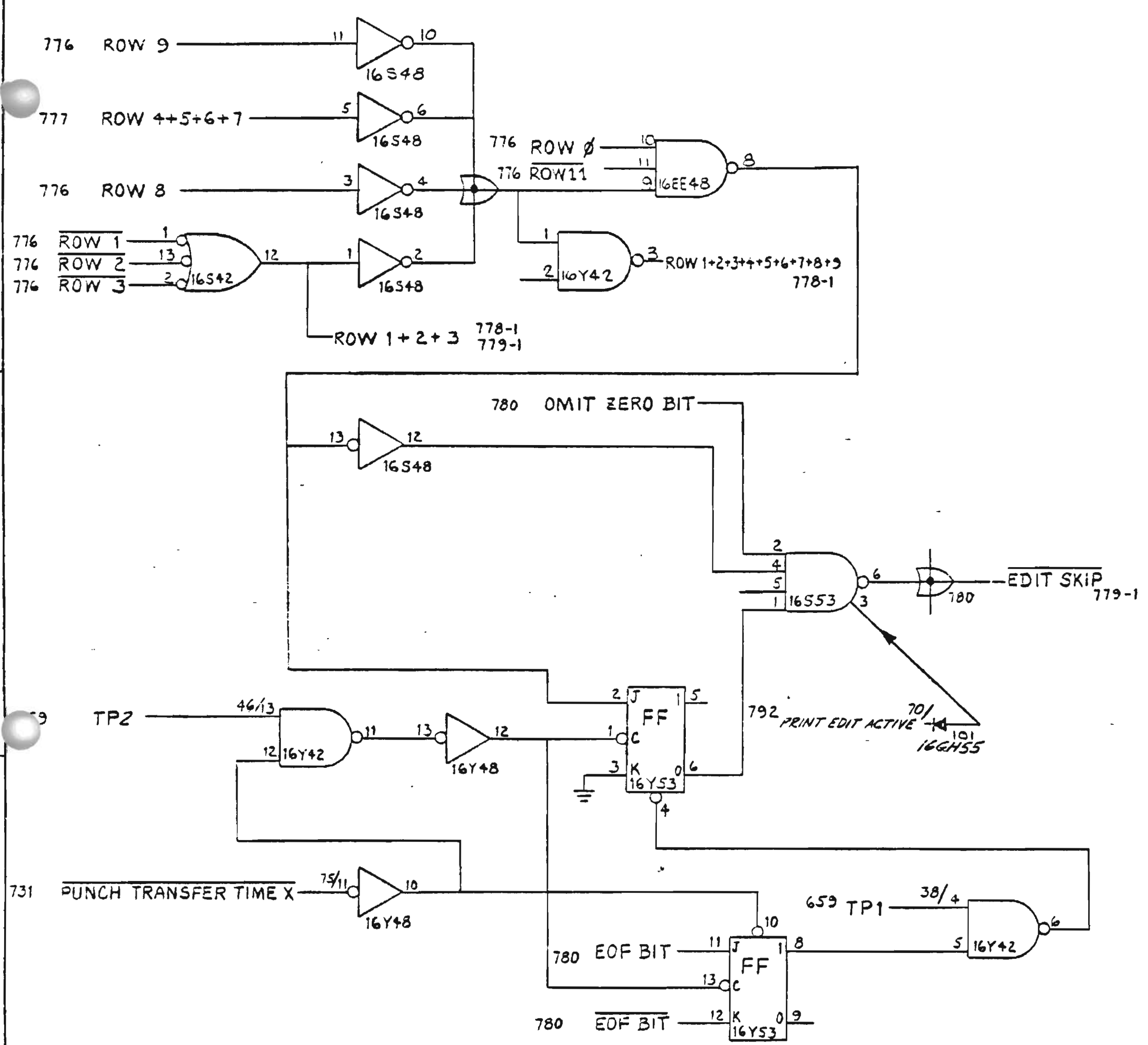
DWG NO. C 7780
DASH NO. –00
SH 1 OF 1



E	5122 05	5121-05	5664-1C	
D	5122-05	5121-05	2020-4C	
C	5122-04	5121-02	2020-2P	
B	5122-02	5121-02	1989-1C	
A	5122-02	5121-02	1634-2C	
-	5122-01	5121-01	1432-2C	11-13-72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

DR80-B-217

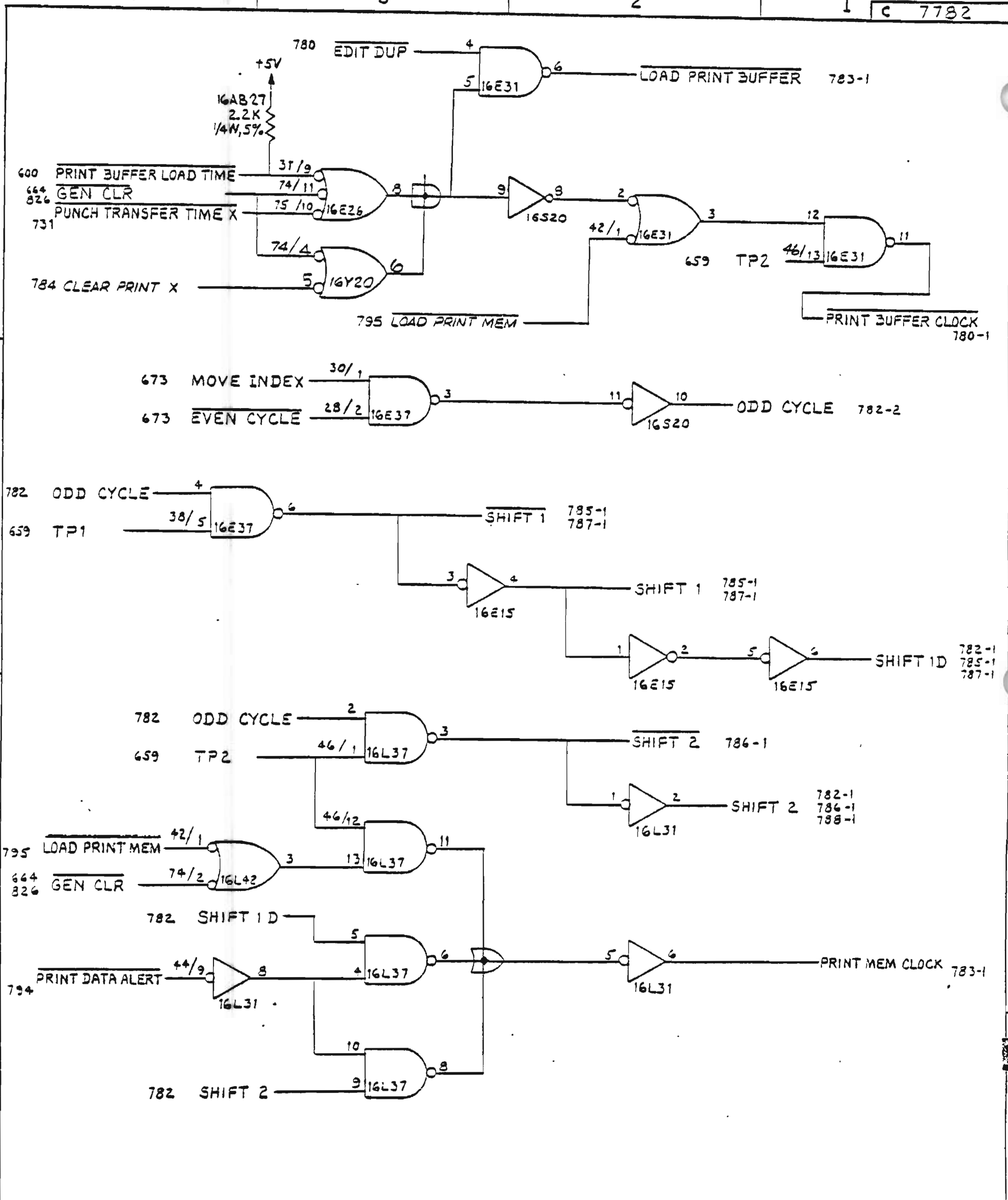
DRN J. ALESI 5/8/72

CHKD 6/2/72

TITLE PRINT LEADING ZERO SUPPRESS

DWG NO. C 7781

DASH NO. -00



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
D	5122-05	5121-05	2020-4D	3 AUG 73
C	5122-05	5121-02	2020-3A	18 JUL 73
B	5122-03	5121-02	2020-1D	
A	5122-02	5121-02	1634-2	
-	5122-01	5121-01	1432-2	

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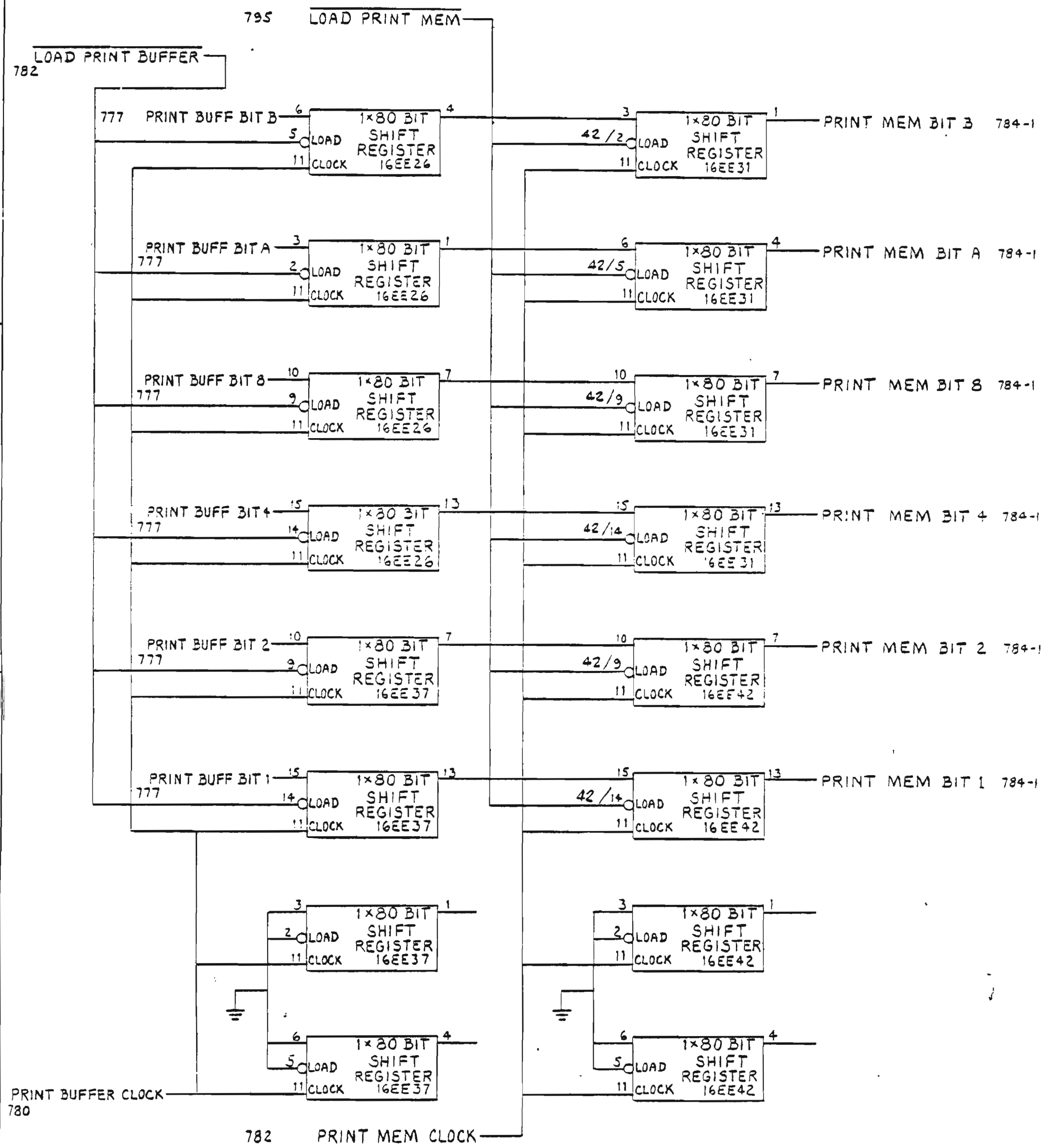
DR30-3-218

DRN J. ALESI 5/8/72

PRINT MEMORY CLOCKING

DWG NO. C 7782 DASH NO. -00 SH 1 OF 1



A	5122-02	5121-02	1694-2C	3-14-73
-	5122-01	5121-01	1432-2C	11-13-72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

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DR80-B-219

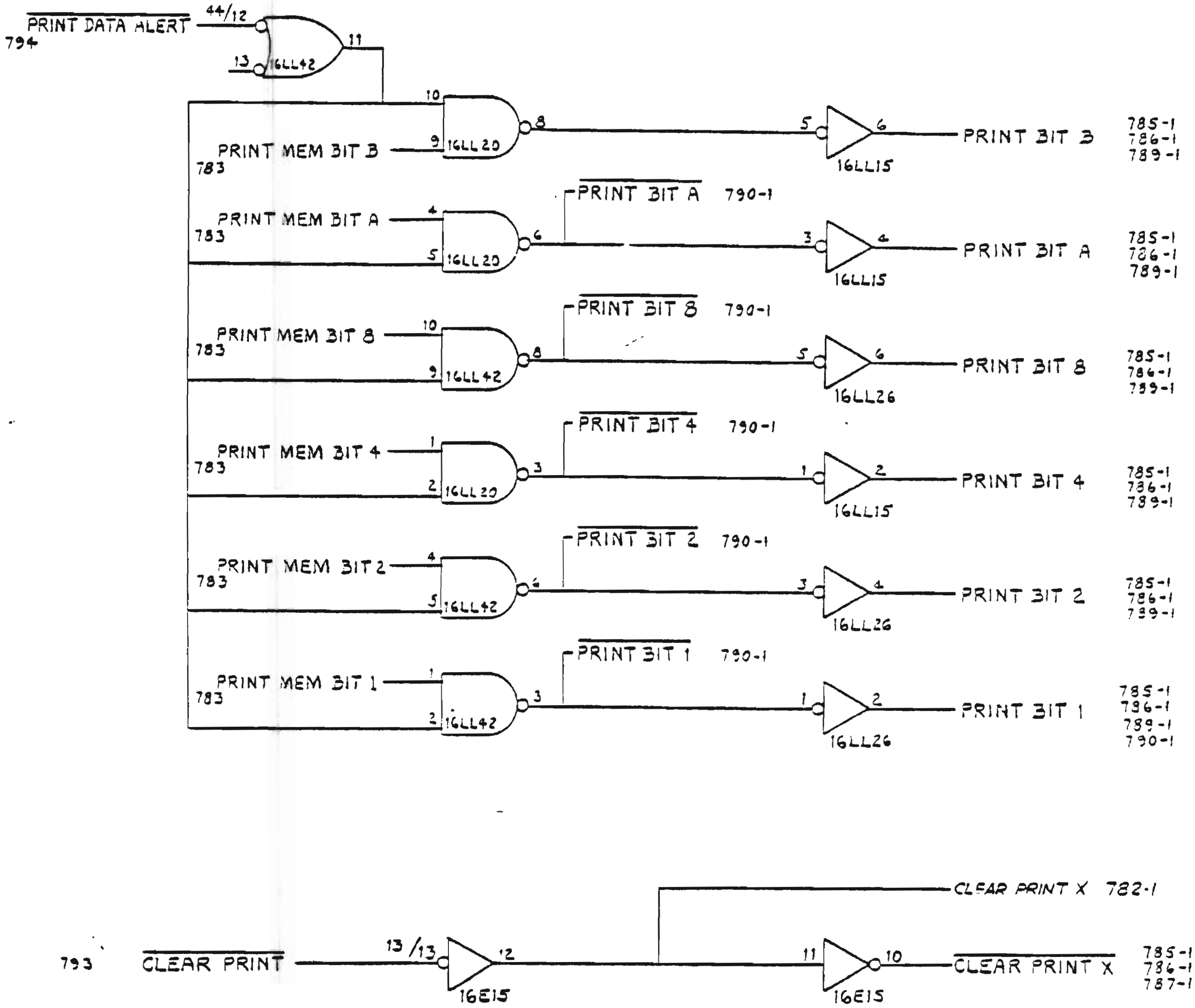
DRN J. ALESI 10/16/72 ENG 11-13-72

CHK 11/11/72 MFG

TITLE PRINT MEMORIES

DWG NO. C 7783 DASH NO. -00 SH 1 OF 1

C 7784



B	5122-05	5121-05	2020-4C	AUG 73
A	5122-05	5121-02	2020-3P	JUL 73
—	5122-01	5121-01	1432-2C	11-13-72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

DR80-B-220

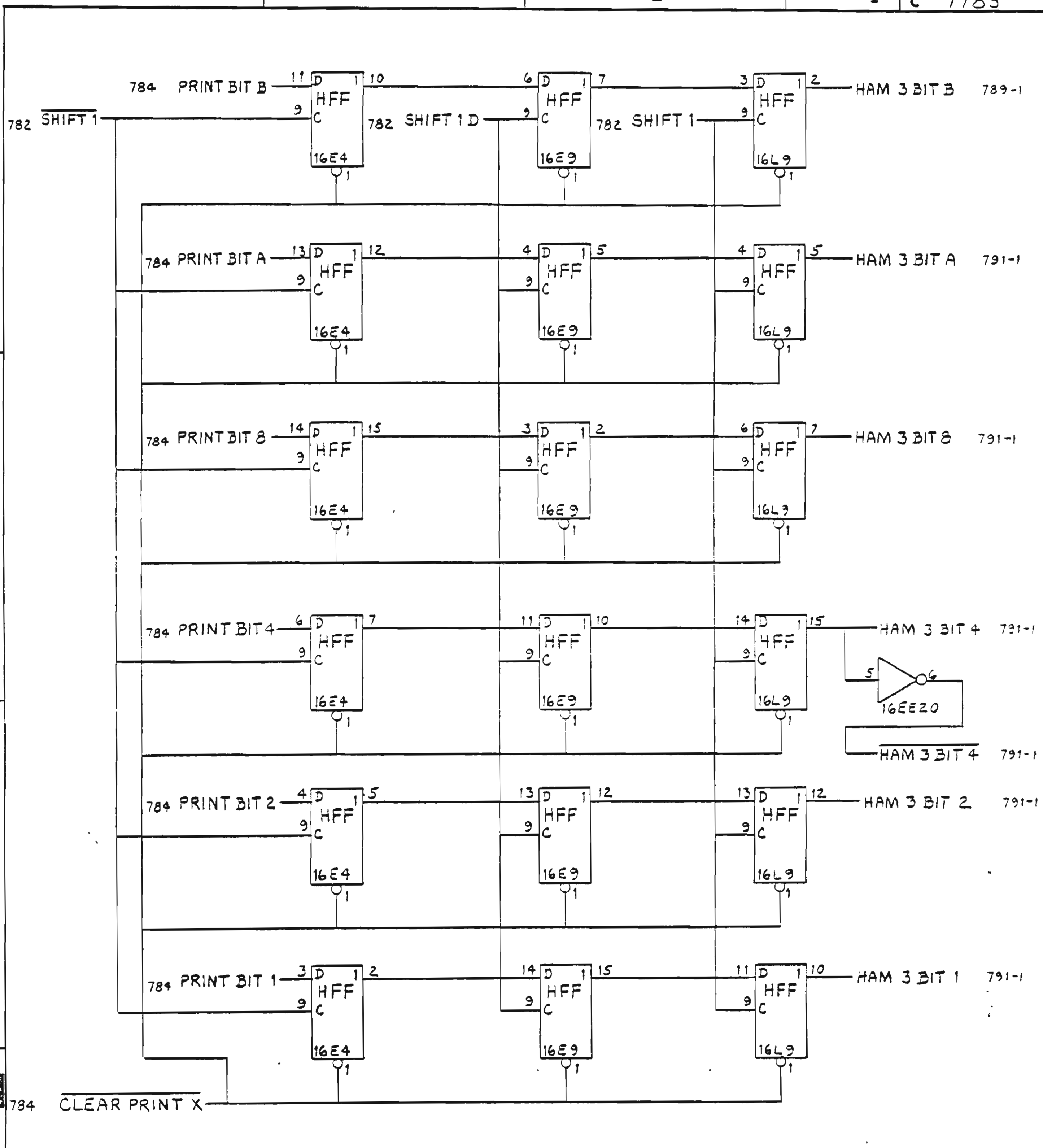
DRN J. ALESI 5/9/72

TITLE PRINT DATA OUTPUTS

DWG NO. C 7784

DASH NO. -00

SH 1 OF 1



DR80-B-221

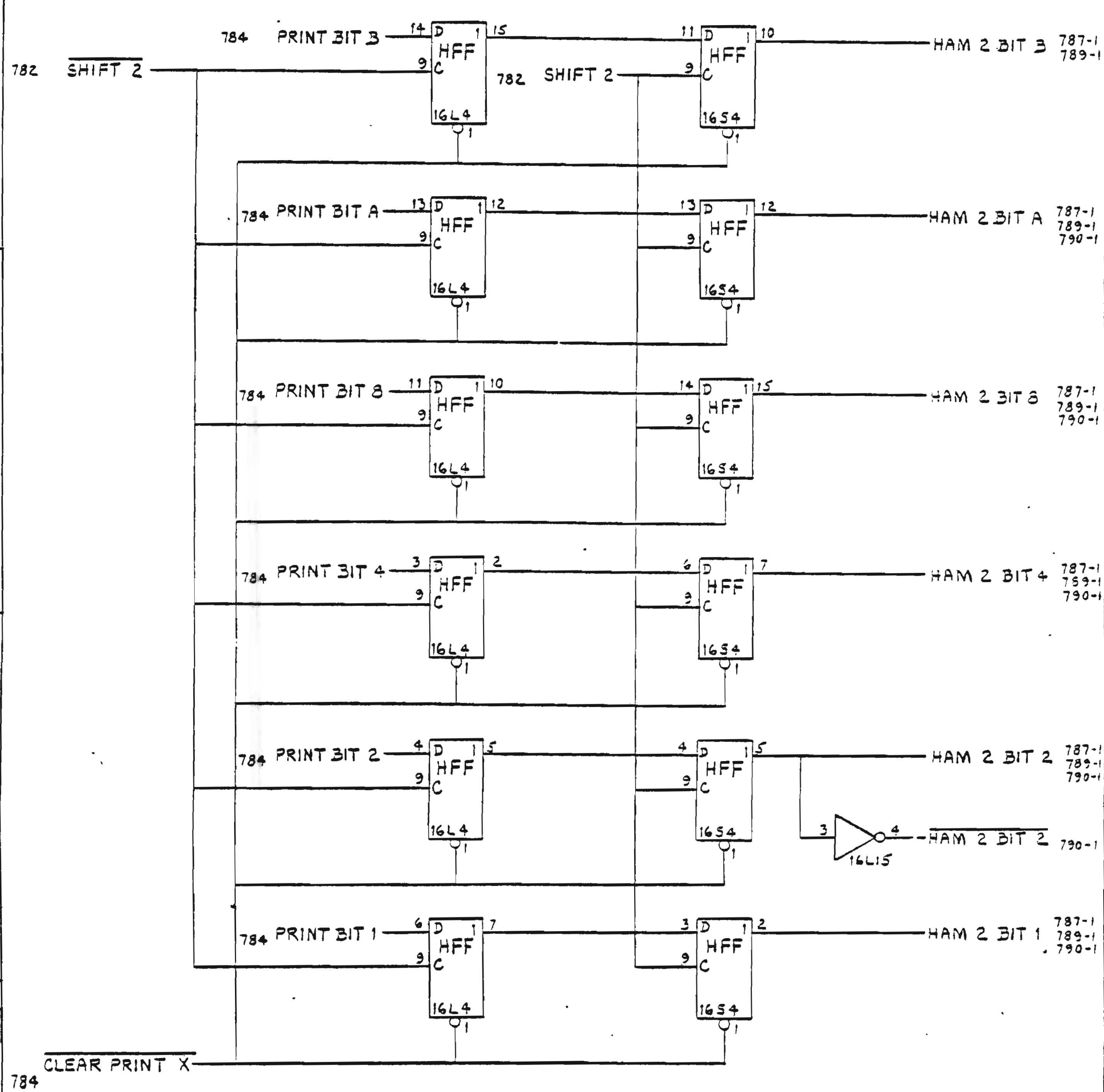
DRN J. ALESI 5/8/72

TITLE ODD COLUMN REGISTERS

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	5122-01	5121-01	11432-2C	

REVISIONS

DWG NO. C 7785 -00 SH 1 OF 1



C 7786

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
—	5122-01	5121-01	1432-20	11-13-72

REVISIONS

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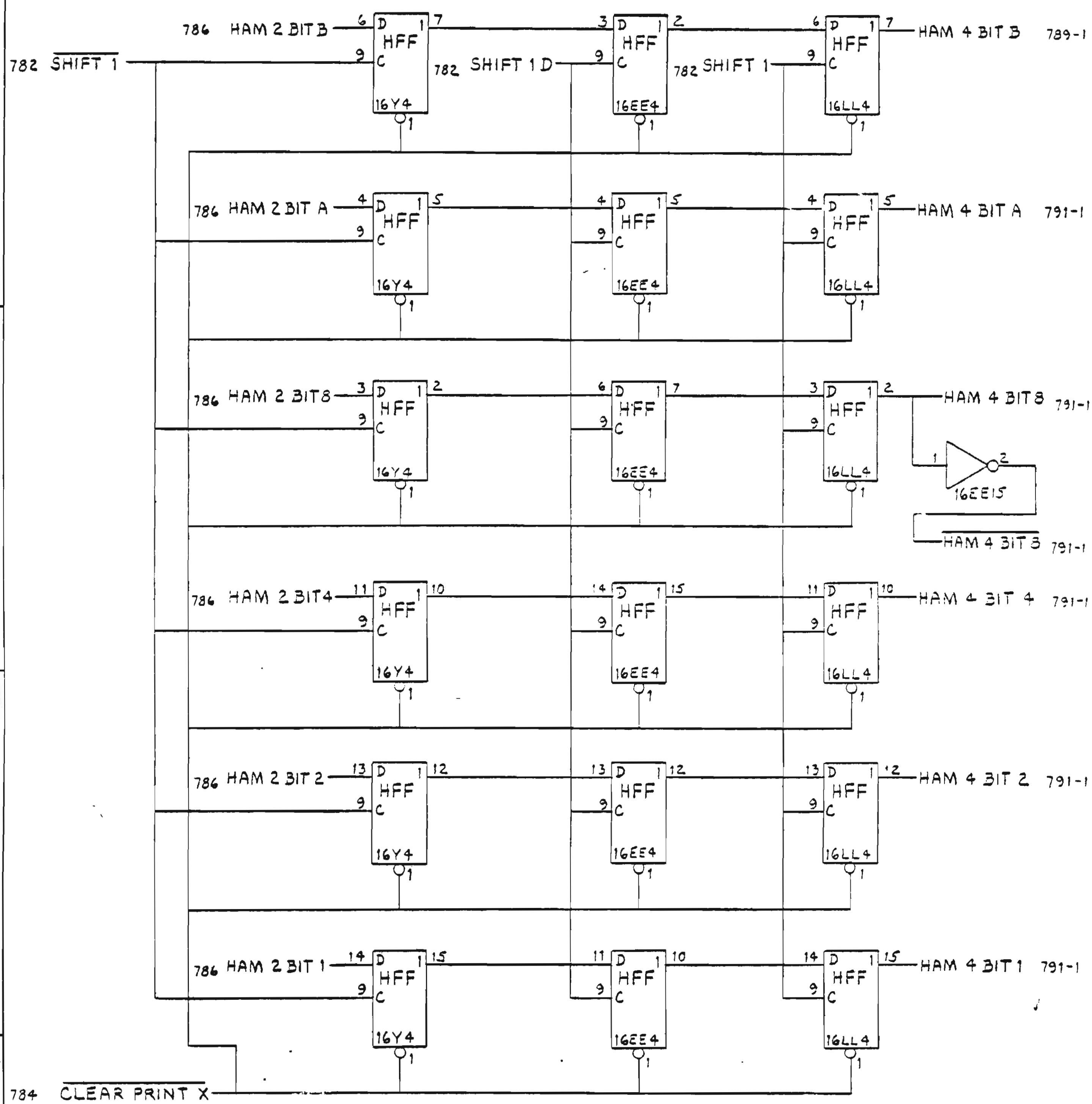
DR80-B-222

DRN J. ALESI 5/8/72

TITLE PRINT EVEN COLUMN REGISTERS PART 1

DWG NO. C 7786 DASH NO. -00 SH 1 OF 1



–	5122-01	5121-01	1432-2C	11-13-72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

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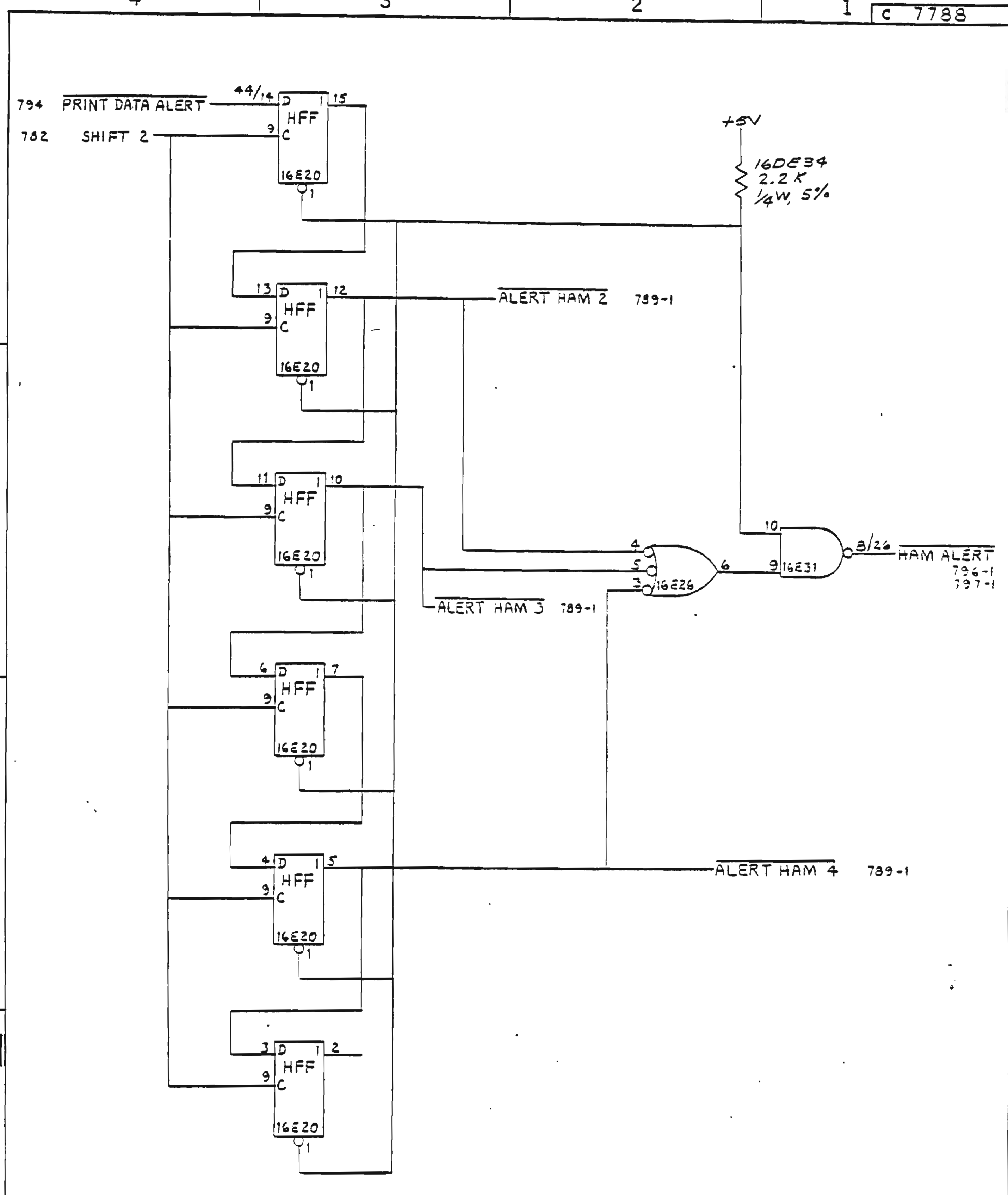
DR80-B-223

DRN J. ALESI 5/8/72

PRINT EVEN COLUMN REGISTERS PART 2

DWG NO. C 7787 DASH NO -00 SH 1 OF 1

C 7787



DR80-B-224

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	5122-03	5121-02	2020-1P	@ 4-16-73
-	5122-01	5121-01	1432-2C	@ 11-13-72

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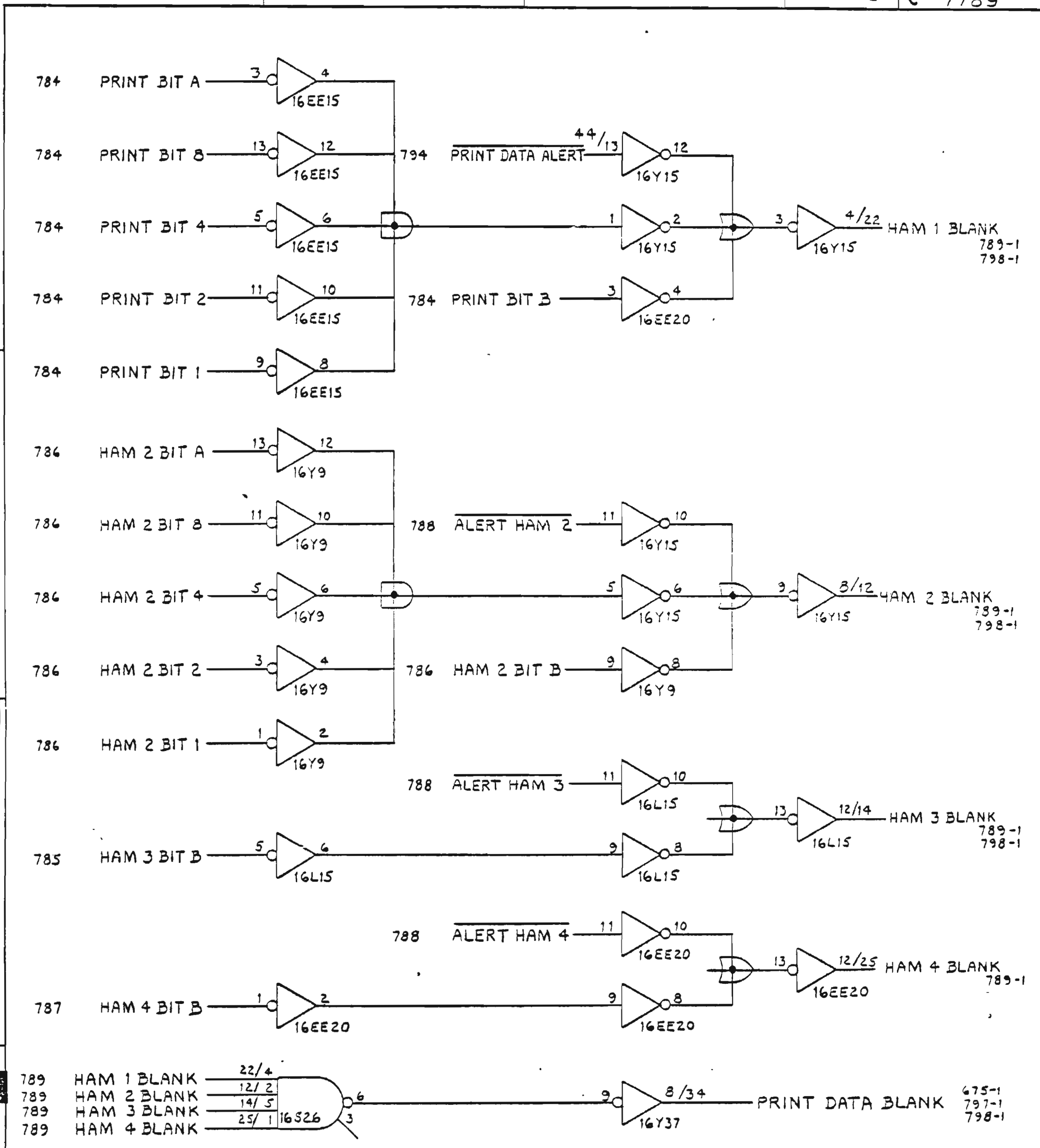
DRN J ALESI 3/8/72

HAMMER ALERT CONTROL

DWG NO. C 7788 DASH NO -00 SH 1 OF 1

C 7789



DR80-B-225

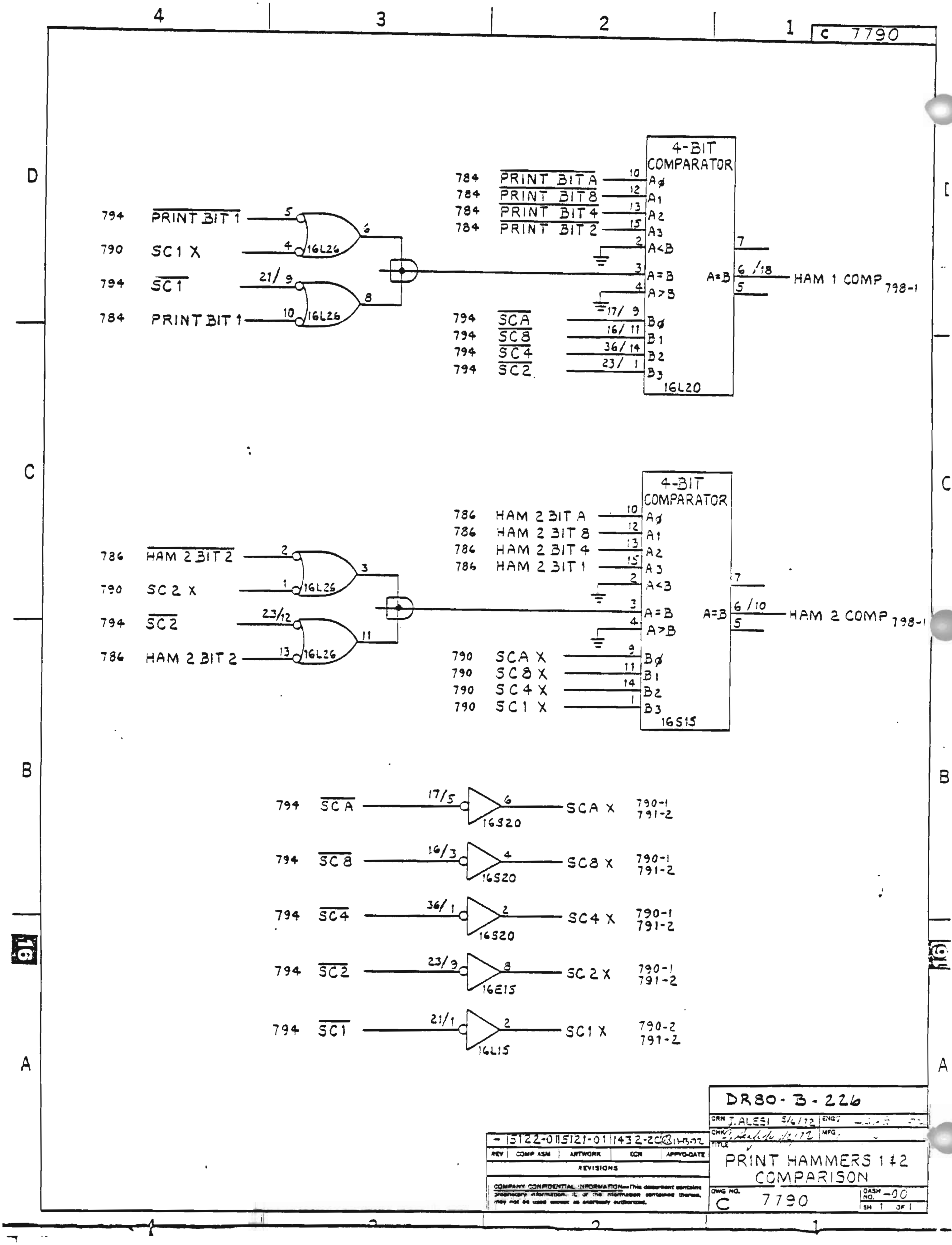
–	5122-01	5121-01	1432-2CE	11-3-72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

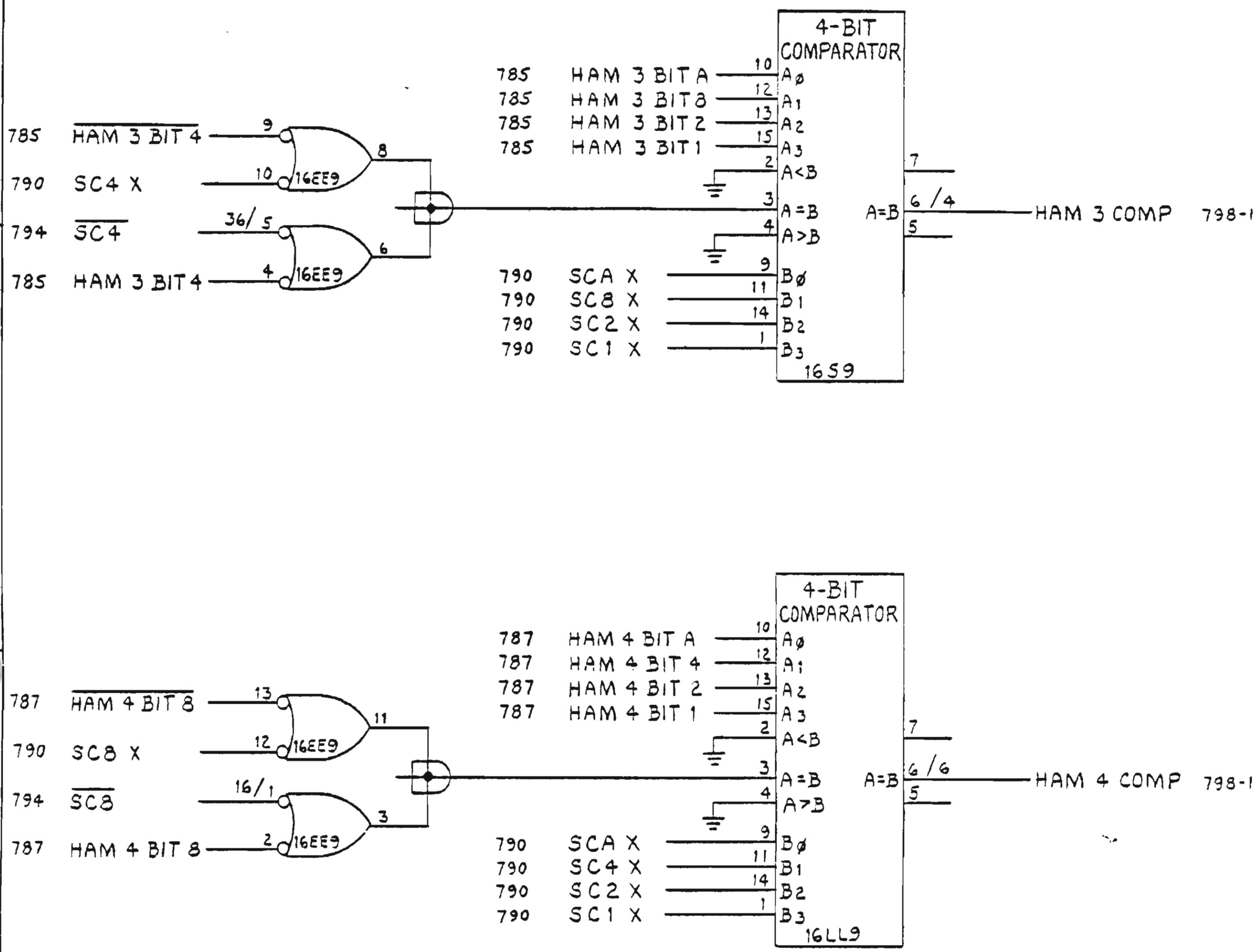
REVISIONS

DRN J.ALESI 5/9/72
CHK 6/2/72
ENG 11-3-72
MFG

TITLE PRINT BLANK CONTROL

DWG NO. C 7789
DASH NO -00
SH 1 OF 1


C 7790
4-BIT COMPARATOR
784 PRINT BIT A
784 PRINT BIT 8
784 PRINT BIT 4
784 PRINT BIT 2
794 PRINT BIT 1
790 SC1 X
794 SC1
784 PRINT BIT 1
16L26
A=B
HAM 1 COMP 798-1
794 SCA
794 SC8
794 SC4
794 SC2
16L20
4-BIT COMPARATOR
786 HAM 2 BIT A
786 HAM 2 BIT 8
786 HAM 2 BIT 4
786 HAM 2 BIT 1
786 HAM 2 BIT 2
790 SC 2 X
794 SC2
786 HAM 2 BIT 2
HAM 2 COMP 798-1
790 SCA X
790 SC8 X
790 SC4 X
790 SC1 X
16S15
794 SCA 17/5 16S20 SCA X 790-1 791-2
794 SC8 16/3 16S20 SC8 X 790-1 791-2
794 SC4 36/1 16S20 SC4 X 790-1 791-2
794 SC2 23/9 16E15 SC 2 X 790-1 791-2
794 SC1 21/1 16L15 SC1 X 790-2 791-2
DR80-B-226
DRN J. ALESI 5/6/72
CHK
ENG
MFG.
TITLE PRINT HAMMERS 1 & 2 COMPARISON
- 5122-01 5121-01 1432-2C 11-13-72
REV COMP ASM ARTWORK ECN APPVD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION—This document contains proprietary information. It, or the information contained therein, may not be used except as expressly authorized.
DWG NO. C 7790
DASH NO. -00
SH 1 OF 1



DR80-B-227

DRN J. ALESI 5/6/72
CHK'D 6/2/72

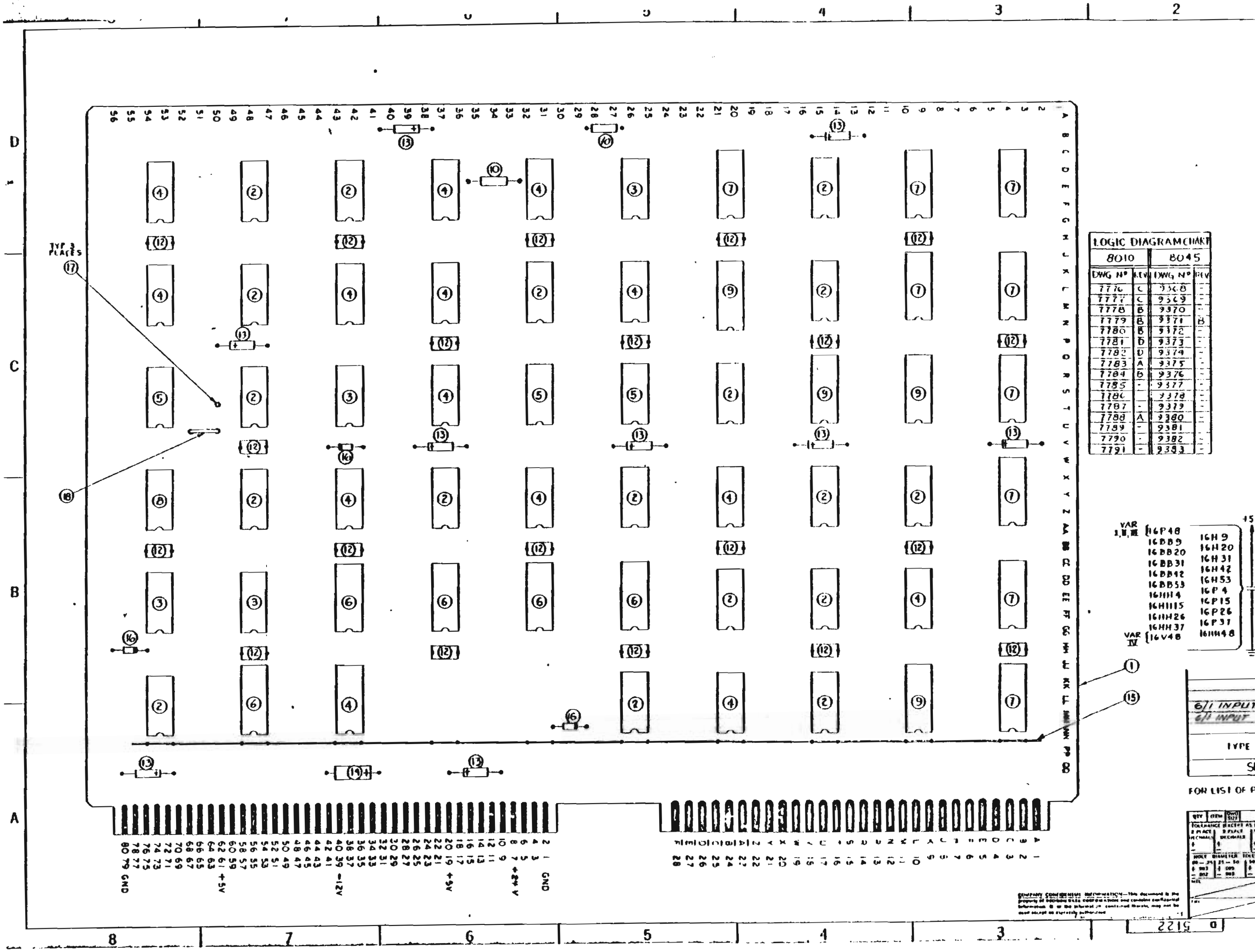
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
–	5122-01	5121-01	1432-2C	

REVISIONS

TITLE PRINT HAMMERS 3 & 4 COMPARISON

DWG NO. C 7791 — CASH NO -00 — SH 1 OF 1

REV	DESCRIPTION	APPD
K	ADDED THIS SHEET PER ECN 3555 2C	

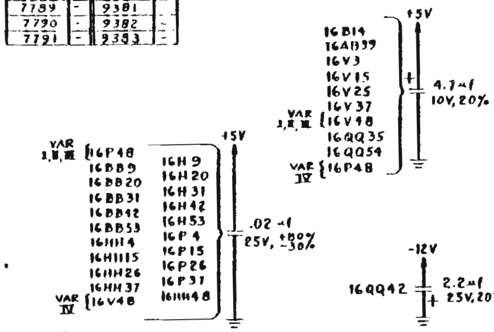


LOGIC DIAGRAM CHART			
8010		8045	
DWG Nº	REV	DWG Nº	REV
7776	C	9368	-
7777	C	9369	-
7778	B	9370	-
7779	B	9371	B
7780	B	9372	-
7781	D	9373	-
7782	D	9374	-
7783	A	9375	-
7784	B	9376	-
7785	-	9377	-
7786		9378	-
7787	-	9379	-
7788	A	9380	-
7789	-	9381	
7790	-	9382	-
7791	-	9383	-

*UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 5122-06 ON ARTWORK 5121-06

*VARIATION REFERENCE

VAR	ASSEMBLY	ARTWORK
I	5122-01,02,03,04,05	5121-01
II	5122-02,03,04,05	5121-02
III	5122-05,06	5121-05
IV	5122-06	5121-06



TYPE	LOC	OUTPUT PIN NO
6/1 INPUT	22E26	8,10,12
6/1 INPUT	22E15	4

SPARE GATES

FOR LIST OF PARTS SEE DETACHED PARTS LIST

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DR80-B-228

COMPONENT ASSEMBLY PRINTER MEMORY

D 5122

2215 0

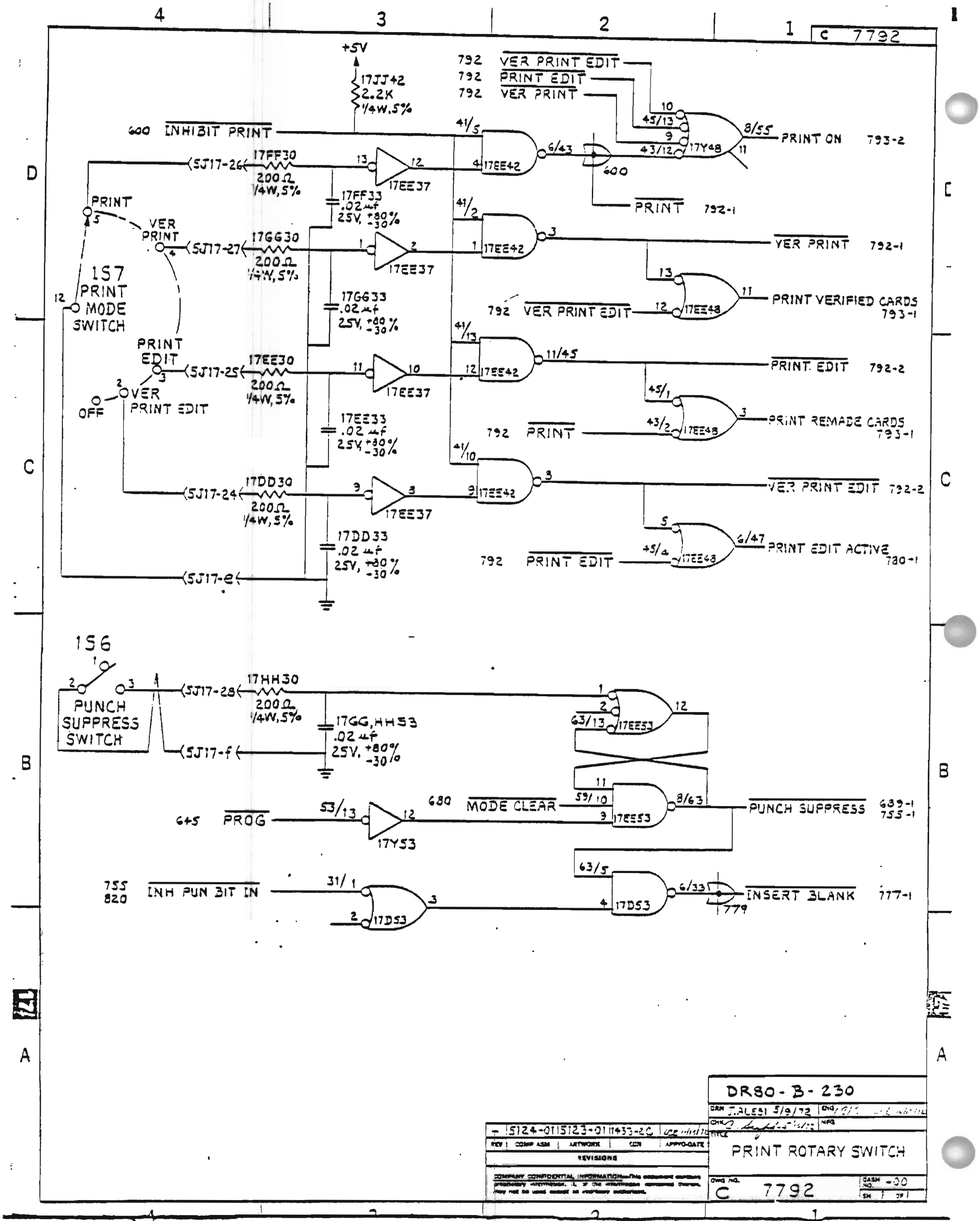
LOGIC/SCHEMATIC CONTENTS

TYPE NUMBER/NAME		PCB LOC
BASIC 8010	INTERFACE	17

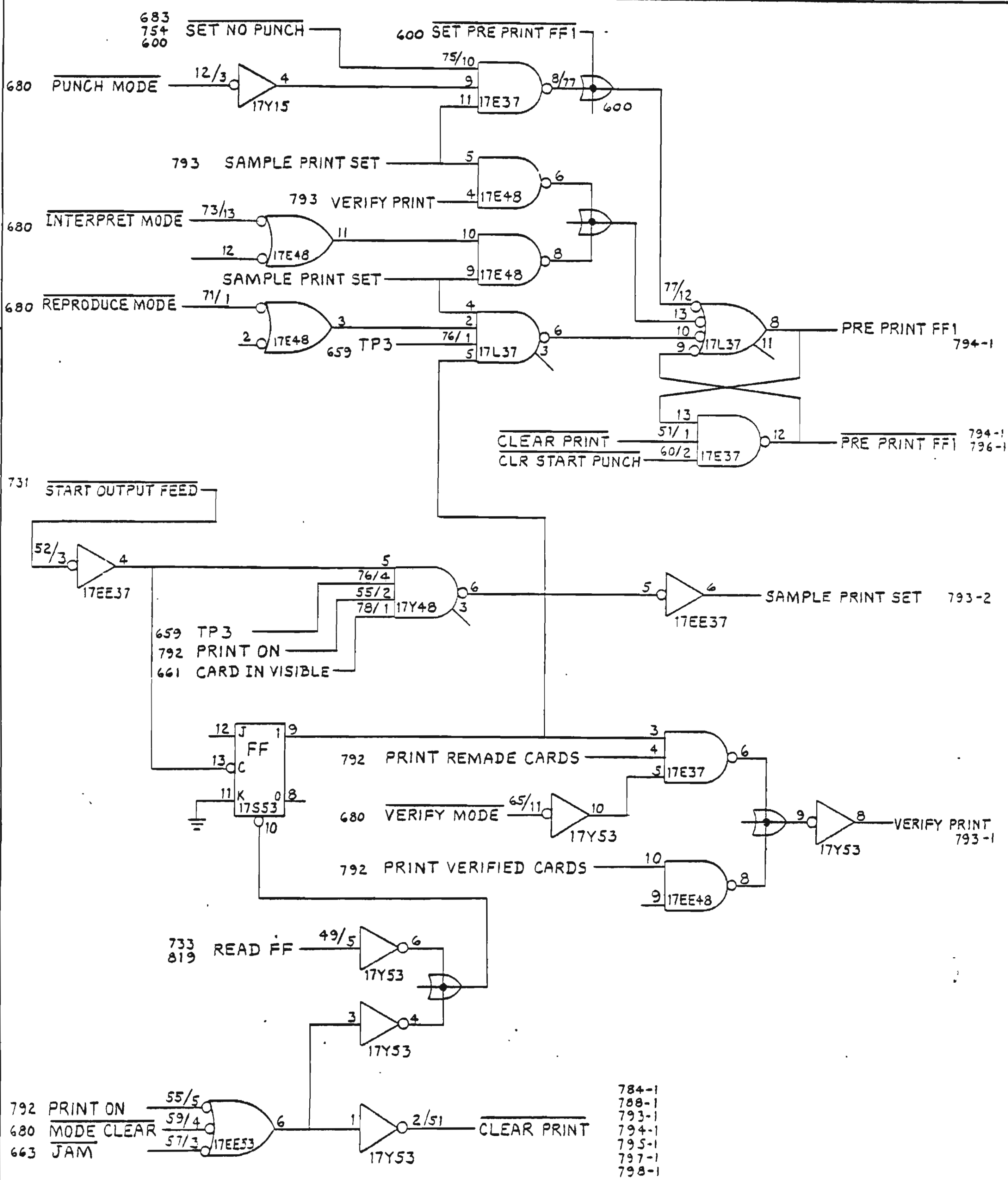
DWG NO.	TITLE	PCB/ASM DRAWING DASH NO.
5123	PWB	01
5124	PRINTER DRIVERS	01

LOGIC SCHEMATIC	LOGIC DRAWING TITLE SCHEMATIC SHEET NO.	LOGIC/SCHEMATIC DRAWING REVISION
7792	PRINT ROTARY SWITCH	-
7793	PRINT MODE CONTROL	-
7794	PRINT CODE REGISTER	-
7795	PRINT MEMORY LOAD	-
7796	PRINT SPROCKET	B
7797	PRINT READOUT CONTROL	-
7798	HAMMER FOUND REGISTERS	-
7799	PRINT FIRING REGISTER	-
7801	PRINT DRIVERS	-
5124	SHEET 2	B
REVISION OF CONTENTS		B
DATE OF ECN		30 OCT 73
APPROVAL		
ENGINEERING CHANGE NOTICE		2697-1C

DR80 - B - 229	DWG NO. 5124	REV	SHT OF


C 7792
+5V
17JJ42 2.2K 1/4W,5%
792 VER PRINT EDIT
792 PRINT EDIT
792 VER PRINT
600 INHIBIT PRINT
17EE42
17Y48
PRINT ON 793-2
PRINT 792-1
5J17-26
17FF30 200Ω 1/4W,5%
17EE37
17FF33 .02 µf 25V, +80% -30%
PRINT
VER PRINT
1S7 PRINT MODE SWITCH
5J17-27
17GG30 200Ω 1/4W,5%
VER PRINT 792-1
17GG33 .02 µf 25V, +80% -30%
17EE48
PRINT VERIFIED CARDS 793-1
792 VER PRINT EDIT
PRINT EDIT
5J17-25
17EE30 200Ω 1/4W,5%
OFF
VER PRINT EDIT
PRINT EDIT 792-2
17EE33 .02 µf 25V, +80% -30%
PRINT REMADE CARDS 793-1
792 PRINT
5J17-24
17DD30 200Ω 1/4W,5%
VER PRINT EDIT 792-2
17DD33 .02 µf 25V, +80% -30%
PRINT EDIT ACTIVE 780-1
792 PRINT EDIT
5J17-e
1S6
PUNCH SUPPRESS SWITCH
5J17-28
17HH30 200Ω 1/4W,5%
17GG,HH53 .02 µf 25V, +80% -30%
5J17-f
17EE53
680 MODE CLEAR
PUNCH SUPPRESS 689-1 755-1
645 PROG
17Y53
755 820 INH PUN BIT IN
17D53
INSERT BLANK 777-1
DR80-B-230
DRN J. ALESI 5/9/72
5124-0115123-0111433-2C
REV COMP ASM ARTWORK ECN APPVD-DATE
REVISIONS
TITLE PRINT ROTARY SWITCH
DWG NO. C 7792
SH 1 OF 1

C 7793



DR80-B-231

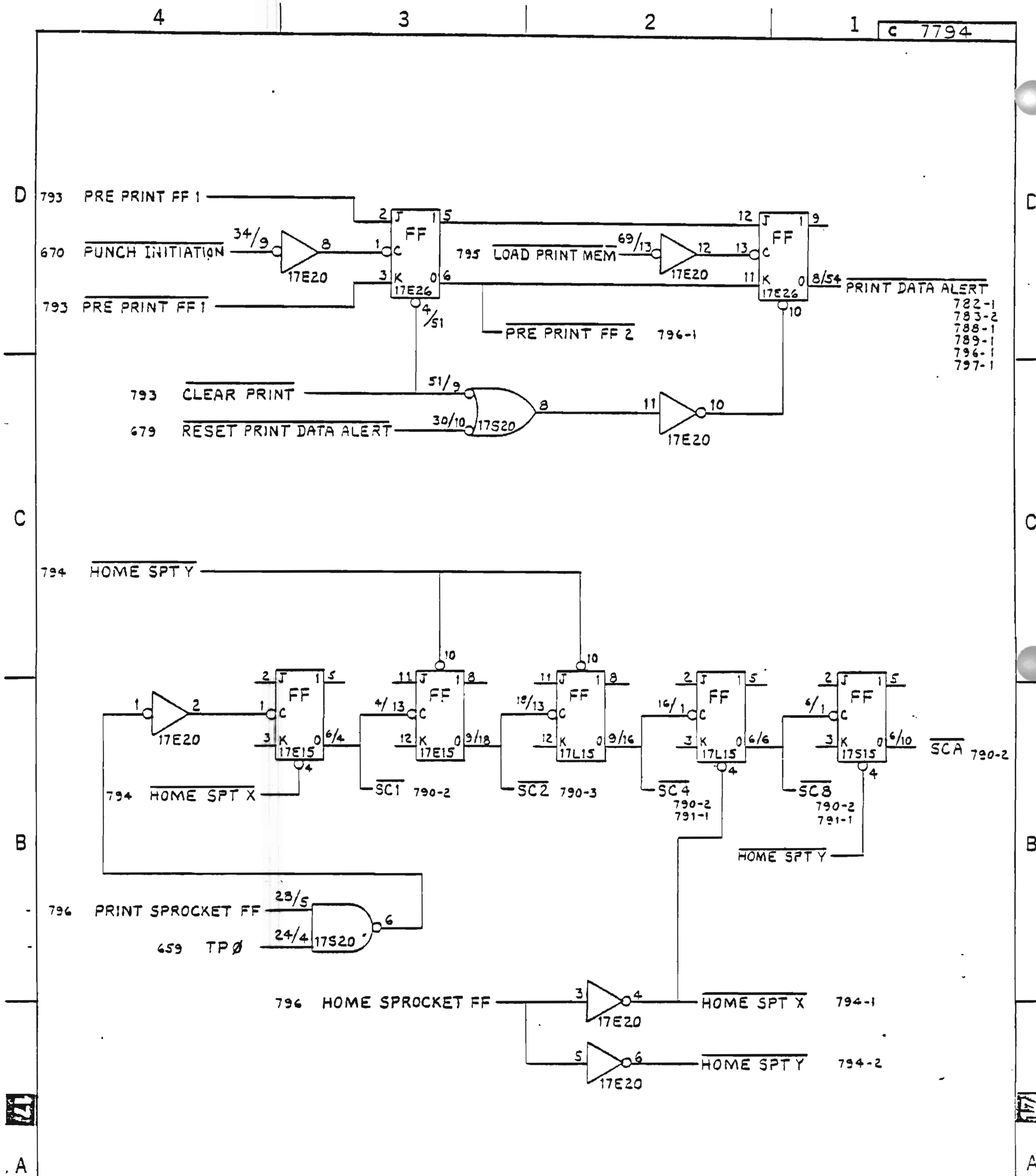
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	5124-01	5123-01	1433-2C	4/13/72

REVISIONS

DRN J. ALESI 5/10/72

TITLE PRINT MODE CONTROL

DWG NO. C 7793 DASH NO. -00 SH 1 OF 1



DR80-B-232

DRN J. ALESI 5/11/72

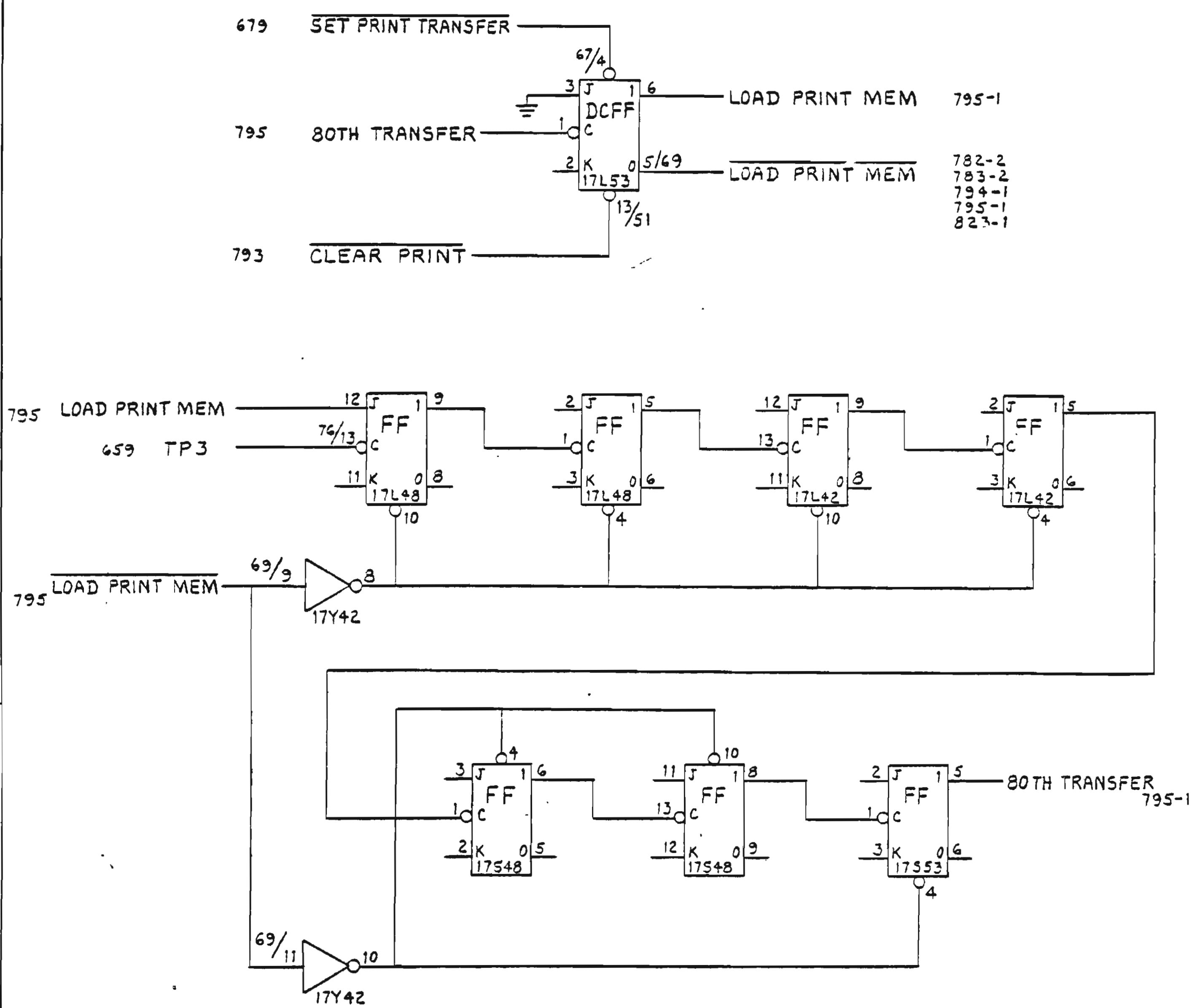
TITLE PRINT CODE REGISTER

DWG NO. C 7794 DASH NO. -00 SH 1 OF 1

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	5124-01	5123-01	1433-2C	

REVISIONS

C 7795



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	5124-01	5123-01	1433-2C	11/13/72 CK

REVISIONS

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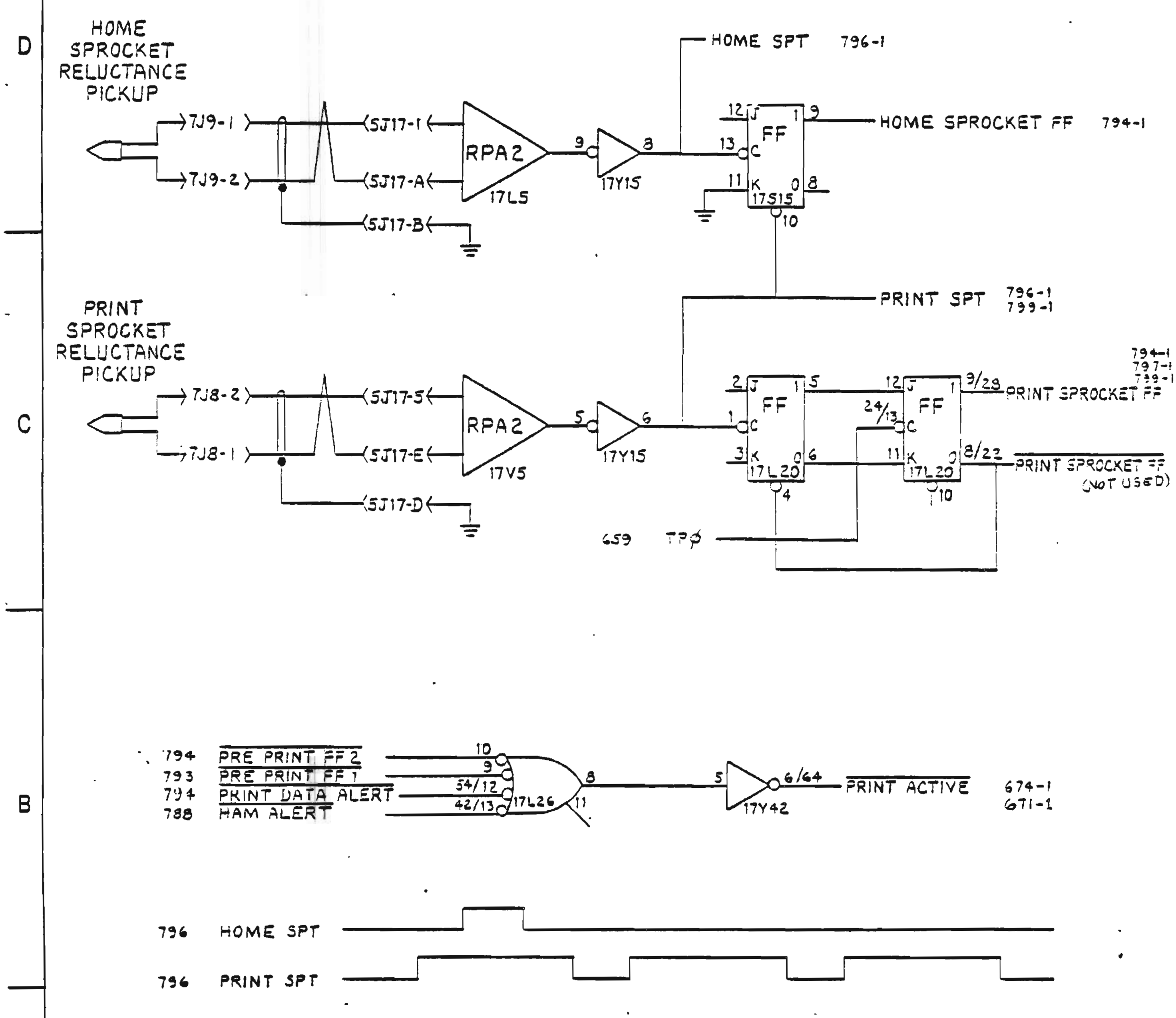
DR80 - B - 233

DRN J. ALESI 5/11/72
CHK 5/16/72
ENG
MFG

TITLE PRINT MEMORY LOAD

DWG NO. C 7795
DASH NO. -00
SH 1 OF 1

C 7796



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
B	5124-01	5123-01	2216-2C	
A	5124-01	5123-01	2060-1C	5-1-73
—	5124-01	5123-01	1433-2C	11/13/72

REVISIONS

DR80-B-234

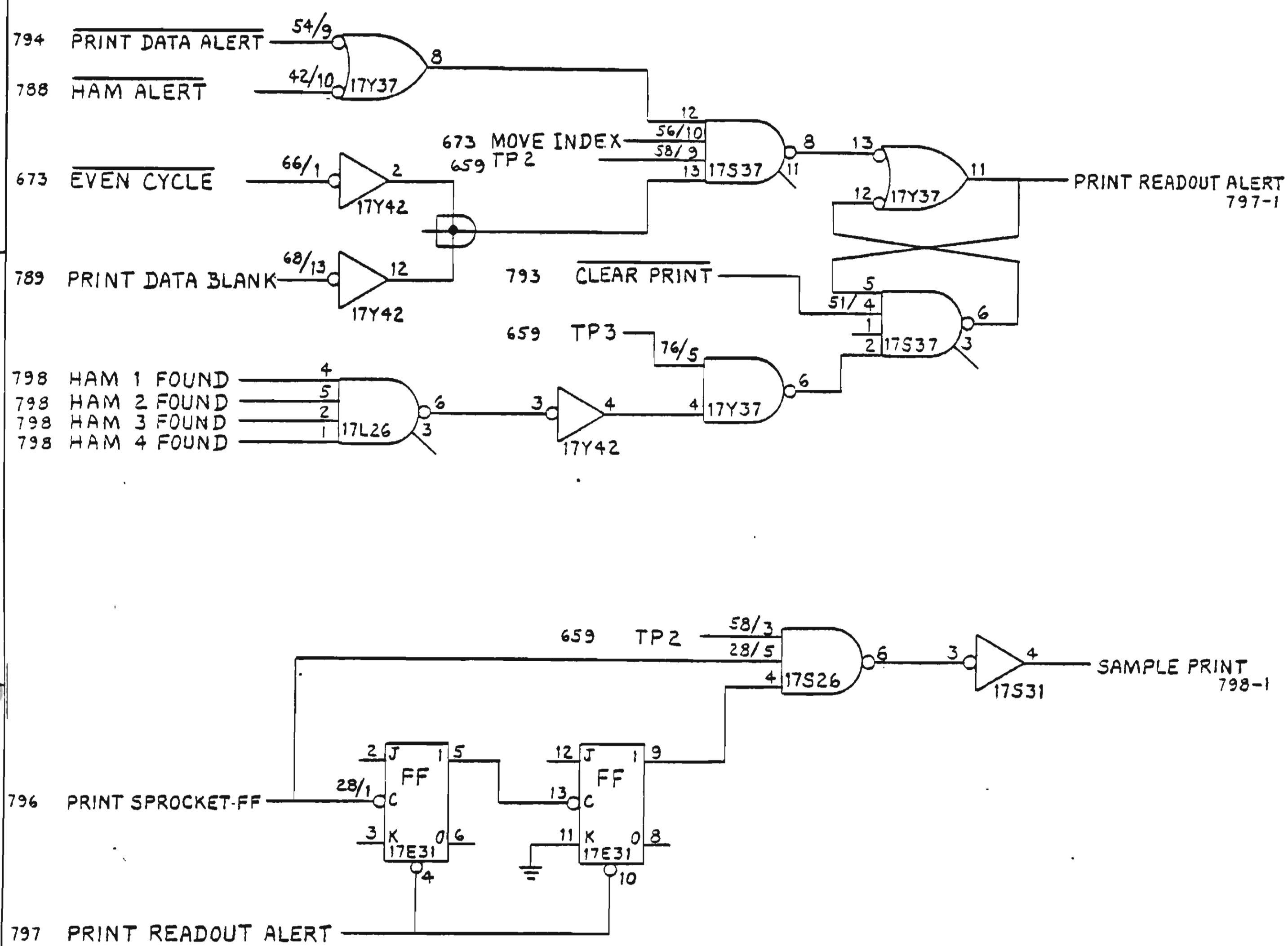
DRN J. ALESI 5/11/72

CHK 5/16/72

TITLE PRINT SPROCKET

DWG NO. C 7796 DASH NO. -00 SH 1 OF 1

C 7797



DR80-B-235

-	5124-01	5123-01	1433-2C	11/13/72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

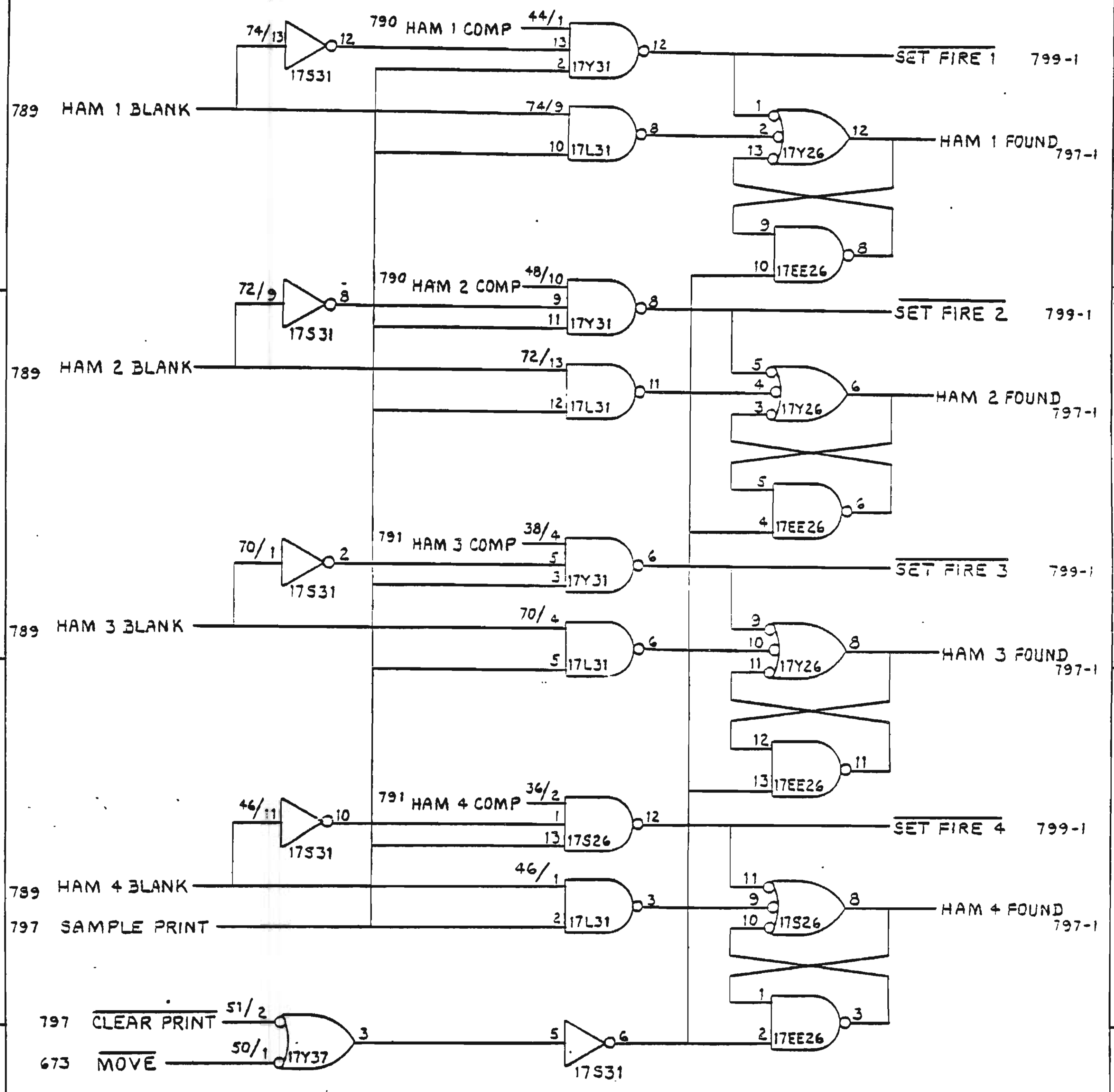
REVISIONS

DRN J. ALESI 5/11/72
CHK 5/16/72
ENG
MFG

TITLE PRINT READOUT CONTROL

DWG NO. C 7797 DASH NO -00 SH 1 OF 1

C 7798



DR80-B-236

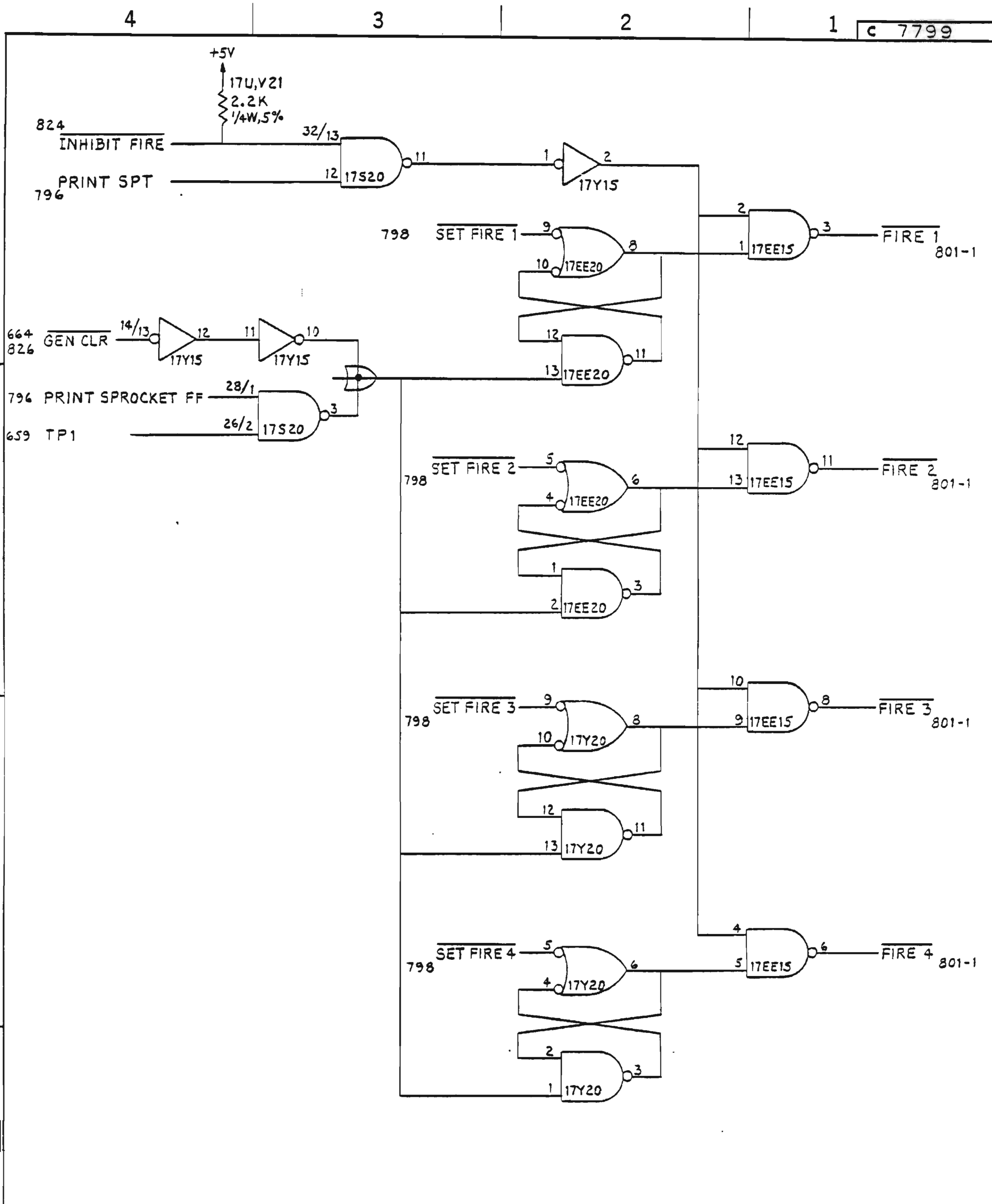
DRN J. ALESI 5/11/72

TITLE HAMMER FOUND REGISTERS

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	5124-01	5123-01	1433-2C	11/13/72

REVISIONS

DWG NO. C 7798 DASH NO. -00 SH 1 OF 1



DR80-B-237

DRN J.ALESI 5/12/72

CHK 5/16/72

TITLE PRINT FIRING REGISTER

DWG NO. C 7799

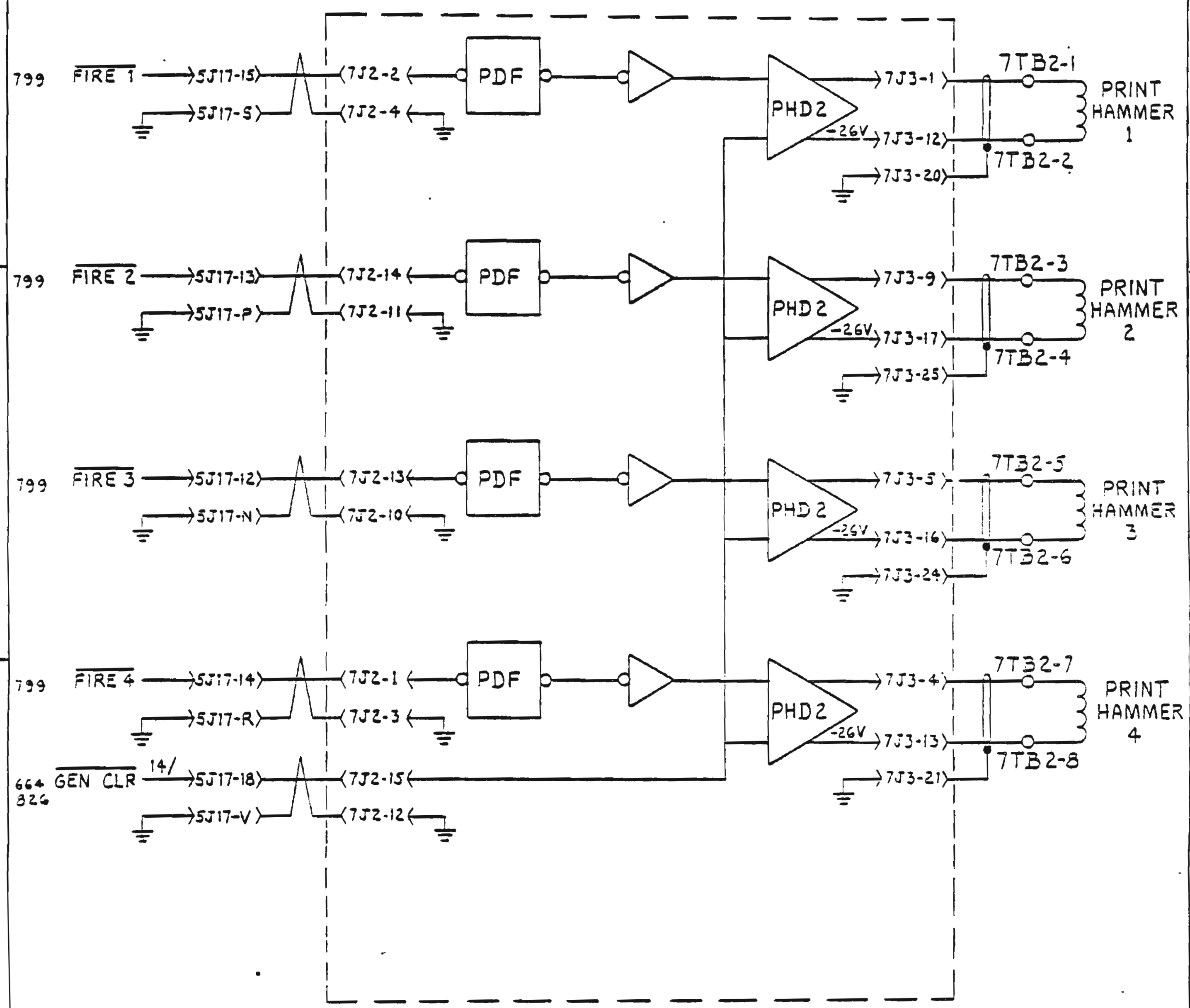
DASH NO. -00

SH 1 OF 1

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	5124-01	5123-01	1433-2C	11/13/72

REVISIONS

C 7801



DR80-B-238

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	5124-01	5123-01	1433-2	

REVISIONS

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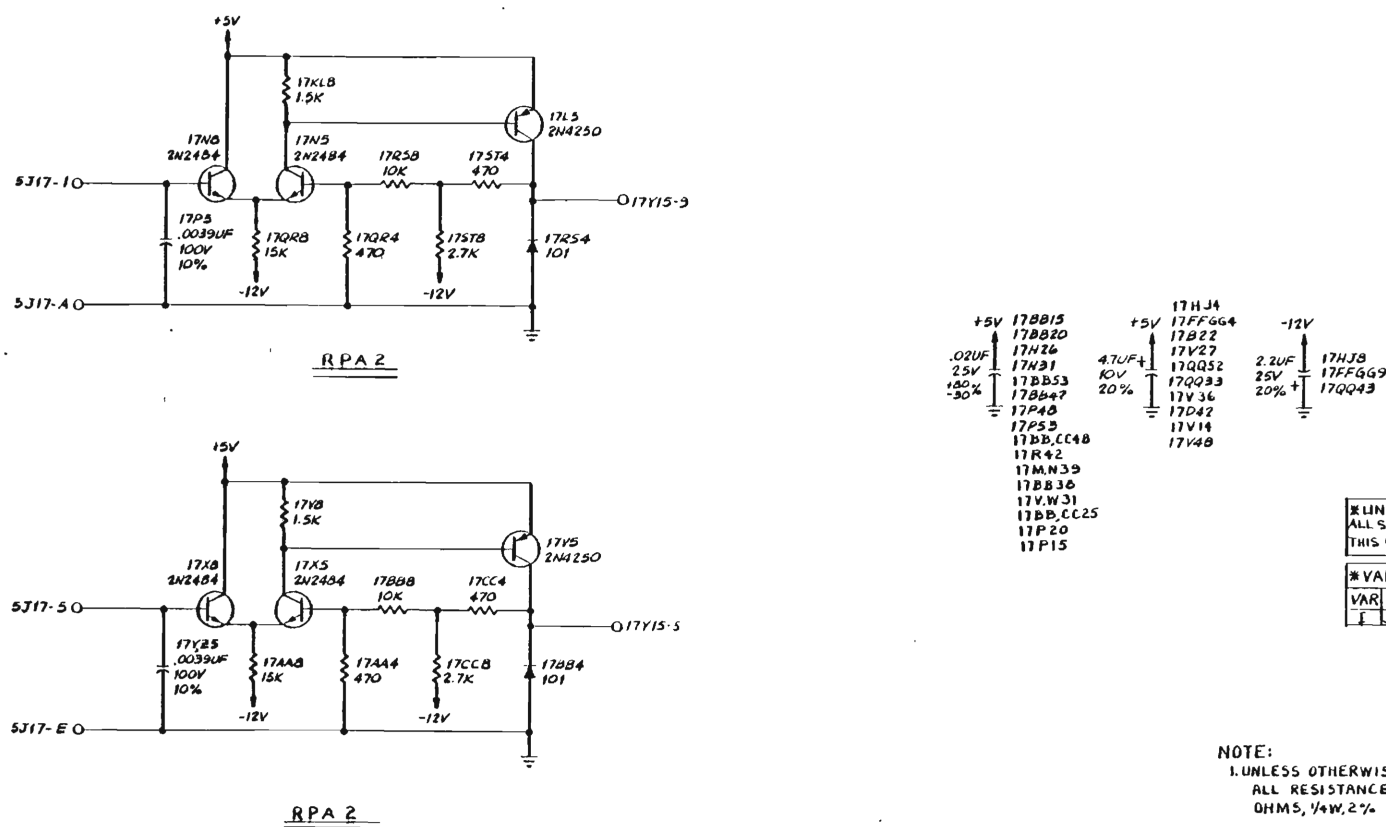
DRN J. ALESI 5/12/72
CHK 5-16-72
ENG
MFG

TITLE PRINT DRIVERS

DWG NO. C 7801 DASH NO. -00 SH 1 OF 1

REV	DESCRIPTION	APPVD DATE
A	RELEASE PER ECN 1433-2C	
B	REVISED/ECN 2697-1C	



*UNLESS OTHERWISE SPECIFIED: ALL SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS.

*VARIATION REFERENCE		
VAR	ASSEMBLY	ARTWORK
I	5124-01	5123-01

NOTE:
1. UNLESS OTHERWISE SPECIFIED: ALL RESISTANCE VALUES ARE IN OHMS, 1/4W, 2%

DR80-B-239

COMPONENT ASSEMBLY
PRINTER DRIVERS

DWG NO. D 5124 DASH 01 SH 2

D 5124

DR80-B-240

LOGIC/SCHEMATIC INDEX 6/26/78 LOCATION- MODULE -13-

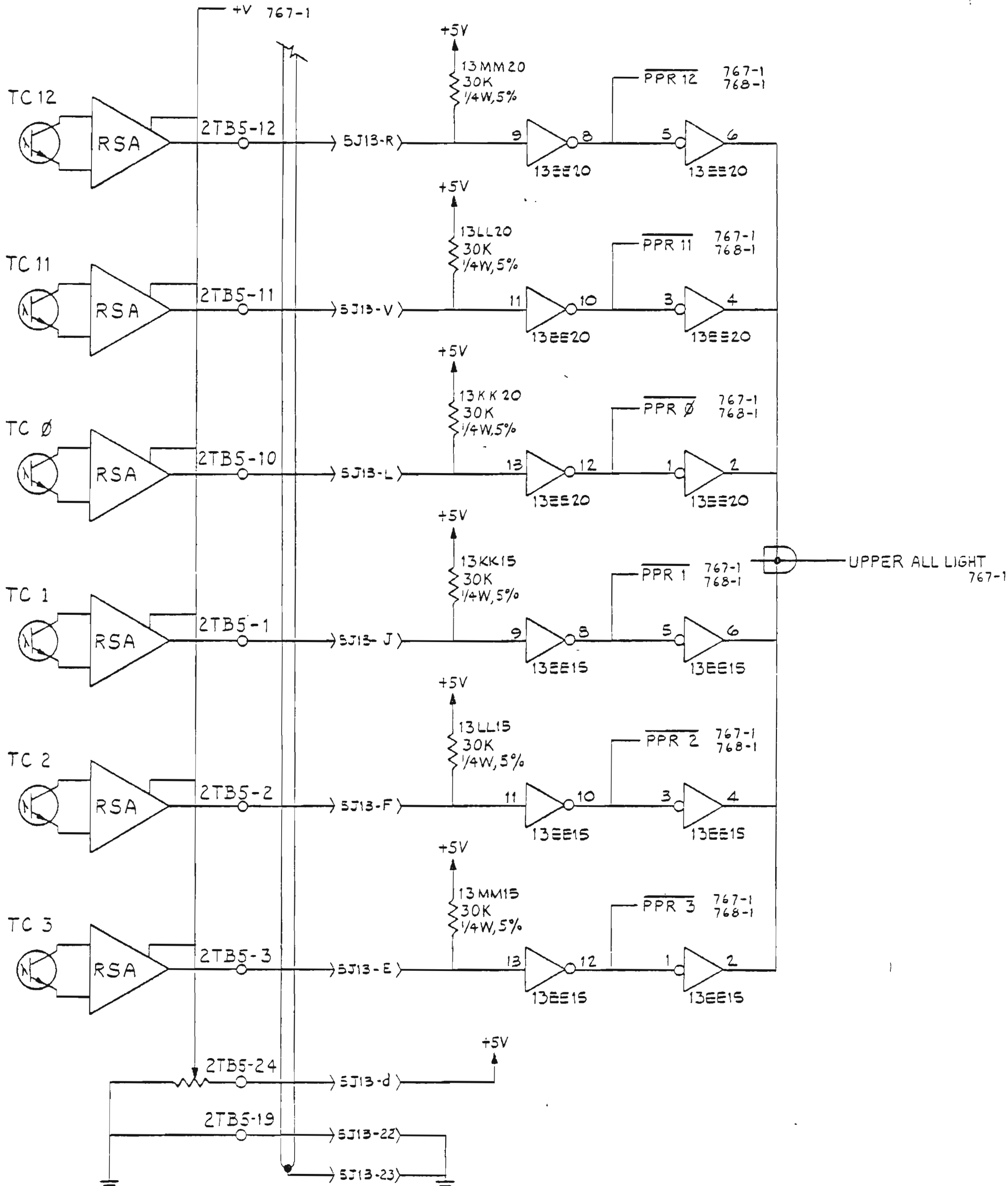
F1027 8001/10 DR 80COL MANUAL- 7545 00
F1027 8010-11 D.R. 80COL MANUAL-902193 00-360

5115-07 PWB
POST-PUNCH READ CHECK 5116-08 ASM

LOGIC-DWG	SH	REV	TITLE
7766		-A	POST-PUNCH READ DATA P-1
7767		--	POST-PUNCH READ DATA P-2
7768		--	PUNCH CHECK COMPARATOR
7769		-A	MEMORY 1 GATING PT-1
7770		-A	MEMORY 1 GATING PT-2
7771		-A	MEMORY 1
7772		-A	MEMORY 2
7773		--	CHECK TRANSFER COUNTER
7774		-G	TRANSFER/COMPARE CONTROL
7775		-E	PUNCH CHECK DETECTION

SCHEM-DWG	SH	REV
5116	04	//
5116	05	-M

C 7766



REV	COMP ASM	ARTWORK	ECN	APPVD DATE
A	5116-07	5115-07	3597-12	
-	5116-01	5115-01	429-20	

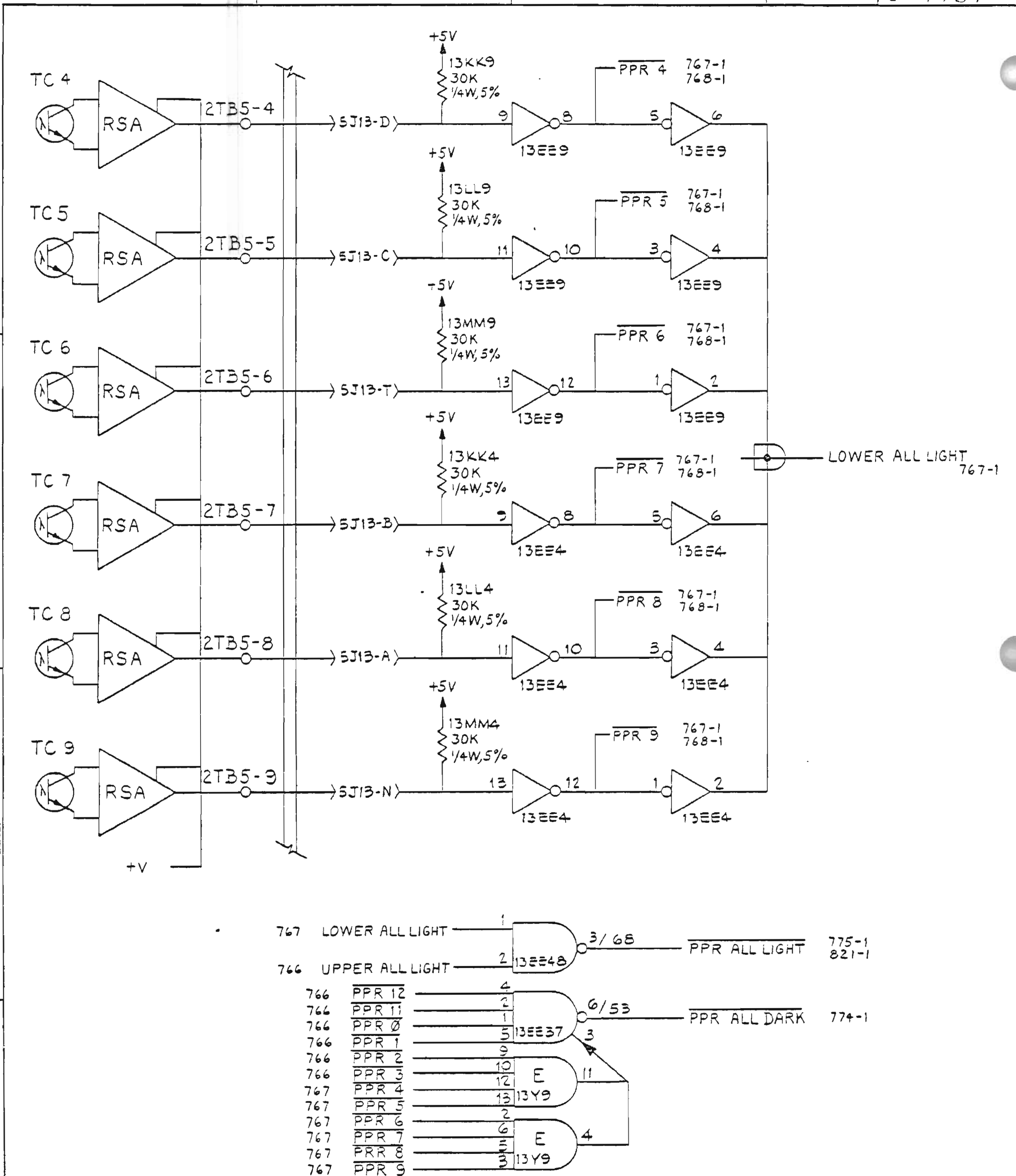
REVISIONS

DECISION DATA COMPUTER CORP.

DRN J. ALESI 4/24/72 | ENG | CHK 5-25-72 | MFG

TITLE (PUNCH CHECK) POST-PUNCH READ DATA, PART 1

DWG NO. C 7766 | DASH NO. -00 | SH 1 OF 1



DECISION DATA COMPUTER CORP.

DRN J. ALESI 4/24/72 ENG.

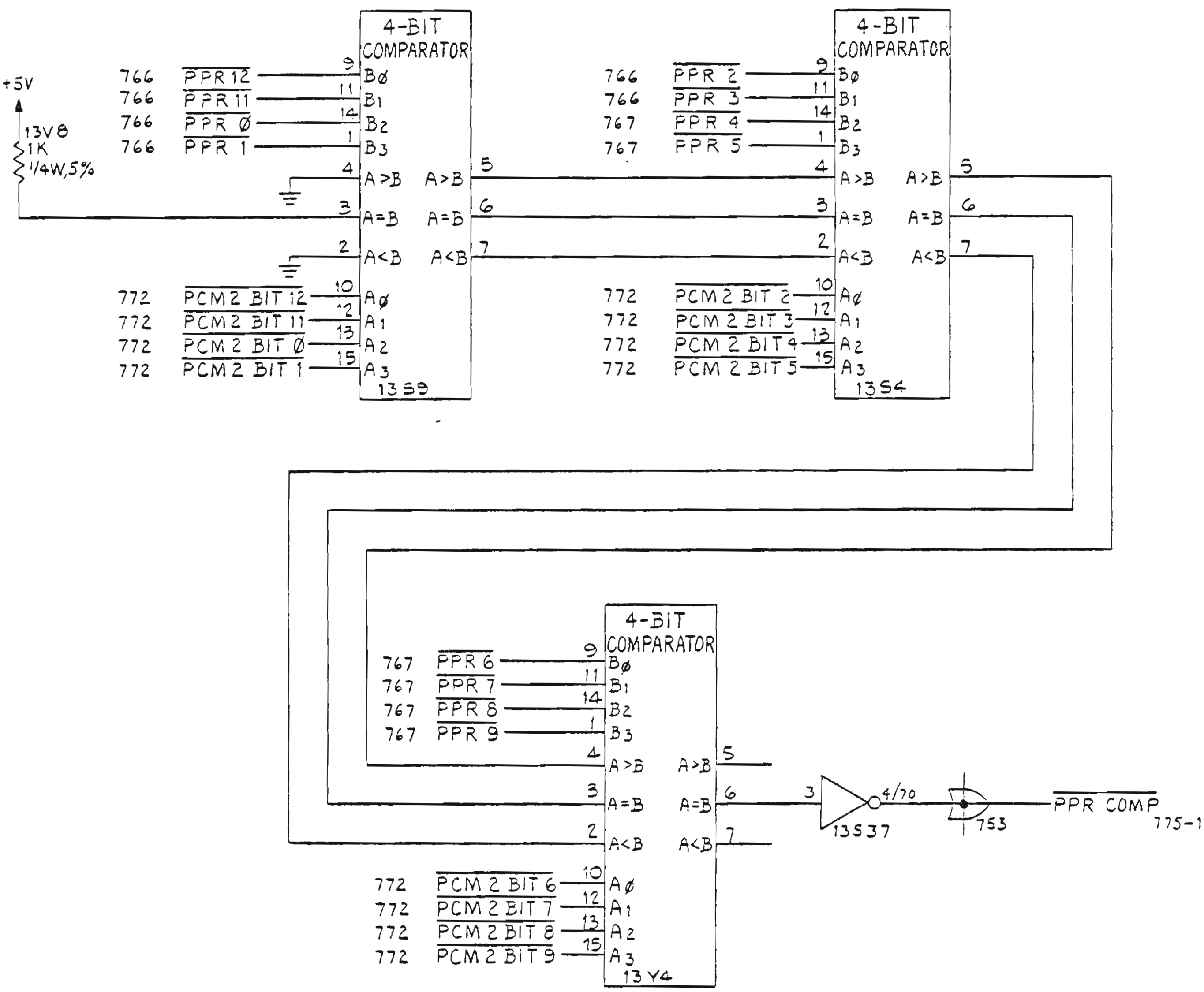
CHK 5-25-72 MFG

TITLE (PUNCH CHECK) POST-PUNCH READ DATA, PART 2

DWG NO. C 7767 DASH NO. -00 SH 1 OF 1

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	5116-01	5115-01	1429-20	

REVISIONS



-	5116-01	5115-01	1429-2C	1-14-72
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

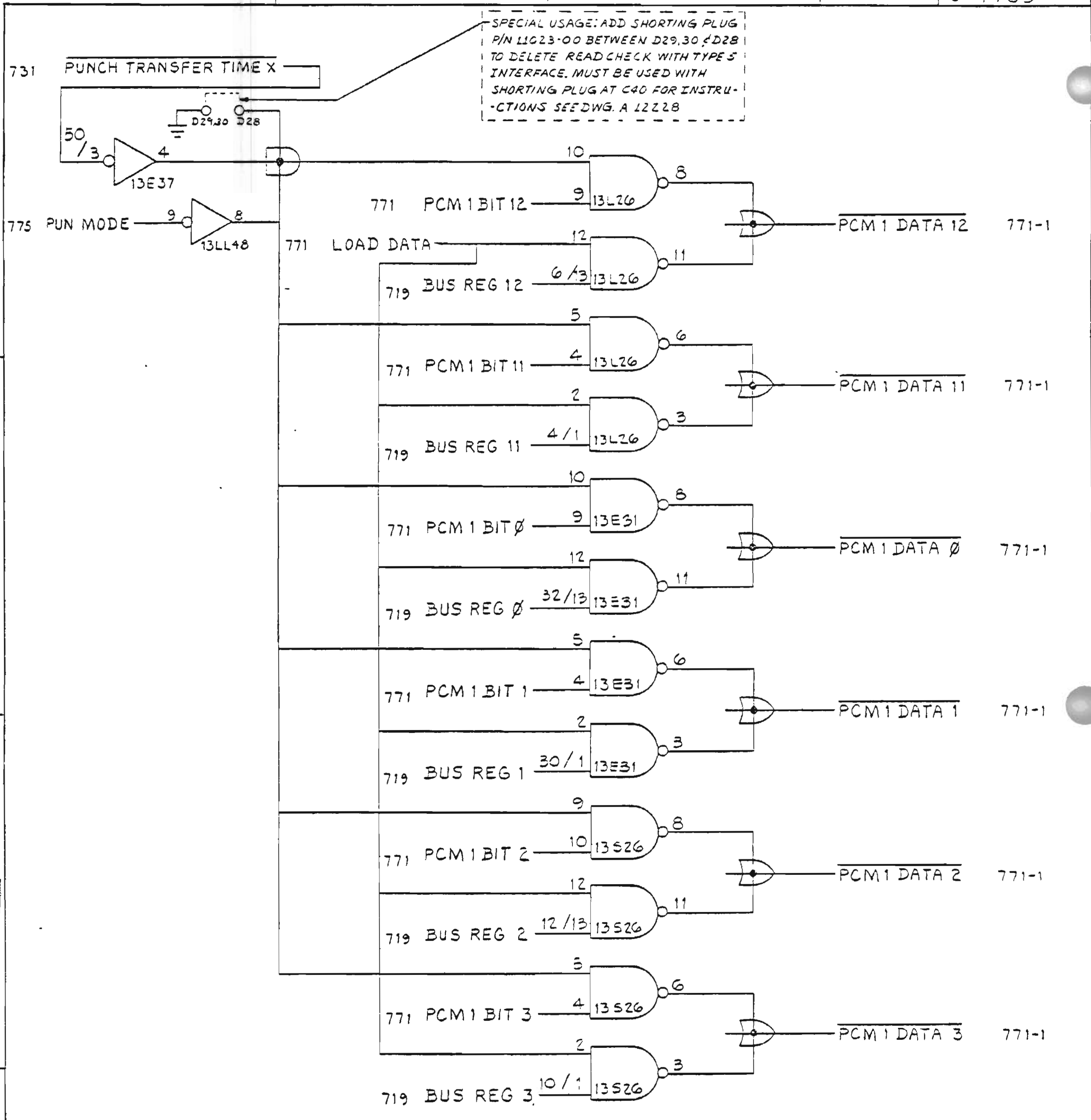
DECISION DATA COMPUTER CORP.

DRN J. ALESI 4/24/72 ENG
CHK 5-15-72 MFG

TITLE (PUNCH CHECK)
PUNCH CHECK COMPARATOR

DWG NO. C 7768 DASH NO -00 SH 1 OF 1

C 7769

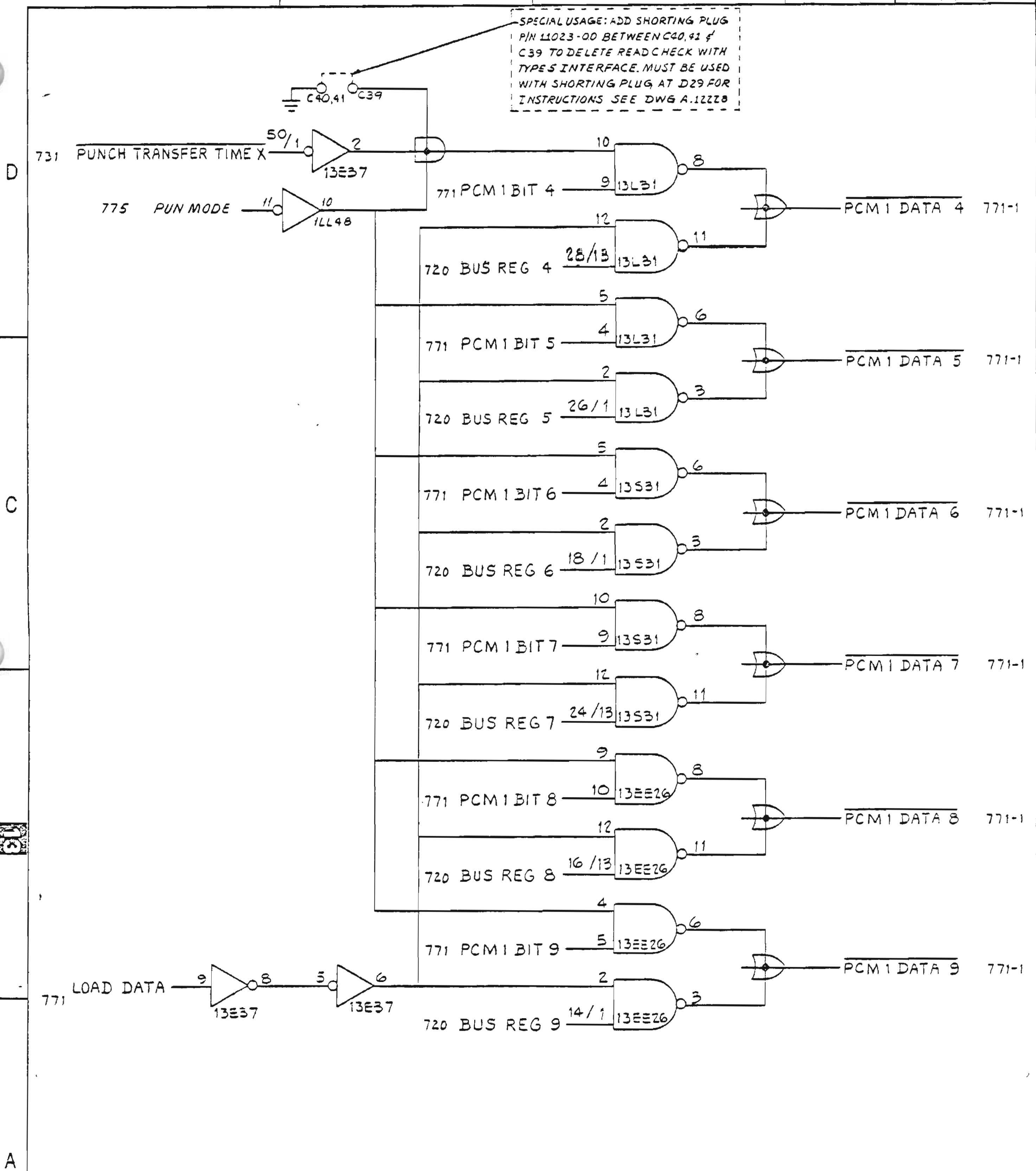


REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	5116-07	5115-07	3849-1C	[illegible]
–	5116-01	5115-01	1429-2C	[illegible]

REVISIONS

DECISION DATA COMPUTER CORP.

DRN J ALESI 4/24/72 ENG [illegible]
CHK [illegible] 5-25-72 MFG
TITLE (PUNCH CHECK) MEMORY 1 GATING, PART 1
DWG NO. C 7769 DASH NO -00
SH 1 OF 1



DECISION DATA COMPUTER CORP.

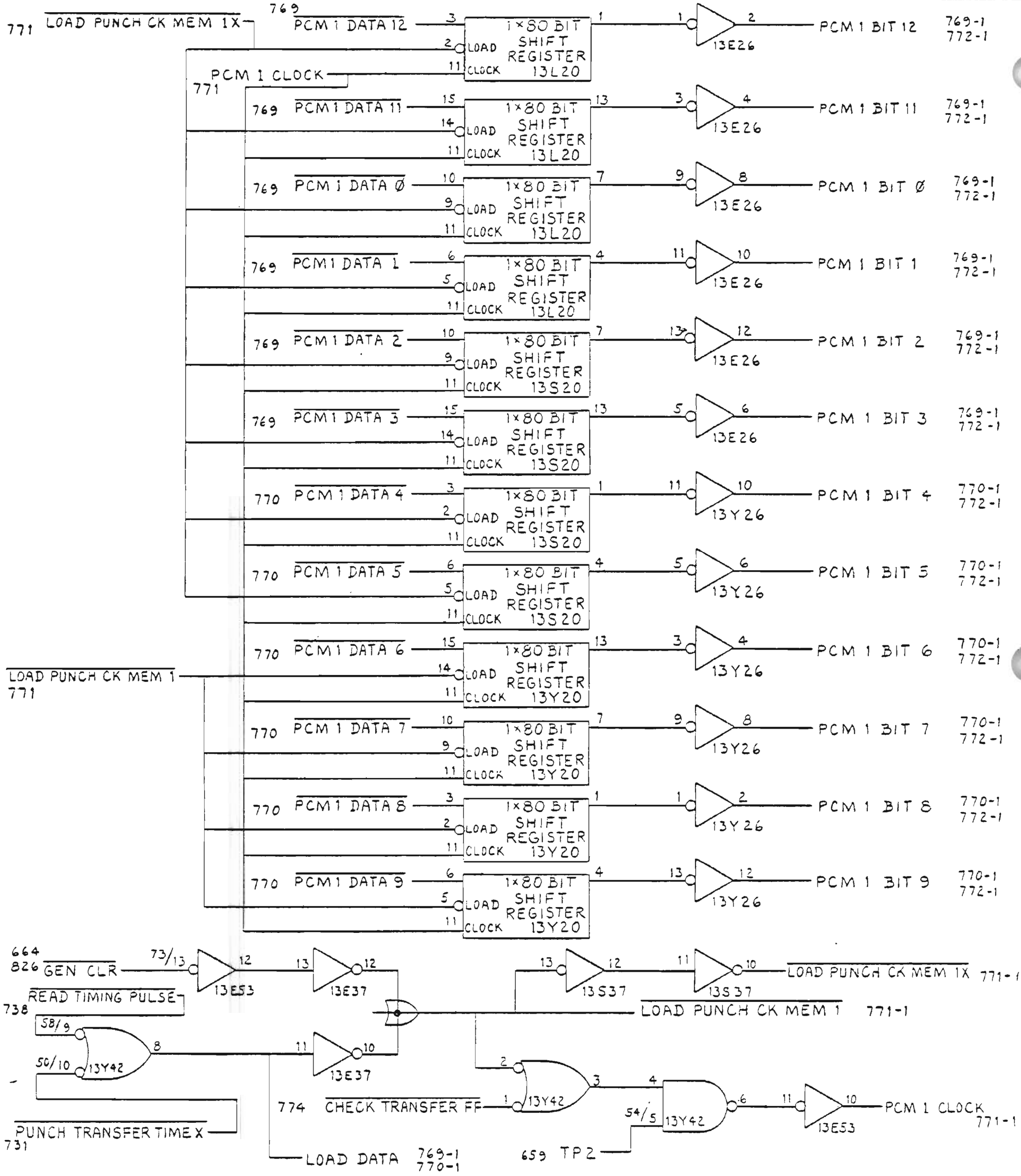
DRN J. ALESI 4/24/72 ENG
CHK
MFG

TITLE (PUNCH CHECK) MEMORY 1 GATING, PART 2

DWG NO. C 7770 DASH NO -00 SH 1 of 1

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	5116-07	5115-07	5849-1C	
–	5116-01	5115-01	1429-2C	

REVISIONS



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
A	5116-07	5115-07	3849-1C	
–	5116-01	5115-01	1429-2C	

REVISIONS

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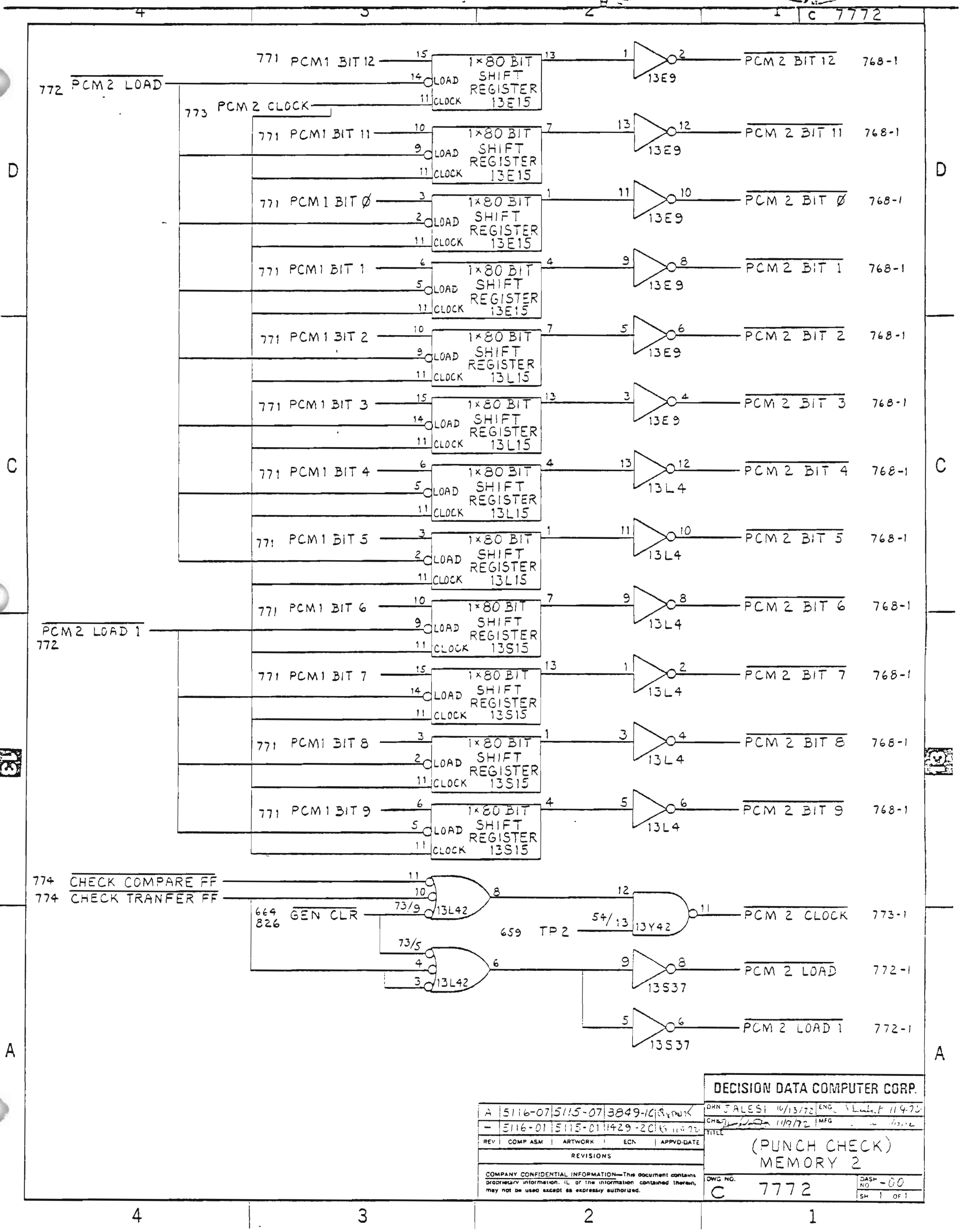
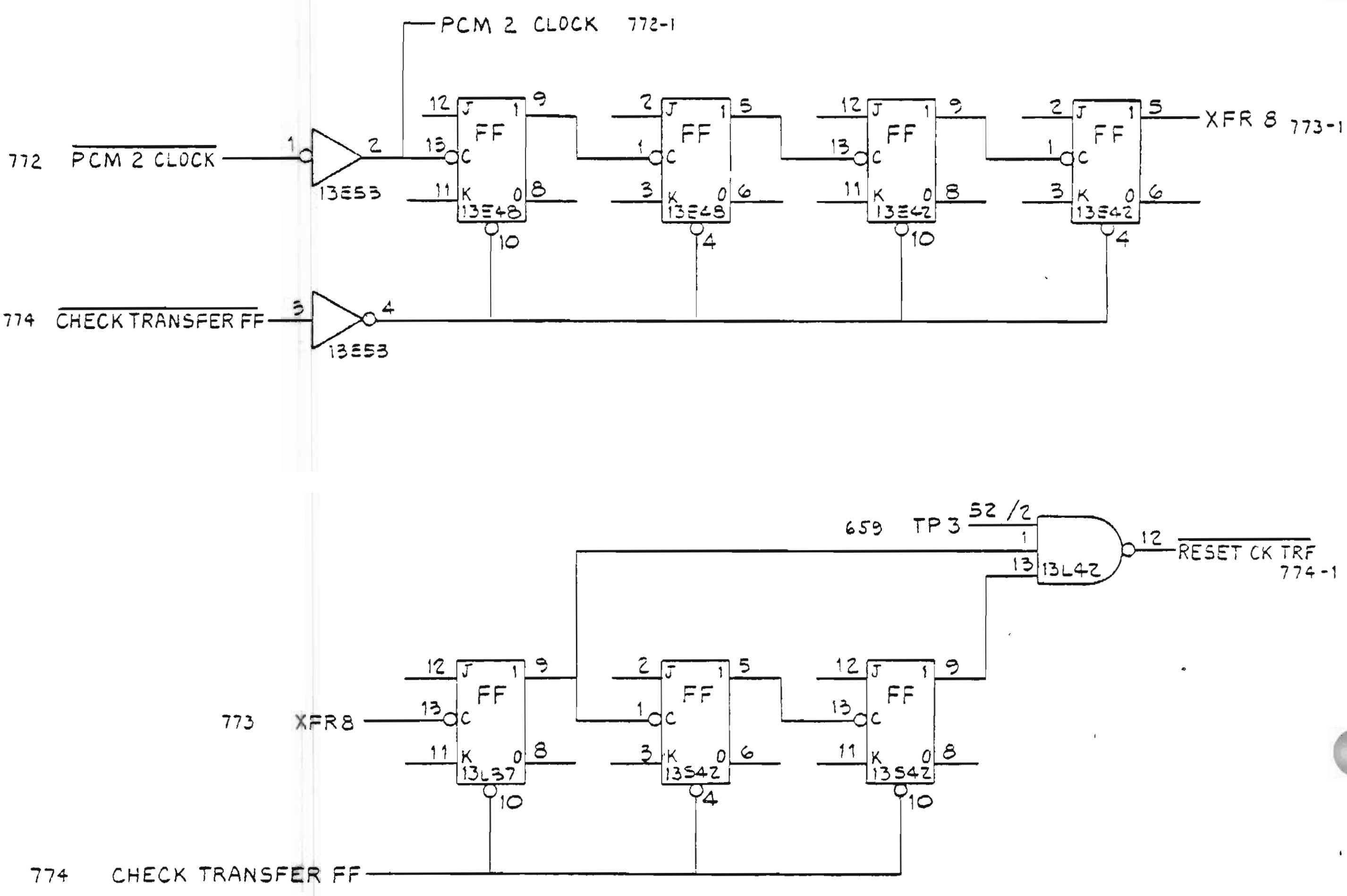
DECISION DATA COMPUTER CORP.

DRN J. ALESI 10/14/72
CHK 11/9/72
ENG
MFG

TITLE (PUNCH CHECK) MEMORY 1

DWG NO. C 7771 DASH NO. -00 SH 1 OF 1


C 7772
771 PCM1 BIT 12
772 PCM2 LOAD
773 PCM 2 CLOCK
1×80 BIT SHIFT REGISTER 13E15
LOAD
CLOCK
13E9
PCM 2 BIT 12 768-1
771 PCM1 BIT 11
PCM 2 BIT 11 768-1
771 PCM1 BIT Ø
PCM 2 BIT Ø 768-1
771 PCM1 BIT 1
PCM 2 BIT 1 768-1
771 PCM1 BIT 2
1×80 BIT SHIFT REGISTER 13L15
PCM 2 BIT 2 768-1
771 PCM1 BIT 3
PCM 2 BIT 3 768-1
771 PCM1 BIT 4
13L4
PCM 2 BIT 4 768-1
771 PCM1 BIT 5
PCM 2 BIT 5 768-1
PCM2 LOAD 1 772
771 PCM1 BIT 6
1×80 BIT SHIFT REGISTER 13S15
PCM 2 BIT 6 768-1
771 PCM1 BIT 7
PCM 2 BIT 7 768-1
771 PCM1 BIT 8
PCM 2 BIT 8 768-1
771 PCM1 BIT 9
PCM 2 BIT 9 768-1
774 CHECK COMPARE FF
774 CHECK TRANFER FF
664 826 GEN CLR
73/9
13L42
659 TP 2
54/13
13Y42
PCM 2 CLOCK 773-1
73/5
13S37
PCM 2 LOAD 772-1
PCM 2 LOAD 1 772-1
DECISION DATA COMPUTER CORP.
A | 5116-07 | 5115-07 | 3849-1C
– | 5116-01 | 5115-01 | 1429-2C
REV | COMP ASM | ARTWORK | ECN | APPVD-DATE
REVISIONS
COMPANY CONFIDENTIAL INFORMATION—This document contains proprietary information. If, or the information contained therein, may not be used except as expressly authorized.
DRN J ALESI 10/13/72
CHK 11/9/72
TITLE (PUNCH CHECK) MEMORY 2
DWG NO. C 7772
DASH NO -00
SH 1 OF 1



REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
-	5116-01	5115-01	1429-2C	[illegible]

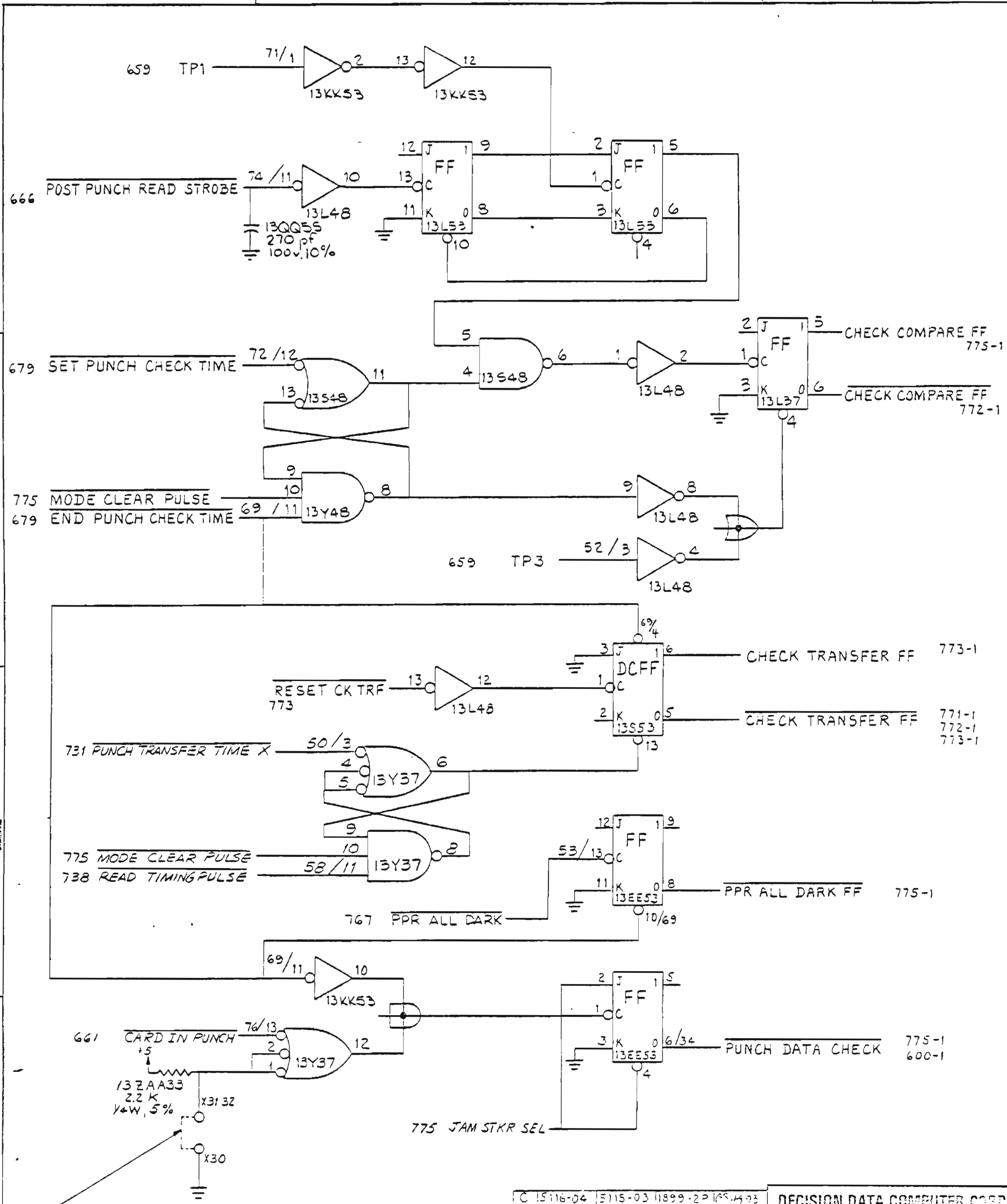
REVISIONS

DECISION DATA COMPUTER CORP.

DRN J. ALESI 4/25/72 ENG [illegible]
CHKD [illegible] MFG [illegible]

TITLE (PUNCH CHECK) CHECK TRANSFER COUNTER

DWG NO. C 7773 DASH NO. -00 SH 1 OF 1



SPECIAL USAGE: ADD SHORTING PLUG P/N 11023-00 BETWEEN X31,32 & X30 TO ENABLE IN-CYCLE REPORTING WITH TYPE 5 INTERFACE FOR INSTRUCTIONS SEE DWG. A 12228

G	5116-07	5115-07	13849-1C	[illegible]
F	5116-06	5115-06	2335-2C	[illegible]
E	5116-06	5115-05	2335-1P	[illegible]
D	5116-05	5115-05	1899-4C	[illegible]

C	5116-04	5115-03	1899-2P	[illegible]
B	5116-03	5115-02	1899-1P	[illegible]
A	5116-02	5115-02	1690 2C	[illegible]
—	5116-01	5115-01	1429-2C	[illegible]
REV	COMP ASM	ARTWORK	ECN	APPVD-DATE

REVISIONS

DECISION DATA COMPUTER CORP

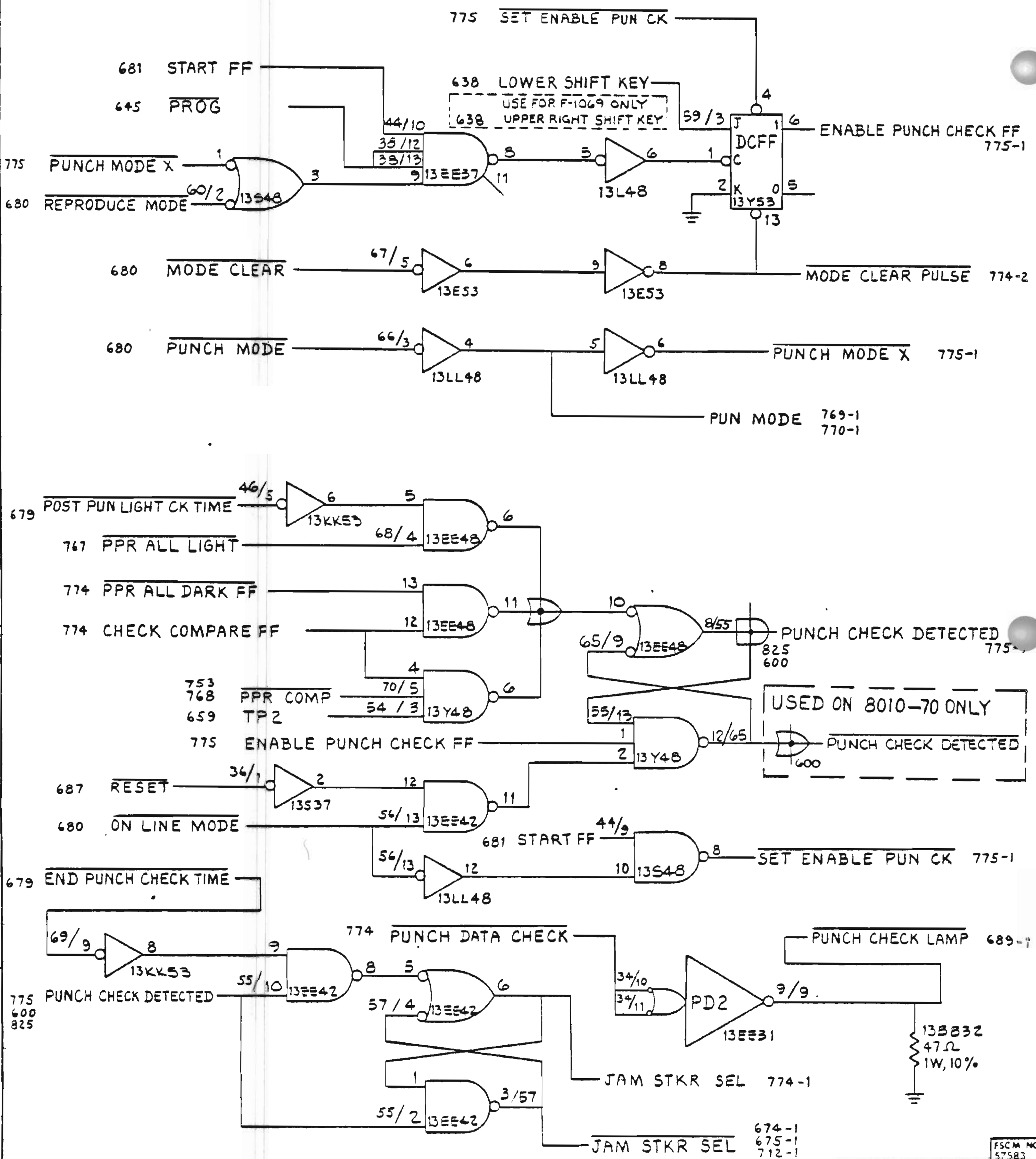
DRN J. ALESI 4/25/72

TITLE (PUNCH CHECK) TRANSFER & COMPARE CONTROL

DWG NO. C 7774 DASH NO. -00

SH 1 OF 1

C 7775



FSCM NO. 57583

REV	COMP ASM	ARTWORK	ECN	APPVD-DATE
E	5116-05	5115-08	5986-1C	[illegible]
D	5116-07	5115-07	3849-1C	[illegible]
C	5116-05	5115-05	3330-1C	[illegible]
B	5116-03	5115-05	1399-4C	[illegible]
A	5116-02	5115-02	1690-2C	[illegible]
-	5116-01	5115-01	1429-2C	[illegible]

REVISIONS

DECISION DATA COMPUTER CORP

DRN J. ALESI 4/25/72

(PUNCH CHECK) DETECTION

DWG NO C 7775 DASH NO -05 SH 1 OF 1

LTR	DESCRIPTION	APPD DATE
L	ADDED THIS SHEET, ECN 3899-16	
M	ADDED -06 ECN 5986-16	

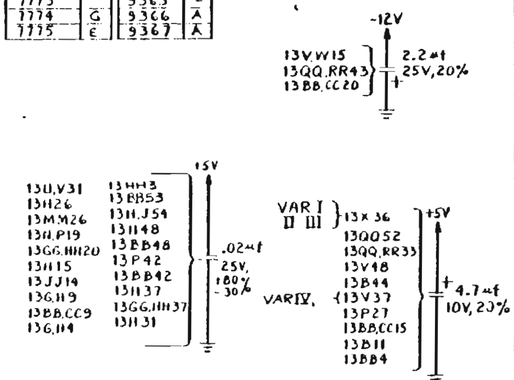
* UNLESS OTHERWISE SPECIFIED, SCHEMATIC INFORMATION ON THIS SHEET IS FOR ALL VARIATIONS. OTHER INFORMATION ON THIS SHEET IS FOR ASSEMBLY 5116-08 ON ARTWORK 5115-08

*VARIATION REFERENCE

VAR	ASSEMBLY	ARTWORK
I	5116-01-02-03-04-05-06	5115-01
II	5116-02-03-04-05-06	5115-02
III	5116-05-06	5115-05
	5116-06	5115-06
IV	5116-07	5115-07
	5116-08	5115-08

LOGIC DIAGRAM CHART

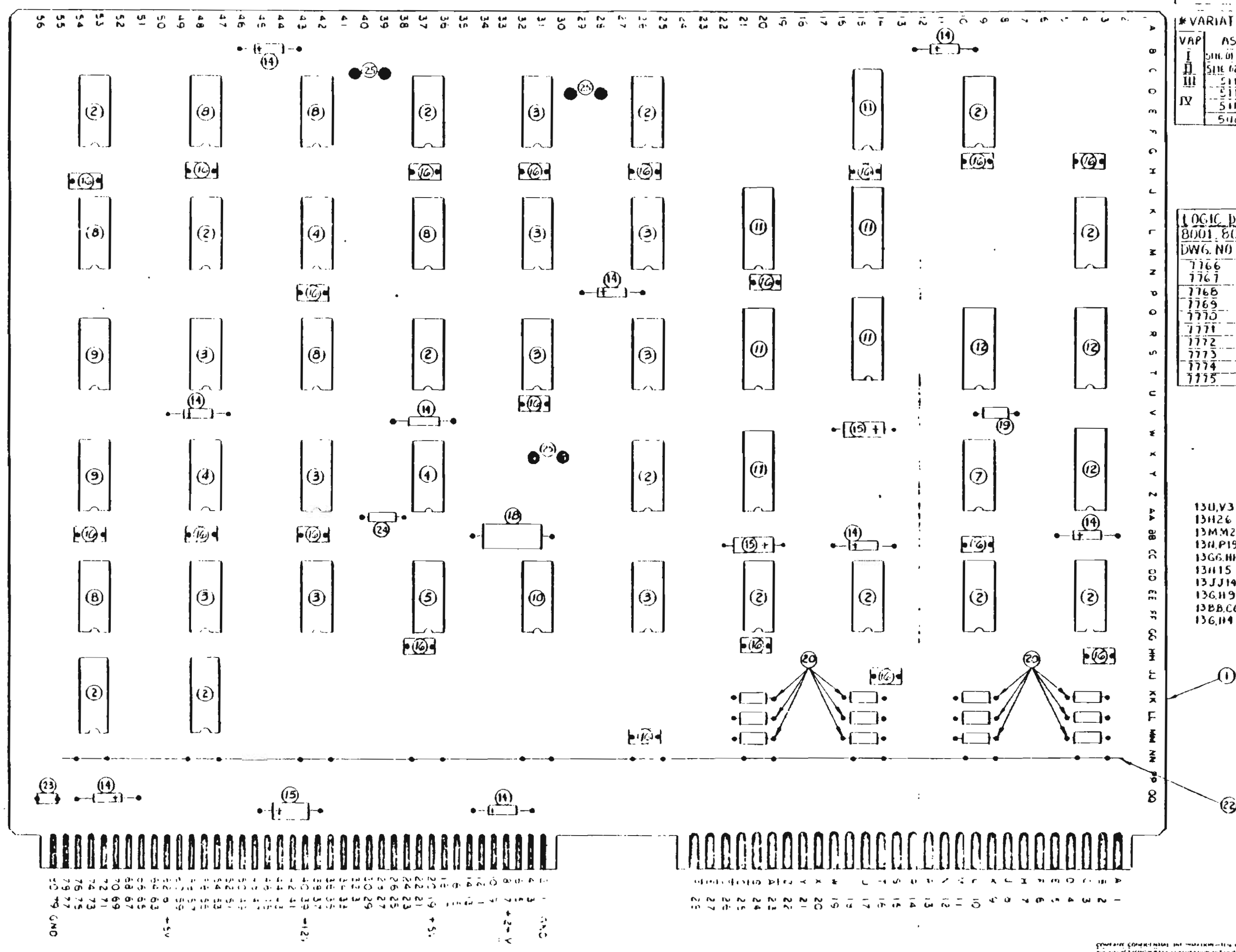
8001, 8010 DWG. NO	REV	8035, 8045 DWG. NO	REV
7766	A	9358	A
7767	-	9359	-
7768	-	9360	-
7769	A	9361	A
7770	A	9362	A
7771	A	9363	A
7772	A	9364	A
7773	-	9365	-
7774	G	9366	A
7775	E	9367	A



TYPE	LOC	OUTPUT PIN NO.
DCFF	S53	8,9
DCFF	Y53	8,9
PD2	EE31	5

SPARE GATES

FOR LIST OF PARTS SEE DETACHED PARTS LIST



DECISION DATA COMPUTER CORP.

COMPONENT ASSEMBLY

D 5116

WIRING REFERENCE CONTENTS

TYPE NUMBER/NAME: 8001, 8010 DATA RECORDER

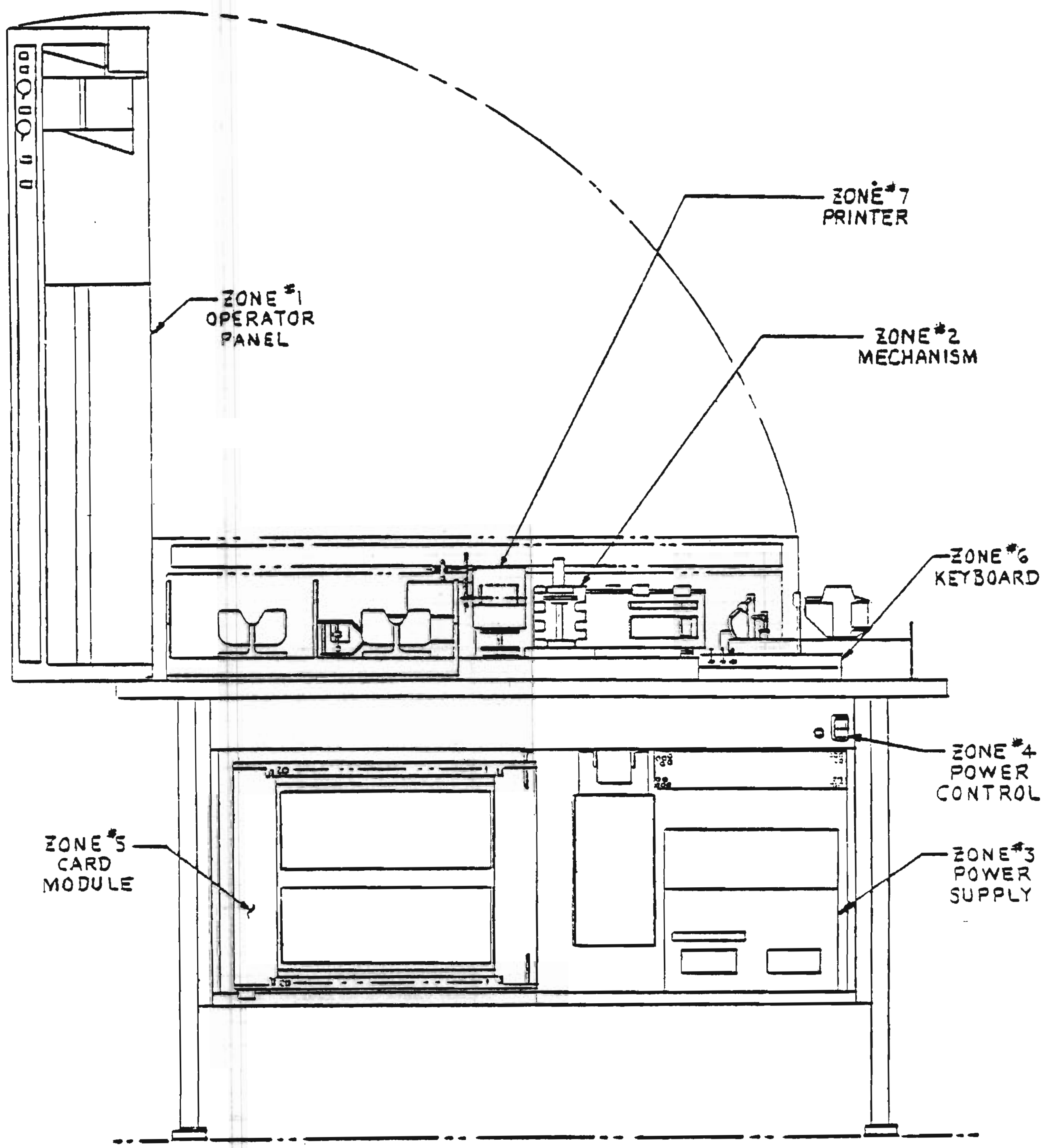
REFERENCE DRAWING NUMBER	TITLE	REFERENCE DRAWING REVISIONS					
6660	Zoning Drawing 80 Column	A	A	A	A	A	A
6661	A.C. Dist. and Power Control	A	A	A	B	B	B
2211 Sh.2,3	Pwr. Supply Schematic 60 Hz.	G	H	H	H	H	J
4590	Pwr. Supply Schematic 50/60 Hz.	B	B	B	B	B	B
3189 SH1,2,3	Motor Control Asm. - Wired	H	H	H	H	J	J
6808 Sh.1&2	Coil Asm. - Wired	A	A	A	A	A	A
2601 SH1,2,3	Keyboard Asm. - Wired	J	J	L	L	S	S
8715 Sh.1-9	80 DR/IDR Comp. Location & Wiring	-	D	F	H	L	L

REVISION OF CONTENTS	-	A	B	C	D
DATE OF E.C.N.	-	5/19/73	26 Jul 73	11 Oct 73	11 Dec 74
APPROVAL	-			RK	RK
ENGINEERING CHANGE NOTICE	-	2125-1P	2125-3P	2125-8C	3853-1C

DR80-D-1

DWG. NO. A 7552 | REV D | SH 1 OF 1

C 6660



REV	DESCRIPTION	APPR-DATE
A	REL PER ECN 1633-16 REVISED 9-26-72	[illegible]

REVISIONS

COMPANY CONFIDENTIAL INFORMATION [illegible]

DR80 - D - 2

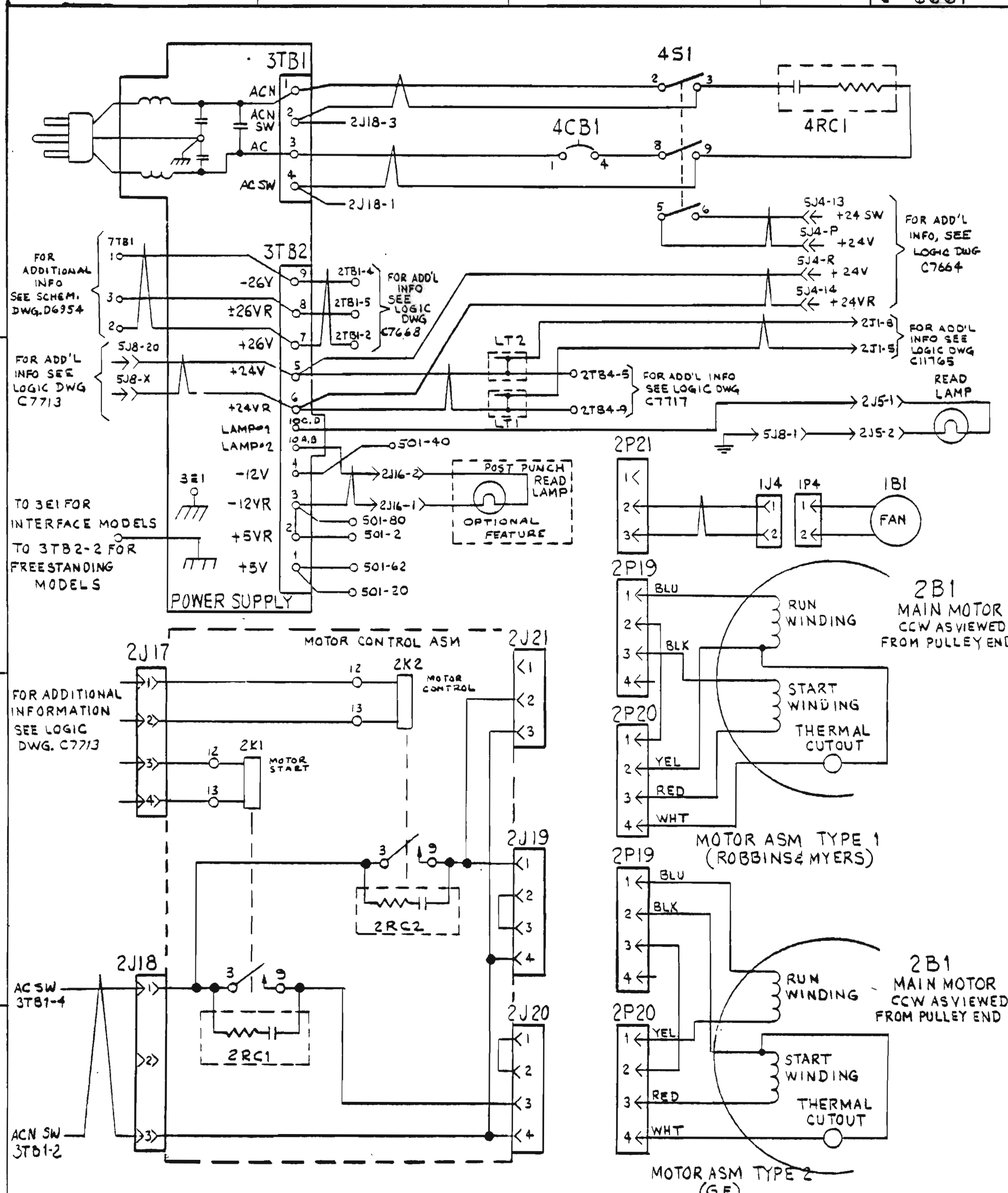
DWN FITZGERALD 7/7/72 CHK [illegible] 10-10-72

TITLE ZONING DRAWING 80 COLUMN

DWG NO. C 6660

DASH NO. -00

SH OF



REV	DESCRIPTION	APPVD-DATE
D	REVISED PER ECN 4063-1C	
C	REVISED PER ECN 3970-1C	
B	REV PER ECN 2584-1C	
A	REL PER ECN # 1633-1C REV PER LC #38, 81	11-20-72

REVISIONS

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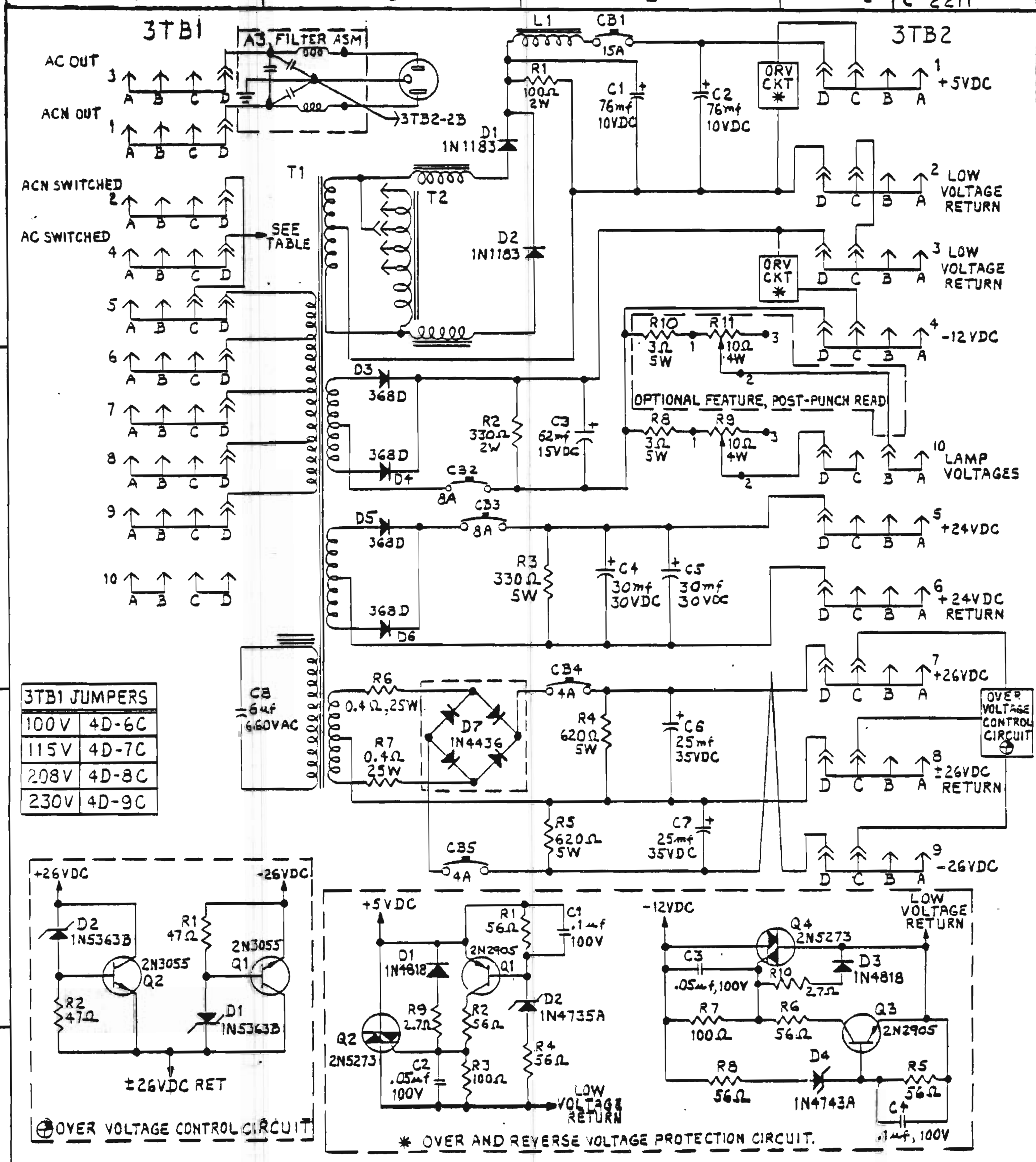
DR80-D-3

DRN FITZGERALD 6/72 ENG 11-20-72 CHK 11-17-72 MFG 12-1-72

TITLE AC DISTRIBUTION AND POWER CONTROL

DWG NO. C 6661 DASH NO. 00 SH 1 OF 1

C 2211



NOTES: 1- ALL RESISTORS ARE 1/2 W, 10%, UNLESS OTHERWISE SPECIFIED.
2- ALL INFORMATION ON THIS SHEET APPLIES TO M.E.C. PART NO. 101826 REV A AND REV B AND E.T.C. PART # E103-01.

DR80 - D - 4

REV	DESCRIPTION	APPRO-DATE
J	ADDED E.T.C. PT. # NOTE 2 ECL 3746	
H	REV 4 REDRAWN, ECN 976-1C	

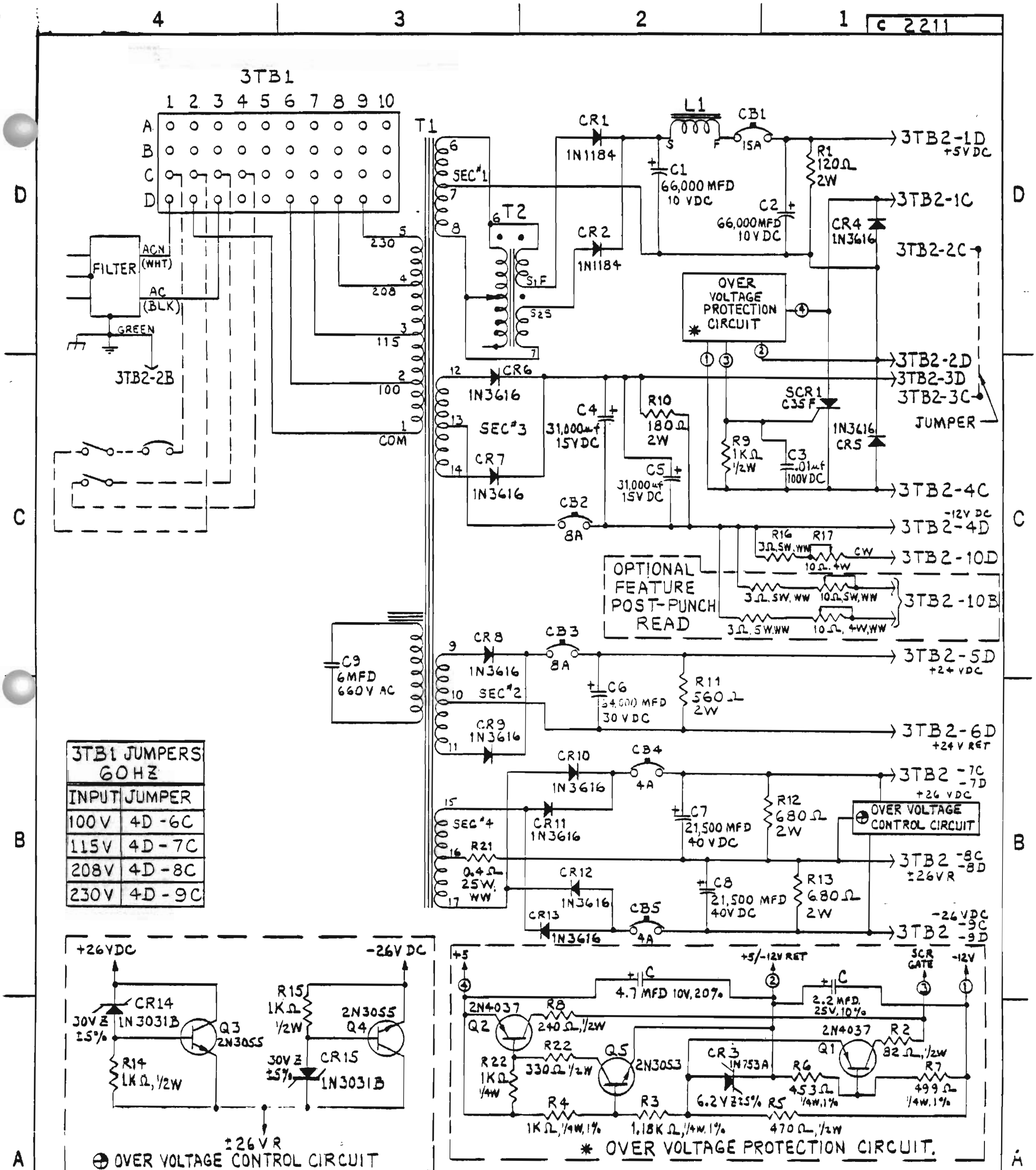
REVISIONS

DRN J. ALESI 2/12/72

TITLE POWER SUPPLY SCHEMATIC 60 HZ

DWG NO. C 2211

SH 2 OF



3TB1 JUMPERS 60HZ	
INPUT	JUMPER
100V	4D-6C
115V	4D-7C
208V	4D-8C
230V	4D-9C

NOTES: 1-ALL RESISTOR ARE 1/2W, 10%, UNLESS OTHERWISE SPECIFIED.
2-ALL INFORMATION ON THIS SHEET APPLIES TO ACME ELECTRIC P/N PS61321 REV. G.

REV	DESCRIPTION	ECN	APPVD-DATE
K	CB2 WAS 4A	3880-1C	
J	ADDED THIS SHT.	3746-1C	

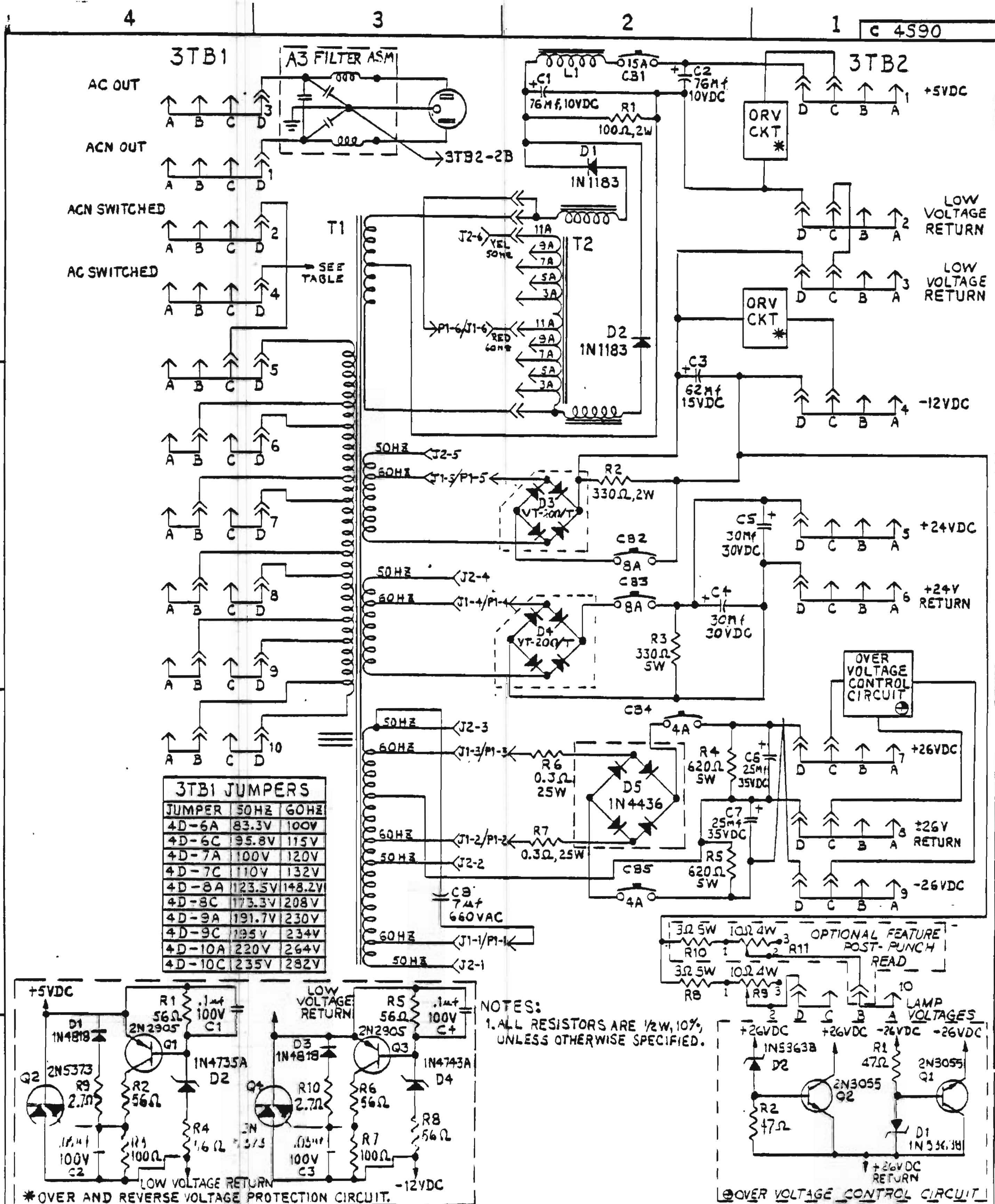
REVISIONS

DR80-D-5

DRN J. ALESI 5 SEP. 74

TITLE POWER SUPPLY SCHEMATIC 60 HZ

DWG NO. C 2211 DASH NO. -00 SH 3 OF



3TB1 JUMPERS		
JUMPER	50HZ	60HZ
4D-6A	83.3V	100V
4D-6C	95.8V	115V
4D-7A	100V	120V
4D-7C	110V	132V
4D-8A	123.5V	148.2V
4D-8C	173.3V	208V
4D-9A	191.7V	230V
4D-9C	195V	234V
4D-10A	220V	264V
4D-10C	235V	282V

NOTES:
1. ALL RESISTORS ARE 1/2W, 10%, UNLESS OTHERWISE SPECIFIED.

REV	DESCRIPTION	APPVD-DATE
B	REVISED PER ECN 996-1C	2-17-72
A	REVISED PER ECN 953-1P	12-24-71
–	RELEASED ECN 631-1C	9-13-71

REVISIONS

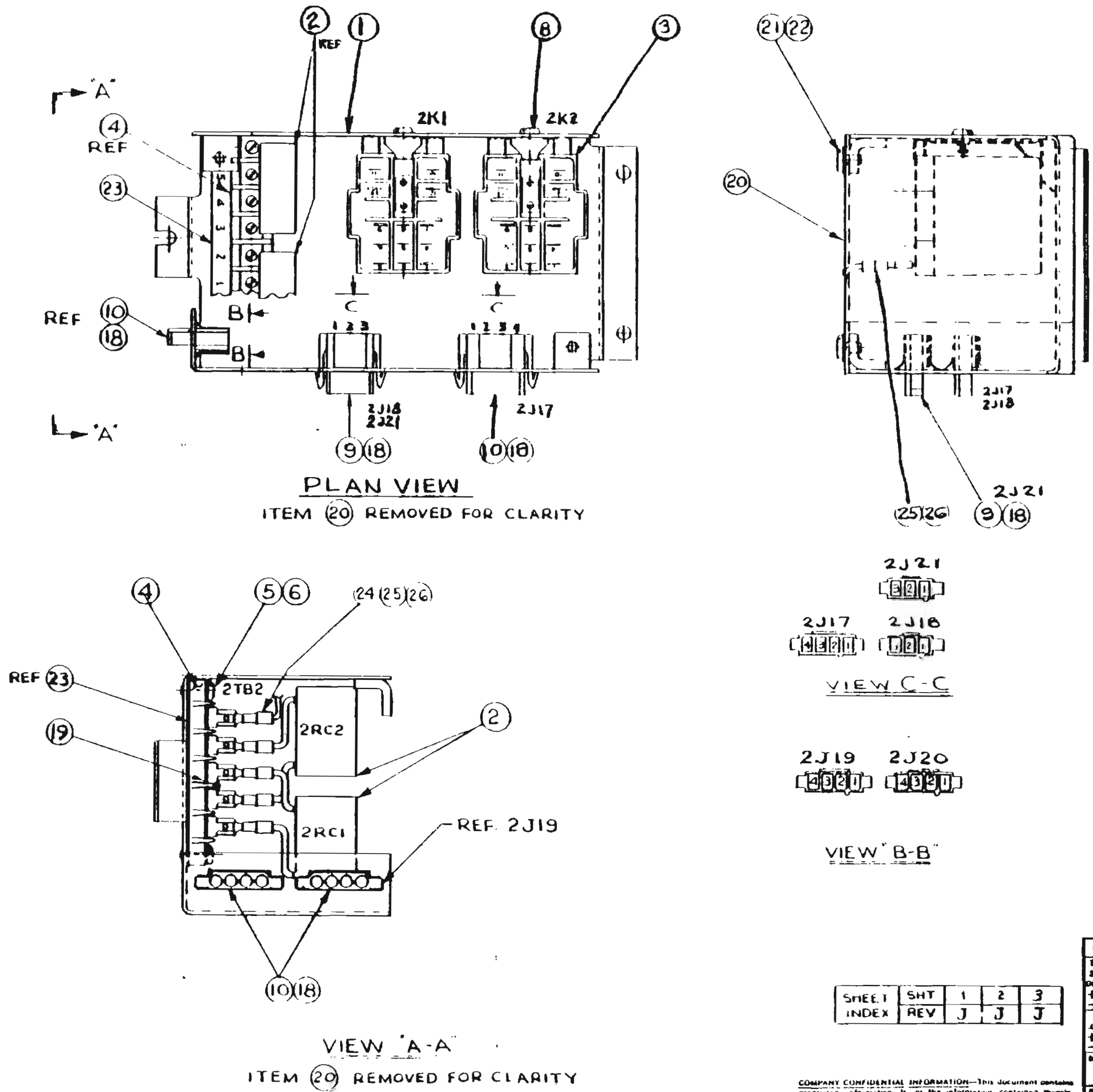
DR80-D-6

DRN J. ALESI 9/2/71
ENG 9-13-71
CHK 9/3/71
MFG

TITLE POWER SUPPLY SCHEMATIC 50/60 HZ

DWG NO. C 4590
DASH NO.
SH 1 OF 1

REV	DESCRIPTION	DATE
C	SEE E.C.N. NO. 360-IC	
D	ADDED ITEM 23 SEE ECN Nº 748-IC	[illegible]
F	SEE DETAIL SH ECN 9462C	[illegible]
G	REVISED PER ECN 1220-IC	[illegible]
H	REVISED PER ECN 1350-IC	[illegible]
J	REVISED PER ECN 2584-IC	[illegible]



NOTE
FOR WIRING DIAGRAM, REFER TO SHEET 2 OF THIS DRAWING.

ALTERNATE CONSTRUCTION

SEE DETACHED F.L. FOR PARTS

SHEET INDEX	SHT	1	2	3
	REV	J	J	J

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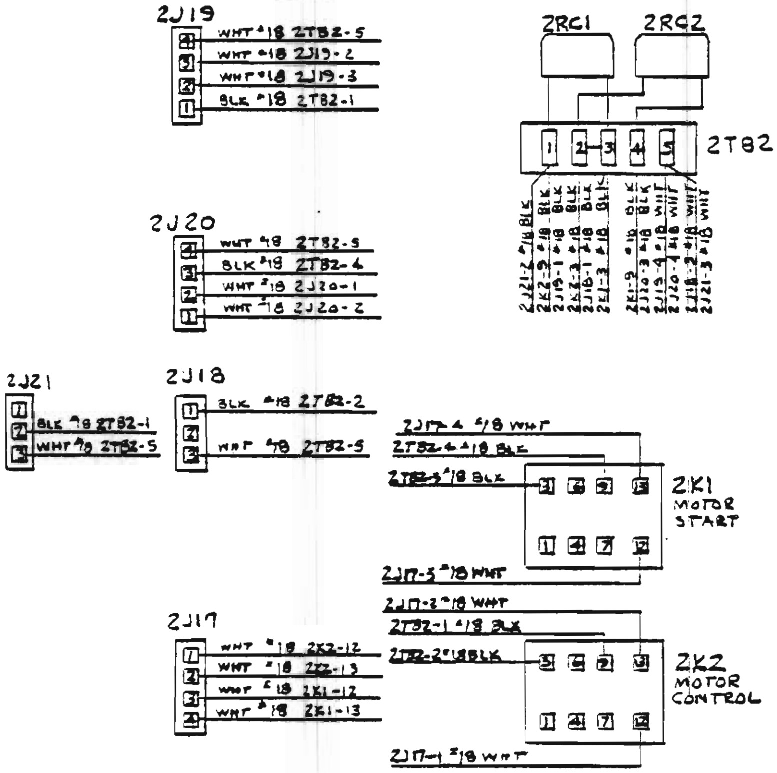
0R30-D-7

DRN A.C. MARTELLI
CHK 1 24 71

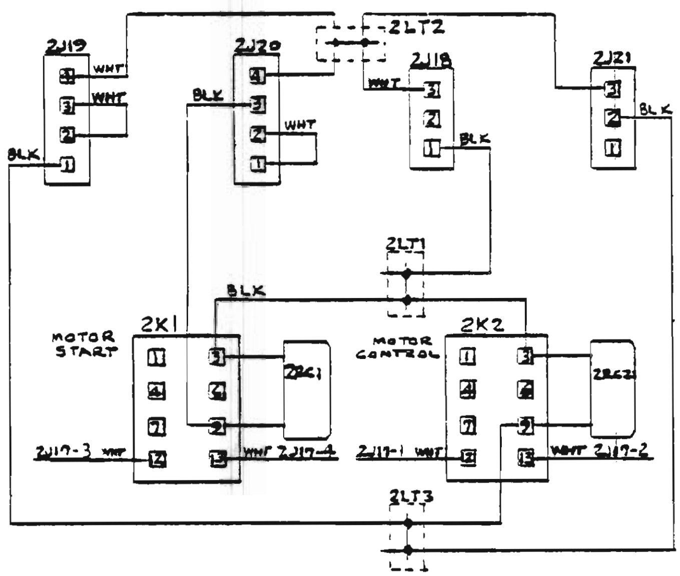
TITLE MOTOR CONTROL ASM WIRED

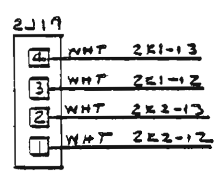
DWG NO. C 3189 DASH NO. 00 SH 1 OF 3

C-3189



ALTERNATE WIRING DIAGRAM





NOTES:
1- ALL WIRES ARE #18 AWG.
2- USE FIND NO (27) ON WIRES ATTACHED TO TERMINALS 3 AND 9 OF 2K1 AND 2K2

J	REVISED/REDRAWN ECN 2584-[illegible]	[illegible]
REV	DESCRIPTION	APPVD-DATE
	REVISIONS	

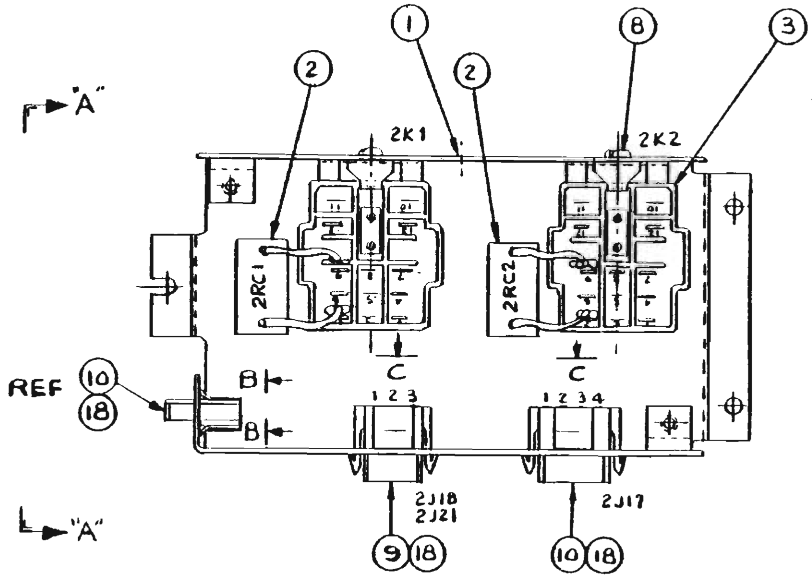
DR80-D-8

DRN CEB 3 OCT 73 | ENG [illegible] OCT 73
CHK | MFG [illegible] 73

TITLE: MOTOR CONTROL ASM WIRED

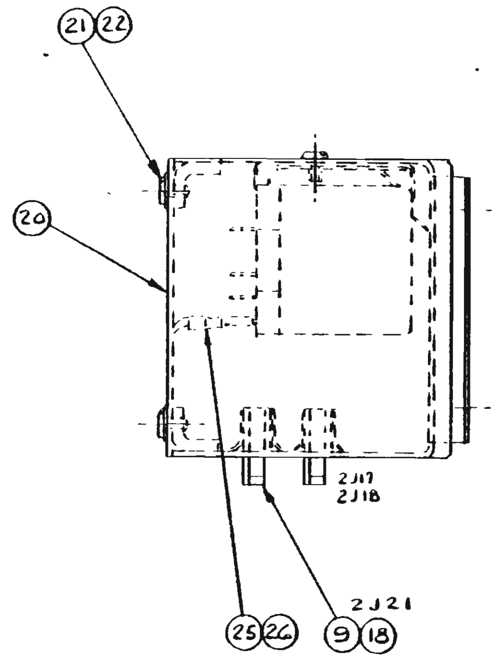
DWG NO. C-3189 | DASH NO. -00 | SH 2 OF 3

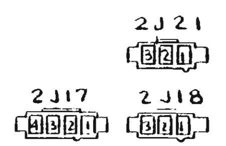
REV	DESCRIPTION	APPVD DATE
J	REVISED & ADDED SHT 3-ECN 2584 IC	RD 8 OCT 73



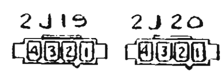
PLAN VIEW

ITEM 20 REMOVED FOR CLARITY





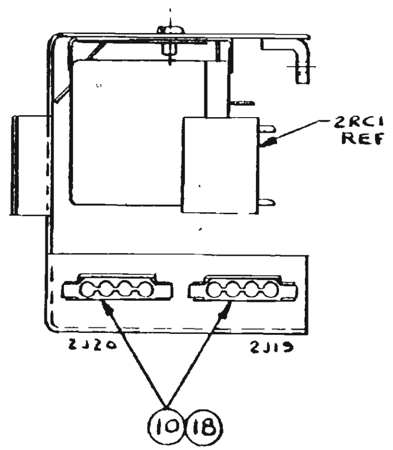
VIEW "C-C"



VIEW "B-B"

NOTE

FOR WIRING DIAGRAM, REFER TO SHEET 2 OF THIS DRAWING



VIEW "A-A"

ITEM 20 REMOVED FOR CLARITY

SEE DETACHED P.L. FOR PARTS

QTY	ITEM	DWG SIZE	PART NO.	DESCRIPTION

TOLERANCE (EXCEPT AS NOTED): 2 PLACE DECIMALS ±; 3 PLACE DECIMALS ±; ANGLES ±

HOLE DIAMETER TOLERANCE: .00–.25 +.003 −.002; .25–.50 +.005 −.003; 50 UP +.010 −.005

MTL

FIN

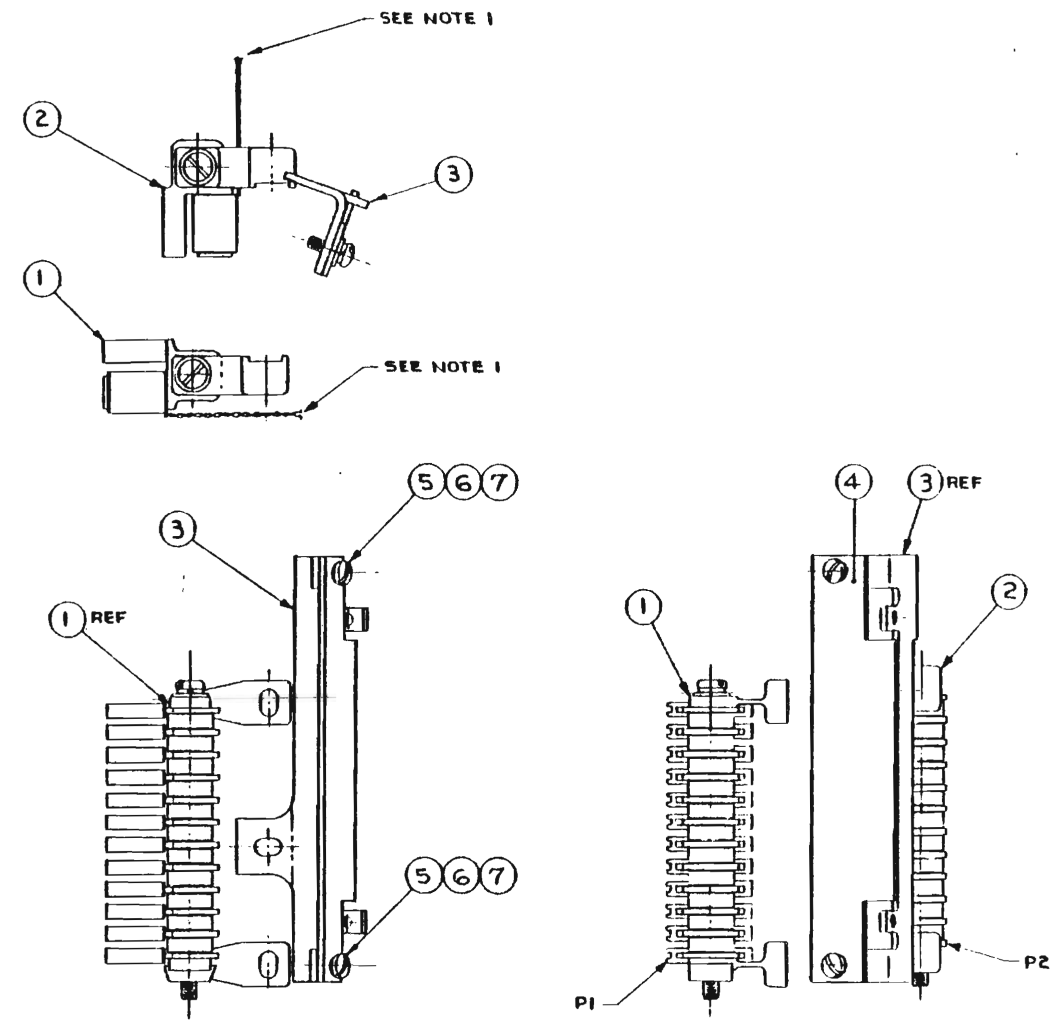
DR80-D-9

DRN CEB 27SEP73 | ENG [signature] 10OCT73

CHK | MFG [signature] 12 OCT 73

TITLE MOTOR CONTROL ASM WIRED

DWG NO. C-3189 | DASH NO -00 | SH 3 OF 3



REV	DESCRIPTION	APPVD DATE
A	REVISED & REDRAWN REL PT NO-00 ECN 1360-7P	
C	SEE ECN 2747-1C	
D	SEE ECN 2964-2C	
E	SEE ECN 3762-1C	

NOTES:

1- SOLDER LEADS OF 1 & 2 TO 4 PER WIRING DIAGRAM ON SHEET 2.

SHEET INDEX	SHT	1	2
	REV	E	A

QTY	ITEM	DWG SIZE	PART NO	DESCRIPTION

TOLERANCE (EXCEPT AS NOTED): 2 PLACE DECIMALS ± / ; 3 PLACE DECIMALS ± / ; ANGLES ± /

HOLE DIAMETER TOLERANCE: .00–.25 +.008 −.002 ; .25–.50 +.005 −.003 ; .50 UP +.010 −.005

MTL

FIN

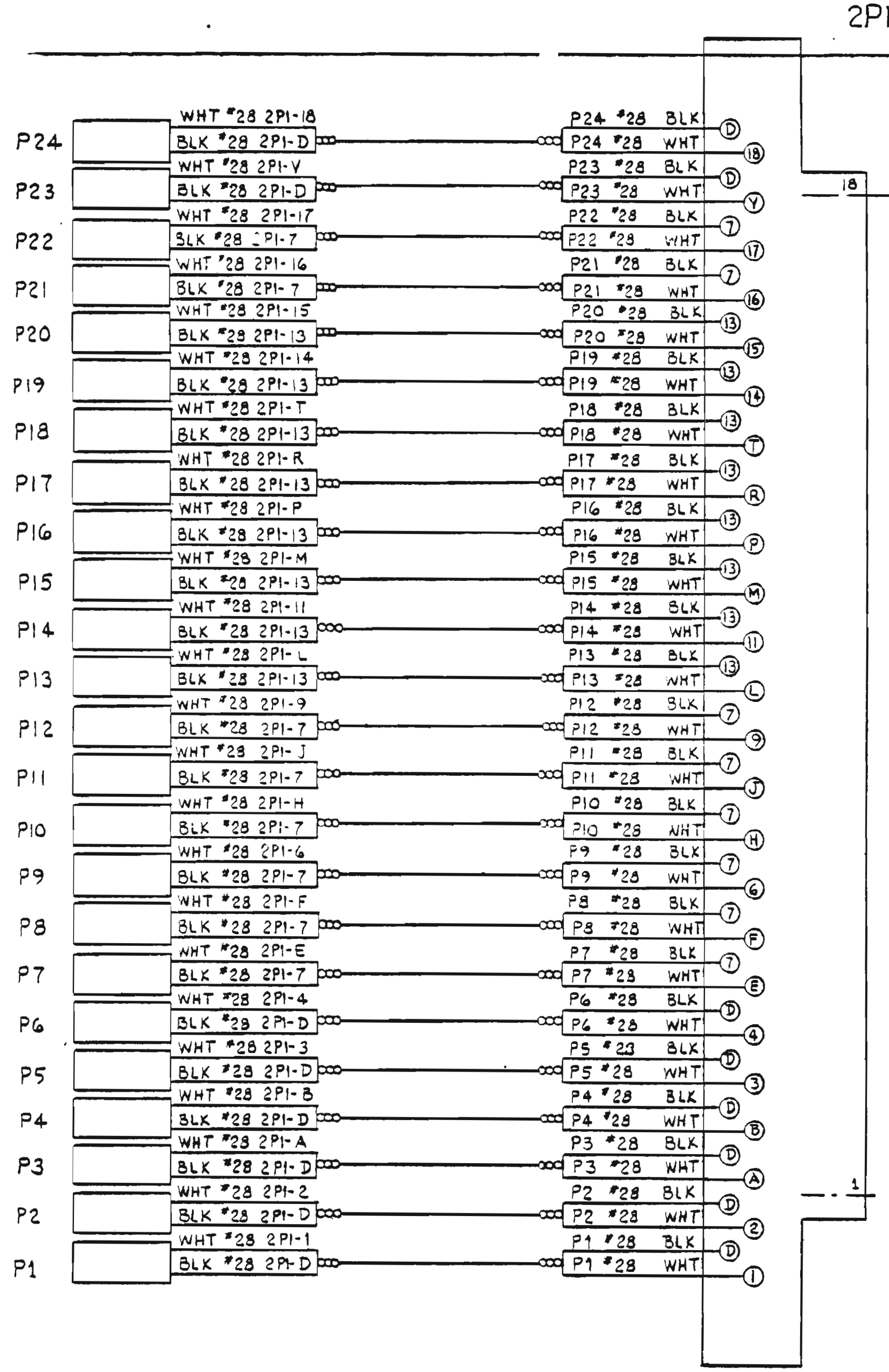
DR80- D- 10

TITLE: COIL ASM-WIRED

DWG NO: C-6808

DASH NO. -00

SH 1 OF 2



REV	DESCRIPTION	ECN	APPVD-DATE
A	REL PT NO-00	1380-7P	

REVISIONS

DR80-D-11

DRN FITZGERALD

CHK

ENG

MFG

TITLE COIL ASM WIRED

DWG NO. C-6808

DASH NO. -00

SH 2 OF 2

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VARIABLE DATA		
STATUS	PART Nº	DESCRIPTION
SUPERSEDED BY 2601-28	2601 00	KEYBD. ASM., U.S.A. ENGLISH, DD
SUPERSEDED BY 2601-13	01	U.S.A. ENGLISH, TI
	02	U.S.A. ENGLISH, DD
	03	U.K. ENGLISH, DD
	04	ITALIAN, DD
	05	PORT/BRAZILIAN, DD
SUPERSEDED BY 2601-41	06	FRENCH, DD
	07	SPANISH, DD
	08	DANISH, DD
	09	GERMAN/AUSTRIAN, DD
	10	FINNISH/SWEDISH, DD
	11	NORWEGIAN, DD
SUPERSEDED BY 2601-42	12	BELGIAN, DD
	13	BASIC, TI
	14	U.K. ENGLISH, TI
	15	PORT/BRAZILIAN, TI-80
SUPERSEDED BY 2601-43	16	FRENCH/BELGIAN, TI
	17	SPANISH, TI
	18	DANISH, TI
	19	GERMAN/AUSTRIAN, TI
	20	FINNISH/SWEDISH, TI
	21	NORWEGIAN, TI
	22	U.S.A. ENGLISH, DD-80
	23	U.S.A. ENGLISH, TI-80
	24	ASCII, DD-80
	25	UNITED/KINGDOM, DD-80
	26	ITALY, DD-80
	27	LATIN AMERICA, DD-80
	28	FRANCE QWERTY, DD-80
SUPERSEDED BY 2601-44	29	FRANCE AZERTY, DD-80
SUPERSEDED BY 2601-45	30	BELGIUM, DD-80
	31	NORWAY/DENMARK, DD-80
	32	FINLAND/SWEDEN, DD-80
	33	PORT./BRAZILIAN, DD-80
	34	GERMANY/AUSTRIA, DD-80
	35	GERMANY (U.S. SYM) DD-80
	36	SPAIN, DD-80
SUPERSEDED BY 2601-47	37	KATAKANA, DD-96
SUPERSEDED BY 2601-48	38	KATAKANA, TI-96
SUPERSEDED BY 2601-49	39	KATAKANA, DD-80
SUPERSEDED BY 2601-50	40	KATAKANA, TI-80
	41	FRENCH, DD
	42	BELGIAN, DD
	43	FRENCH/BELGIAN, TI
	44	FRANCE/AZERTY, DD-80
	45	BELGIUM, DD-80
	46	PORT/BRAZILIAN, TI-96
	47	KATAKANA, DD-96
	48	KATAKANA, TI-96
	49	KATAKANA, DD-80
	2601-50	KEYBD. ASM., KATAKANA, TI-80

SEE NOTE 1

NOTES:
1. ITEMS 24, 25 & 26 TO BE ADDED TO MODULE ASSEMBLY FOR FEATURE F-1051.

MULTISTATUS DWG

SHEET INDEX	SHEET	1	2	3
	REV.	S	S	S

PR80-D-12

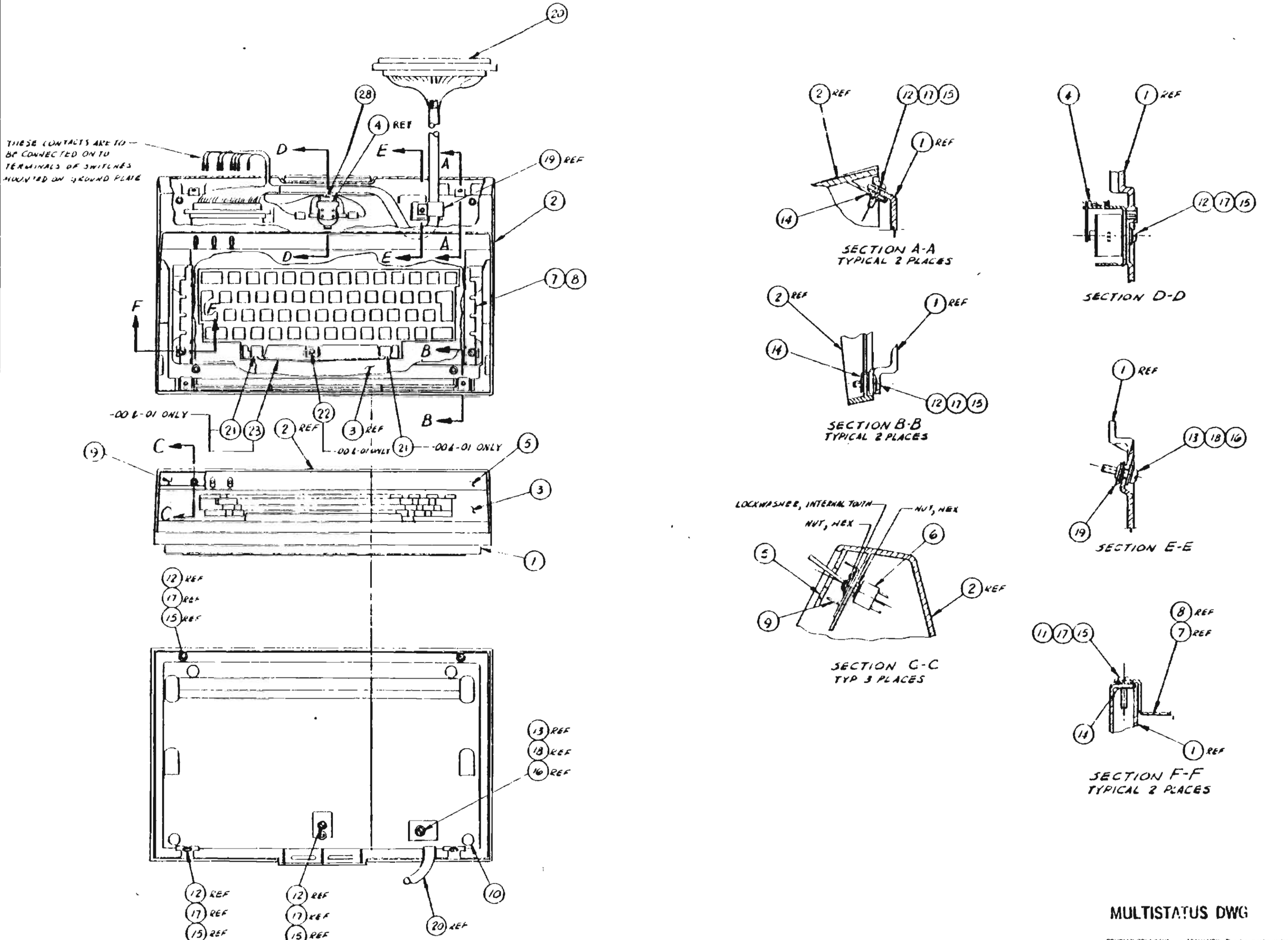
KEYBOARD ASM

D-2601

1092-D

1 80 083

REV	DESCRIPTION	APPVD DATE
F	[illegible] ECN 1339-IC	[illegible]
G	SEE ECN 1362-IC	[illegible]
H	[illegible]	[illegible]
J	ADDED & RELEASED PT-34 THRU 36 SEE ECN NO 1707-IC	[illegible]
L	ADDED & RELEASED PT-37 THRU 40 SEE ECN # 2241-IC	[illegible]
M	SEE ECN NO 2783-IC	[illegible]
N	SEE ECN NO 3048-IC	[illegible]
P	ADDED & RELEASED PT.-46 SEE ECN NO 3599-IC	[illegible]
R	SEE ECN 3379-IC	[illegible]
S	SEE ECN NO 3543-IC	[illegible]
X	SEE ECN NO 4131-IC	[illegible]



MULTISTATUS DWG

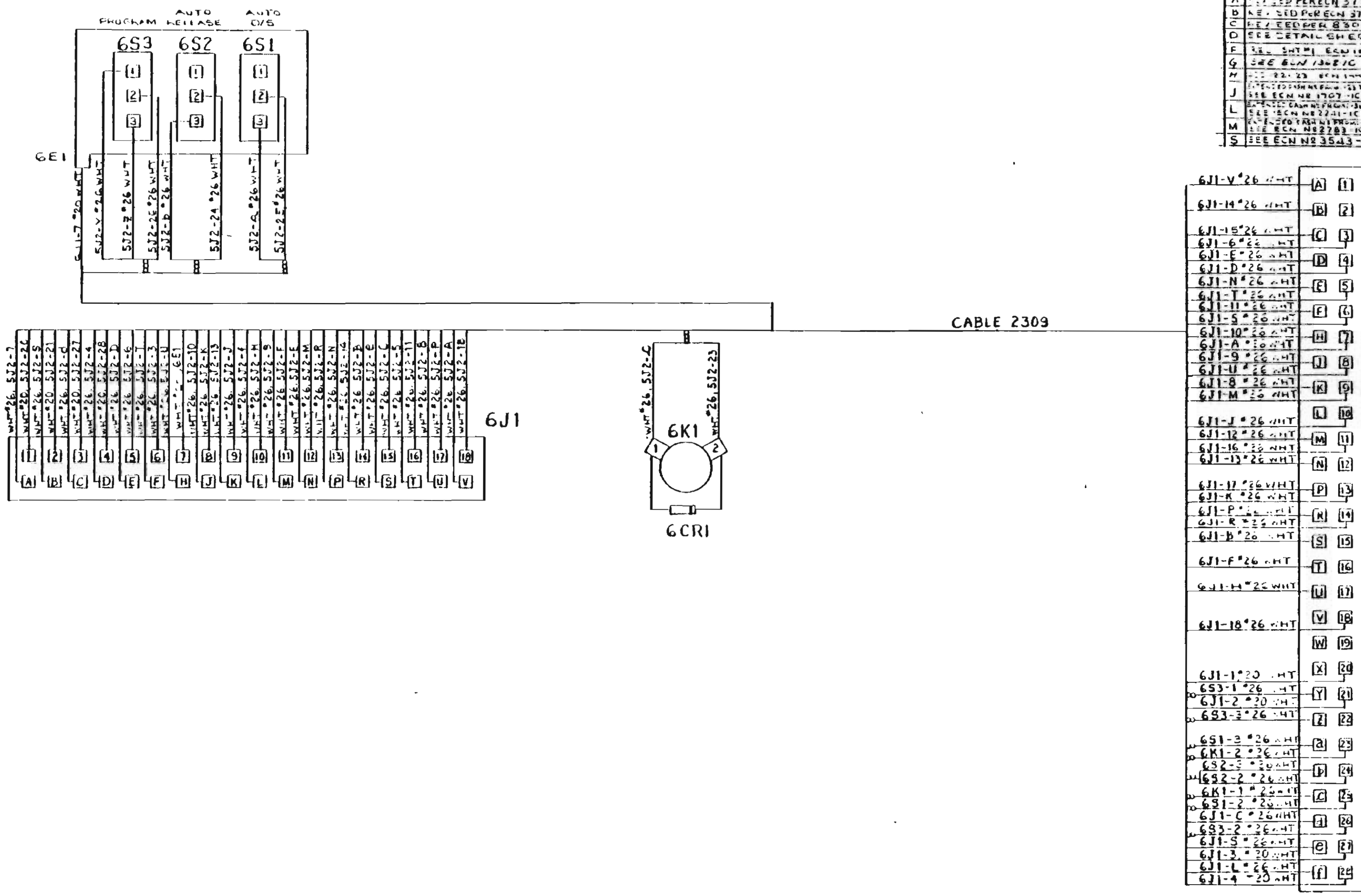
COMPANY CONFIDENTIAL INFORMATION

PART NO	DESCRIPTION
DR80-D-13	
TITLE	KEYBOARD ASM
DWG NO	D 2601

1092 a

REV	DESCRIPTION	APPVD DATE
A	REVISED PER ECN 377-1P	[illegible]
B	REVISED PER ECN 377-2C	[illegible]
C	REVISED PER 830-1C	[illegible]
D	SEE DETAIL SH ECN 840-1C	[illegible]
F	SEE SHT #1 ECN 1639-1C	[illegible]
G	SEE ECN 1368 IC	[illegible]
H	[illegible] 22-23 ECN [illegible] -1C	[illegible]
J	EXTENDED DASH NR FROM -35 TO -36 SEE ECN NR 1707-1C	[illegible]
L	EXTENDED DASH NR FROM -36 TO -40 SEE ECN NR 2241-1C	[illegible]
M	EXTENDED DASH NR FROM -40 TO -35 SEE ECN NR 2783-1C	[illegible]
S	SEE ECN NR 3543-1C	[illegible]

X	SEE ECN NR 4131-1C



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DR80-D-14

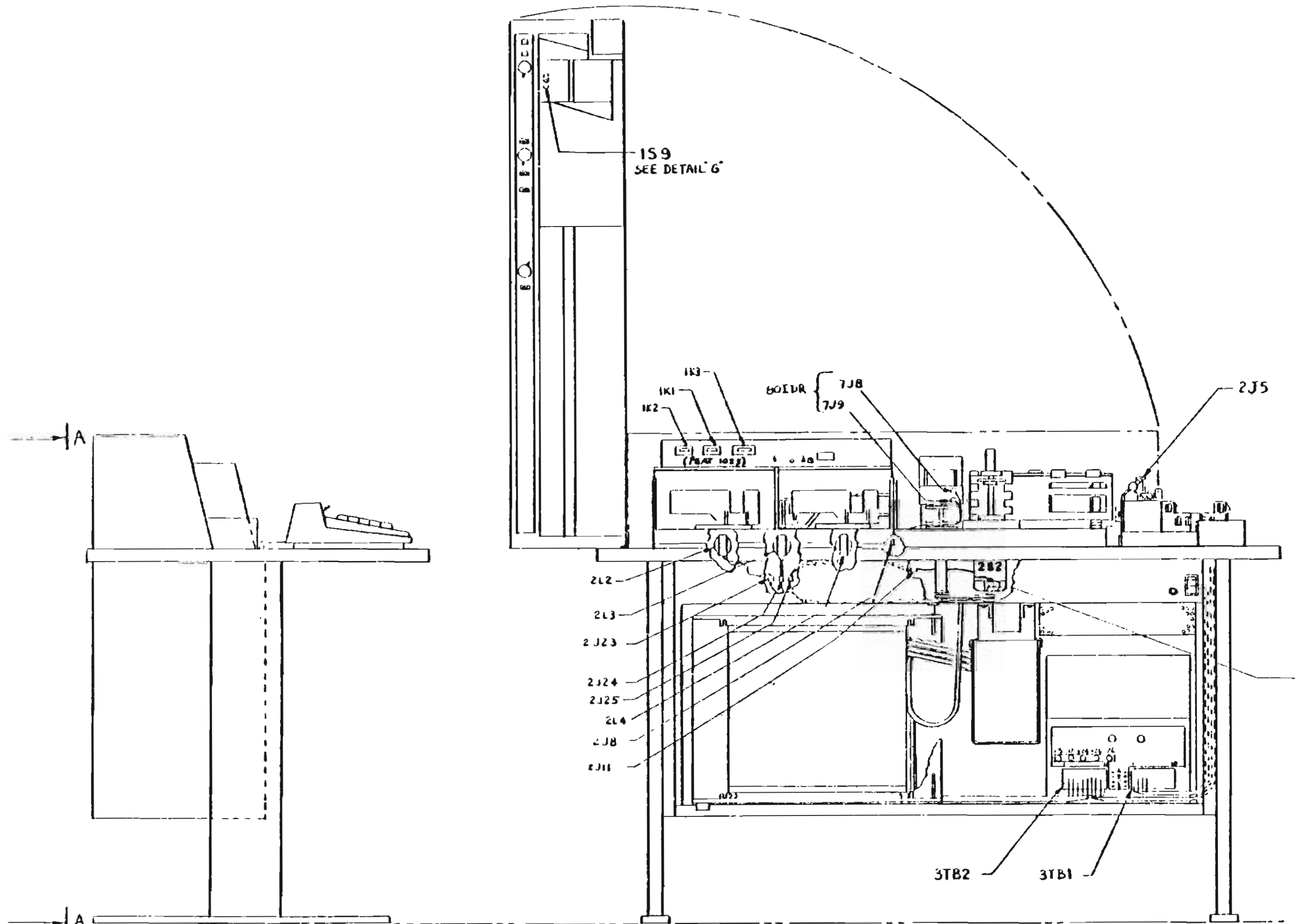
KEYBOARD ASSEMBLY, WIRED CR, IDR & SDR

D 2601

SH 3 OF 3

1092

REV	DESCRIPTION	APP'D DATE
–	RELEASE PER ECN 1674-IC	[illegible]
A	REVISED PER ECN [illegible]-IC	[illegible]
B	REVISED PER ECN [illegible]-IC	[illegible]
C	REVISED PER ECN [illegible]-IC	[illegible]
D	REVISED PER ECN [illegible]-IC	[illegible]
E	REVISED PER ECN [illegible]-IC	[illegible]
F	REVISED PER ECN 2241-IC	[illegible]
G	REVISED PER ECN 2294-IC	[illegible]
H	REVISED PER ECN 2829-IC	[illegible]
J	REVISED PER ECN 2750-IC	[illegible]
K	REVISED PER ECN 3350-IC	[illegible]
L	REVISED PER ECN 3748-IC	[illegible]



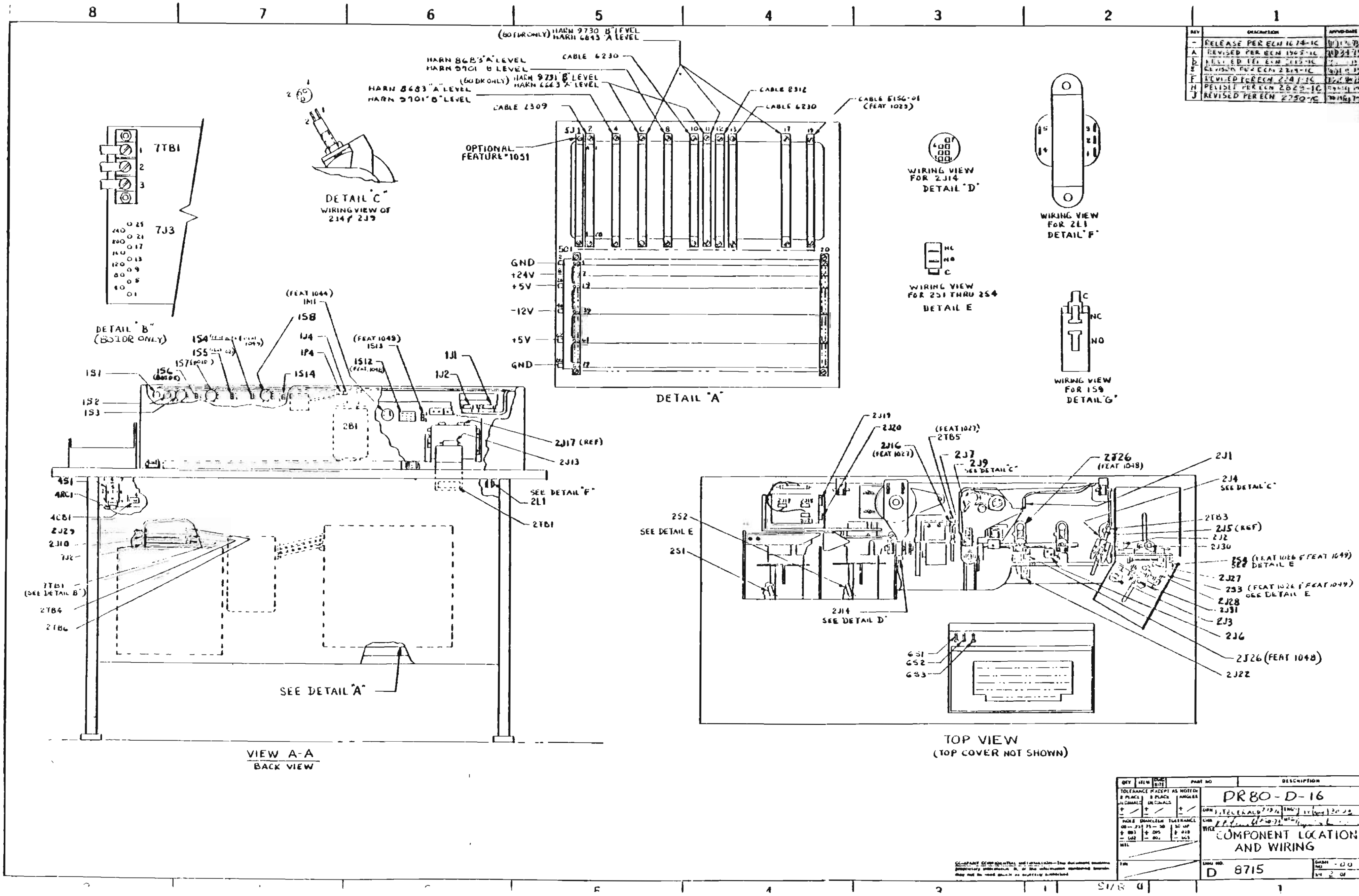
SHEET	1	2	3	4	5	6	7	8	9
INDEX REV	L	J	F	L	K	K	F	G	–

DR80-D-15

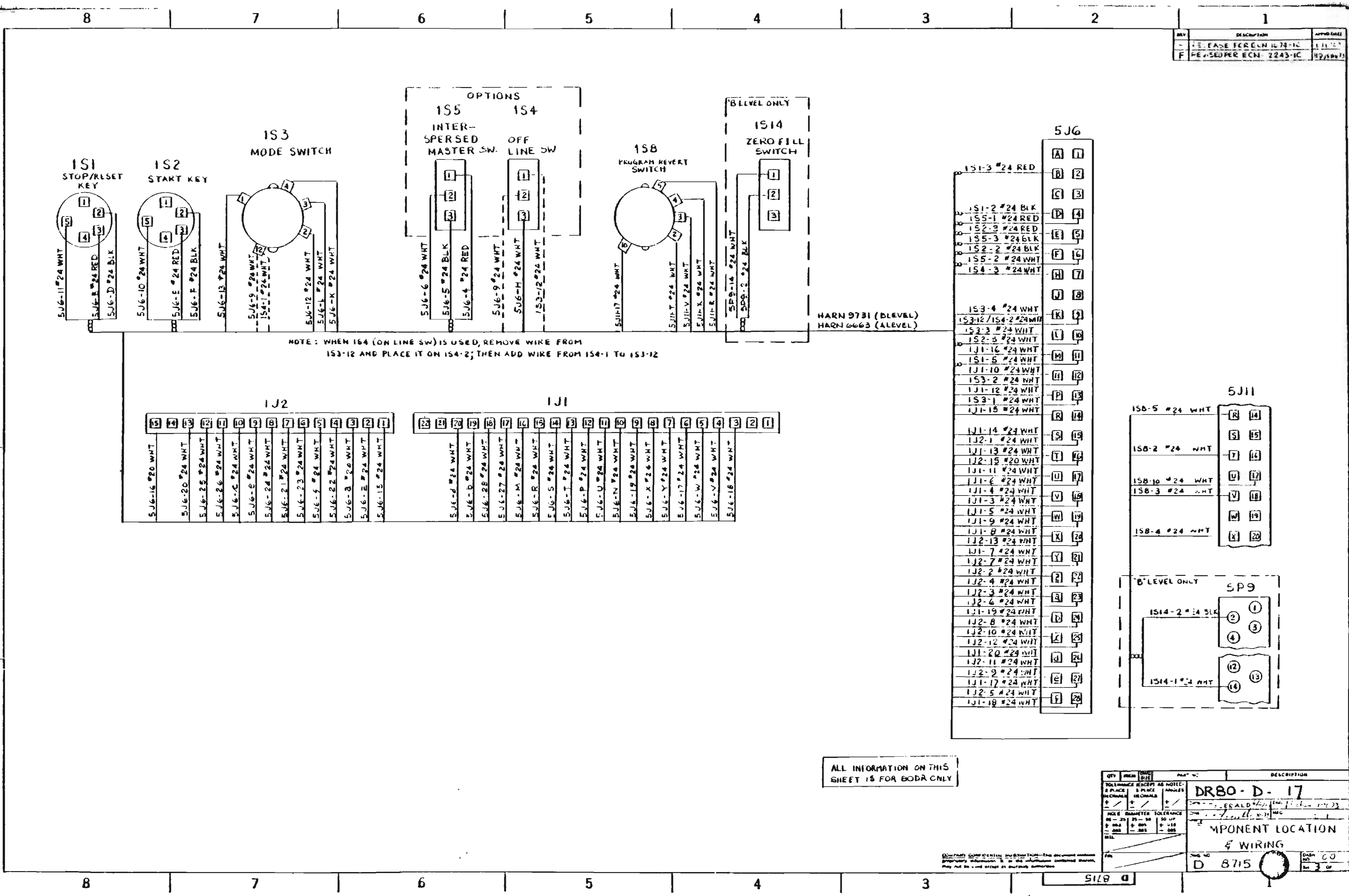
COMPONENT LOCATION
& WIRING

DWG NO. D 8715

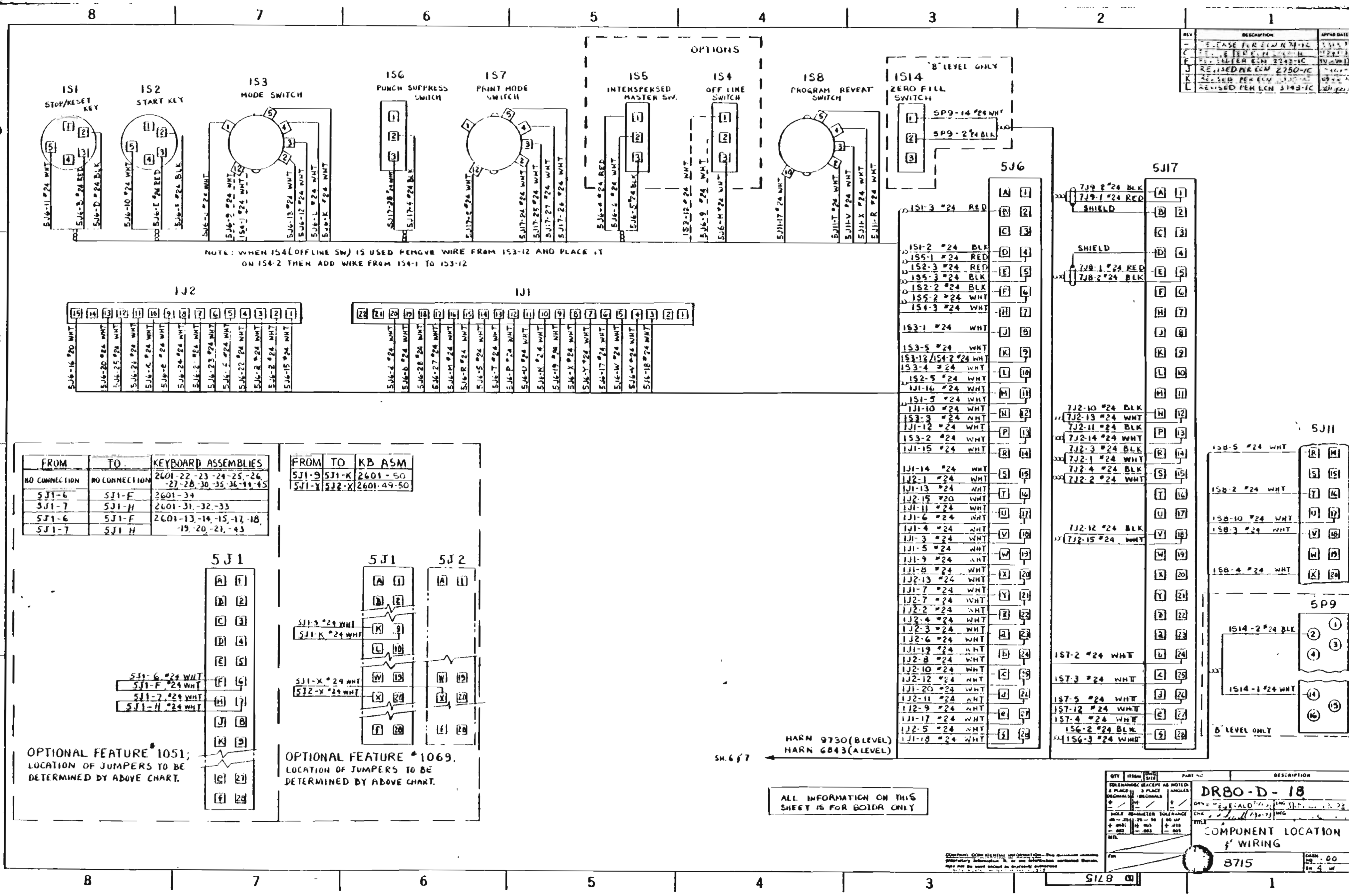
SH 1 OF 9



REV | DESCRIPTION | APPVD DATE
- RELEASE PER ECN 1674-1C
A REVISED PER ECN 1963-1C
E REVISED PER ECN 2314-1C
F REVISED PER ECN 2241-1C
H REVISED PER ECN 2623-1C
J REVISED PER ECN 2750-1C
(60 DR ONLY) HARN 9730 'B' LEVEL
HARN 6693 'A' LEVEL
HARN 8683 'A' LEVEL
HARN 9701 'B' LEVEL
(60 DR ONLY) HARN 9731 'B' LEVEL
HARN 6663 'A' LEVEL
CABLE 6230
CABLE 2309
CABLE 2312
CABLE 6156-01 (FEAT 1023)
OPTIONAL FEATURE 1051
GND
+24V
+5V
-12V
+5V
GND
DETAIL 'A'
7TB1
7J3
DETAIL 'B' (8010R ONLY)
DETAIL 'C'
WIRING VIEW OF 2J4 & 2J9
WIRING VIEW FOR 2J14
DETAIL 'D'
WIRING VIEW FOR 2S1 THRU 2S4
DETAIL E
WIRING VIEW FOR 2L1
DETAIL 'F'
WIRING VIEW FOR 1S9
DETAIL 'G'
1S1
1S2
1S3
1S6 (8010R)
1S7 (8010)
1S5
1S4
1S8
1S14
1J4
1P4
(FEAT 1044) 1M1
(FEAT 1049) 1S13
1S12
1J1
1J2
2B1
2J17 (REF)
2J13
SEE DETAIL 'F'
2L1
2TB1
4S1
4RC1
4CB1
2J29
2J10
7J2
7TB1 (SEE DETAIL 'B')
2TB4
2TB6
SEE DETAIL 'A'
VIEW A-A
BACK VIEW
2J19
2J20
2J16 (FEAT 1027)
(FEAT 1027) 2TB5
2J7
2J9 SEE DETAIL 'C'
2J26 (FEAT 1048)
2J1
2J4 SEE DETAIL 'C'
2TB3
2J5 (REF)
2J2
2J30
2S4 (FEAT 1026 & FEAT 1049) SEE DETAIL E
2J27
2S3 (FEAT 1026 & FEAT 1049) SEE DETAIL E
2J28
2J31
2J3
2J6
2J26 (FEAT 1048)
2J22
2S2
SEE DETAIL E
2S1
2J14
SEE DETAIL 'D'
6S1
6S2
6S3
TOP VIEW
(TOP COVER NOT SHOWN)
PR80-D-16
COMPONENT LOCATION
AND WIRING
D 8715



OPTIONS
1S1
STOP/RESET KEY
1S2
START KEY
1S3
MODE SWITCH
1S5
INTER-SPERSED MASTER SW.
1S4
OFF LINE SW
1S8
PROGRAM REVERT SWITCH
'B' LEVEL ONLY
1S14
ZERO FILL SWITCH
5J6
5J11
5P9
1J1
1J2
HARN 9731 (B LEVEL)
HARN 6663 (A LEVEL)
NOTE: WHEN 1S4 (ON LINE SW) IS USED, REMOVE WIRE FROM 1S3-12 AND PLACE IT ON 1S4-2; THEN ADD WIRE FROM 1S4-1 TO 1S3-12
1S1-3 #24 RED
ALL INFORMATION ON THIS SHEET IS FOR BODA ONLY
DR80 - D - 17
MPONENT LOCATION & WIRING
D 8715



NOTE: WHEN 1S4 (OFF LINE SW) IS USED REMOVE WIRE FROM 1S3-12 AND PLACE IT ON 1S4-2 THEN ADD WIRE FROM 1S4-1 TO 1S3-12

FROM	TO	KEYBOARD ASSEMBLIES
NO CONNECTION	NO CONNECTION	2601-22, -23, -24, -25, -26, -27, -28, -30, -35, -36, -44, -45
5J1-6	5J1-F	2601-34
5J1-7	5J1-H	2601-31, -32, -33
5J1-6	5J1-F	2601-13, -14, -15, -17, -18, -19, -20, -21, -43
5J1-7	5J1-H	

OPTIONAL FEATURE # 1051; LOCATION OF JUMPERS TO BE DETERMINED BY ABOVE CHART.

FROM	TO	KB ASM
5J1-9	5J1-K	2601-50
5J1-Y	5J2-X	2601-49-50

OPTIONAL FEATURE # 1069. LOCATION OF JUMPERS TO BE DETERMINED BY ABOVE CHART.

ALL INFORMATION ON THIS SHEET IS FOR BO10A ONLY

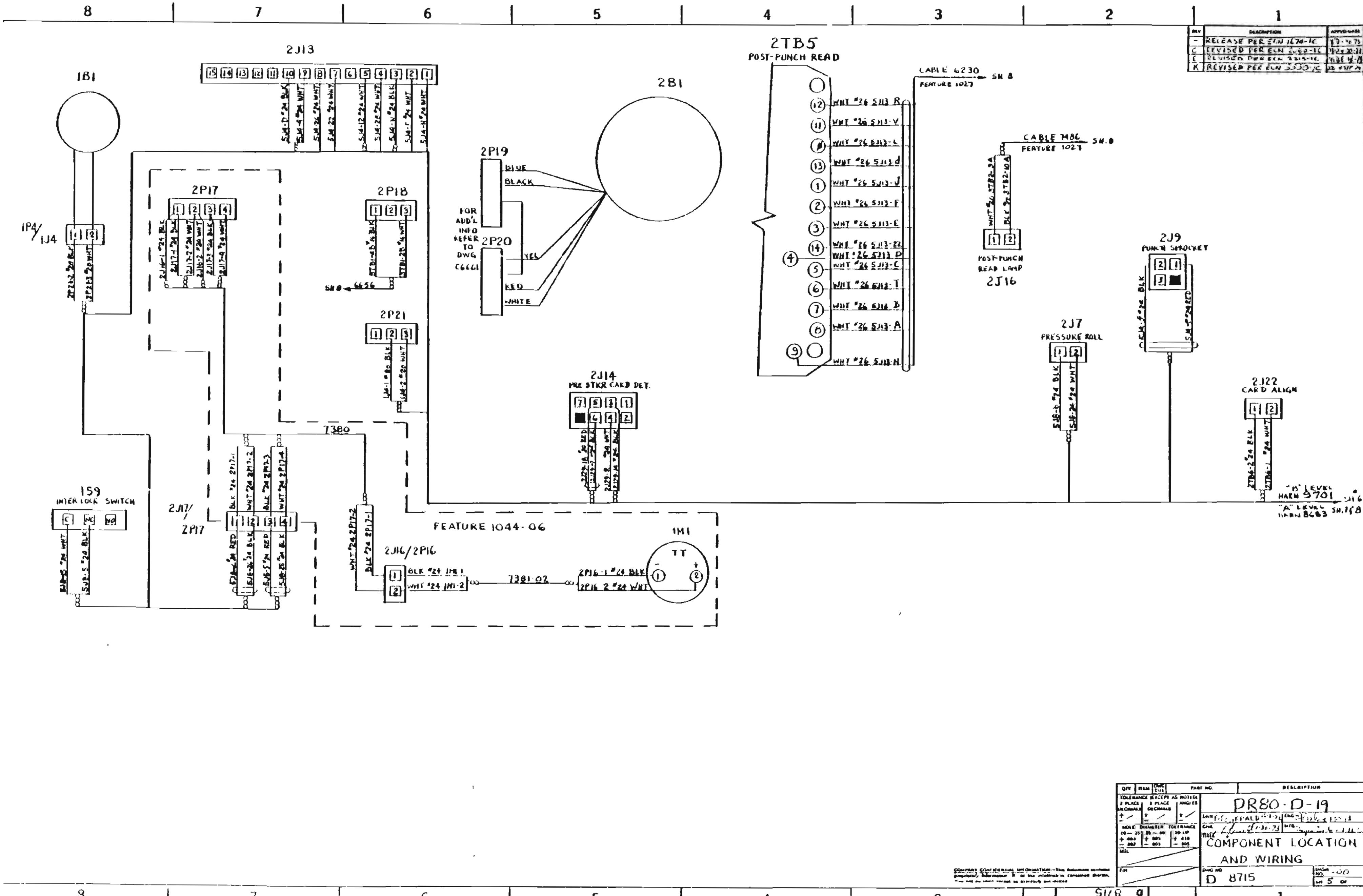
REV	DESCRIPTION
-	RELEASE PER ECN
C	
F	
J	REVISED PER ECN 2750-1C
K	REVISED PER ECN
L	REVISED PER ECN 3148-1C

DR80-D-18

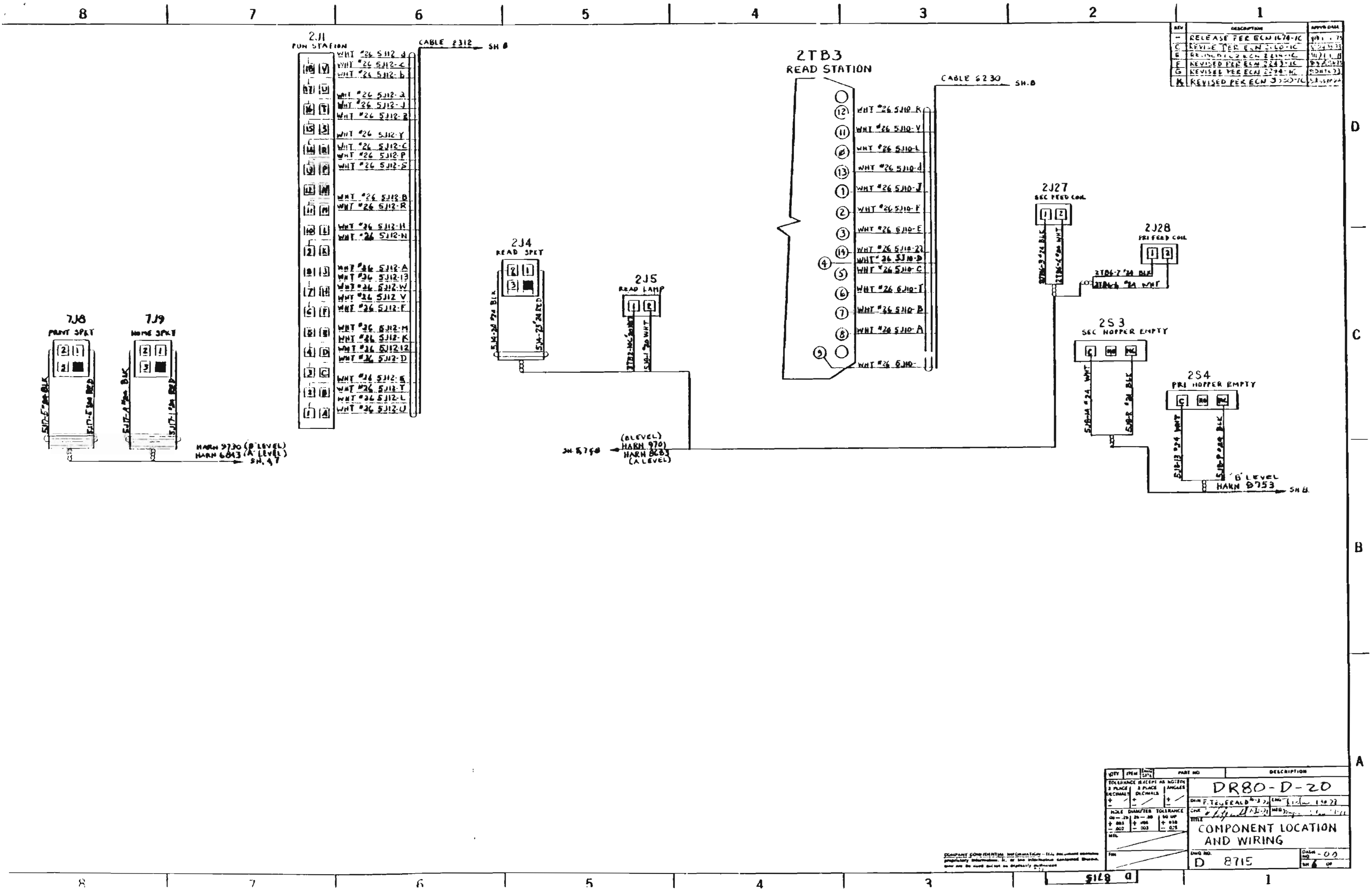
COMPONENT LOCATION & WIRING

8715

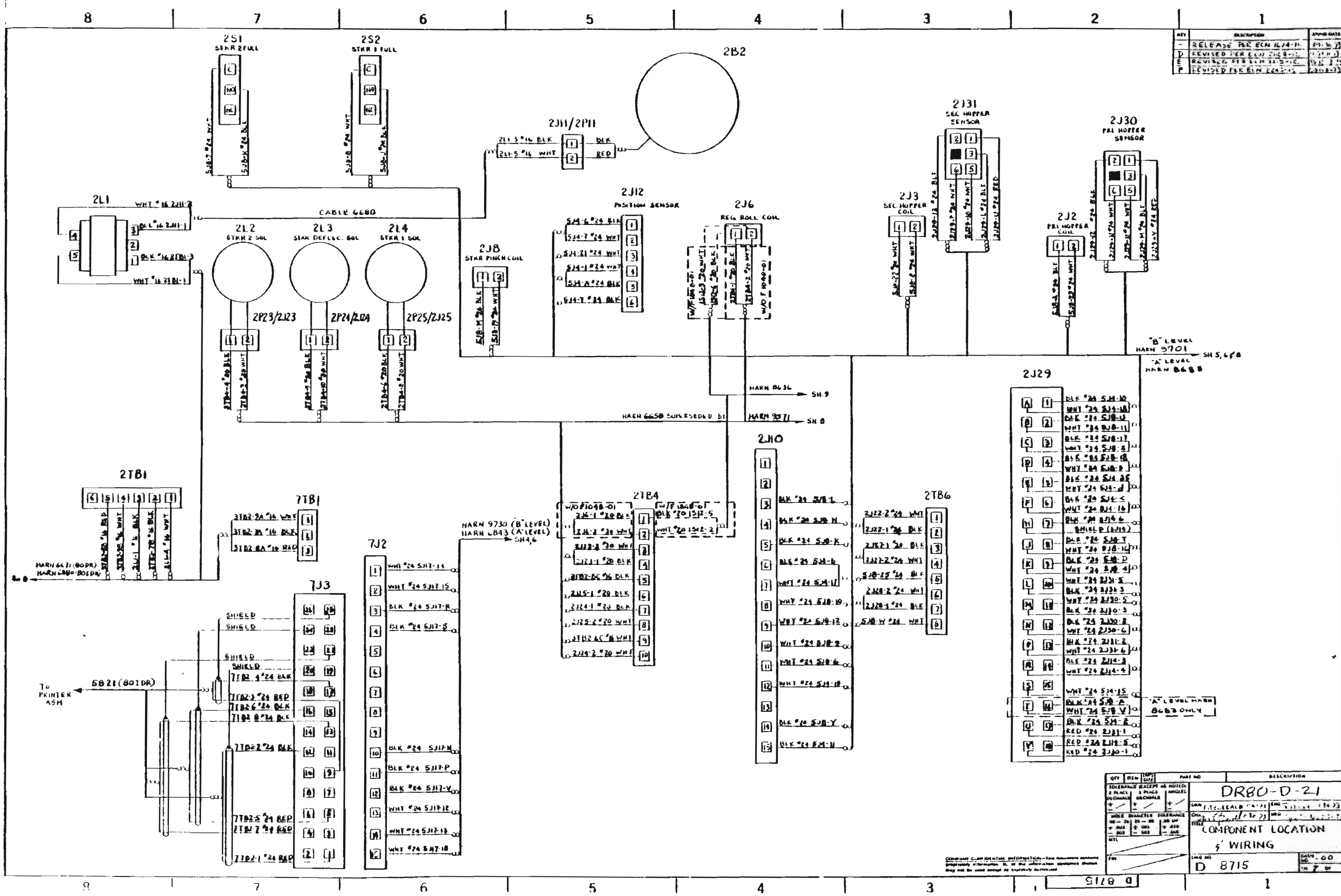
8715



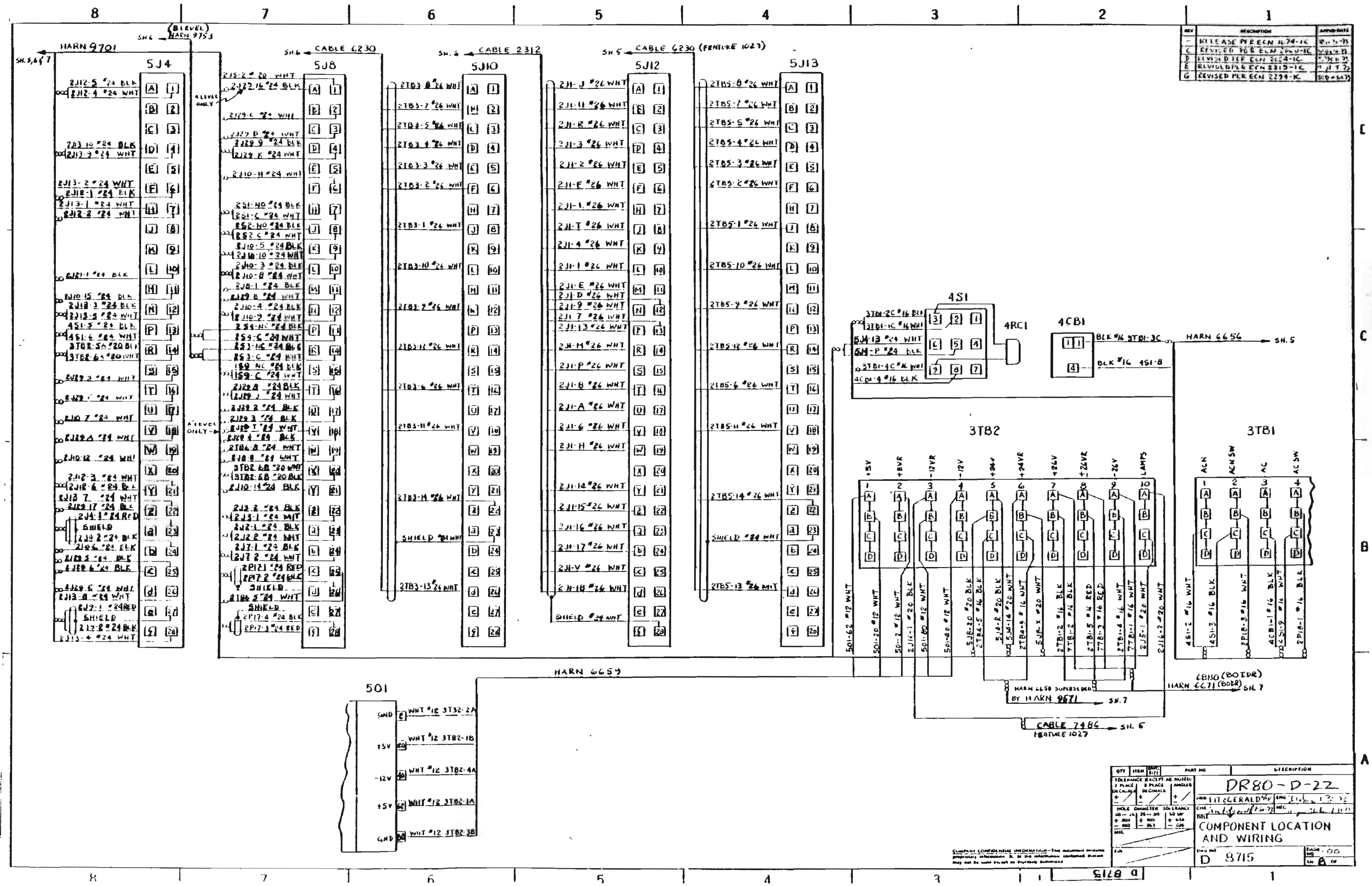
2J13
1B1
1P4/1J4
2P17
2P18
2P19
2P20
2P21
FOR ADD'L INFO REFER TO DWG C6661
BLUE
BLACK
YEL
RED
WHITE
2B1
2TB5
POST-PUNCH READ
CABLE 6230
FEATURE 1027
SH 8
CABLE 7486
FEATURE 1027
POST-PUNCH READ LAMP
2J16
2J9
PUNCH SPROCKET
2J7
PRESSURE ROLL
2J14
PRE STKR CARD DET.
2J22
CARD ALIGN
"B" LEVEL HARN 5701
"A" LEVEL HARN 8683
7380
1S9
INTERLOCK SWITCH
2J17/2P17
FEATURE 1044-06
2J16/2P16
7381-02
1M1
RELEASE PER ECN 1674-1C
REVISED PER ECN
REVISED PER ECN
REVISED PER ECN
PR80-D-19
COMPONENT LOCATION AND WIRING
D 8715



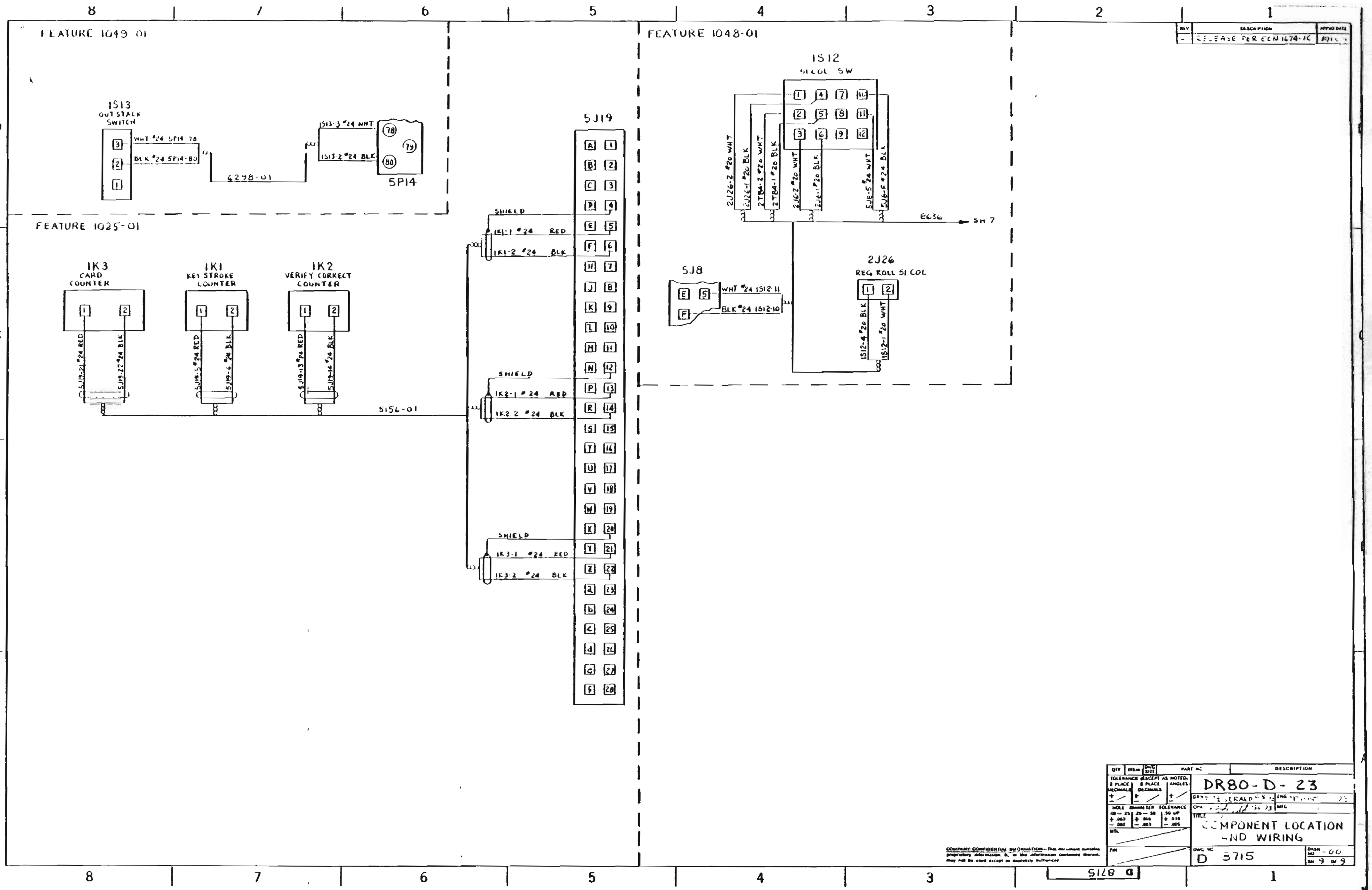
2J1
PUN STATION
CABLE 2312 SH 8
2TB3
READ STATION
CABLE 6230 SH.8
2J4
READ SPKT
2J5
READ LAMP
2J27
SEC FEED COIL
2J28
PRI FEED COIL
2S3
SEC HOPPER EMPTY
2S4
PRI HOPPER EMPTY
'B' LEVEL
HARN 8753 SH 8
7J8
PRNT SPKT
7J9
HOME SPKT
HARN 9730 ('B' LEVEL)
HARN 6843 ('A' LEVEL)
SH. 4,7
(B LEVEL)
HARN 970
HARN 8683
(A LEVEL)
SH 5758
RELEASE PER ECN 1674-IC
REVISED PER ECN 2243-IC
REVISED PER ECN 3350-IC
DR80-D-20
F. TEUFERALD
COMPONENT LOCATION
AND WIRING
D 8715



2S1 STKR 2 FULL
2S2 STKR 1 FULL
2B2
2J11/2P11
2L1
CABLE 6680
2L2 STKR 2 SOL
2L3 STKR DEFLEC. SOL
2L4 STKR 1 SOL
2P23/2J23
2P24/2J24
2P25/2J25
2J8 STKR PINCH COIL
2J12 POSITION SENSOR
2J6 REG ROLL COIL
2J3 SEC HOPPER COIL
2J31 SEC HOPPER SENSOR
2J2 PRI HOPPER COIL
2J30 PRI HOPPER SENSOR
"B" LEVEL HARN 9701
"A" LEVEL HARN 8688
SH 5,6,8
HARN 8636 SH 9
HARN 6650 SUPERSEDED BY HARN 9371 SH 8
2J29
2J10
2TB4
2TB6
2TB1
7TB1
7J2
7J3
HARN 9730 ('B' LEVEL)
HARN 6843 ('A' LEVEL)
SH 4,6
HARN 6471 (80DR)
HARN 6840 (80DR)
SHIELD
5821 (801DR)
TO PRINTER ASM
"A" LEVEL HARN 8683 ONLY
RELEASE PER ECN
REVISED PER ECN
DR80-D-21
COMPONENT LOCATION & WIRING
D 8715



REV | DESCRIPTION | APPD-DATE
- RELEASE PER ECN 1174-1C
C REVISED PER ECN 2060-1C
D REVISED PER ECN 2154-1C
E REVISED PER ECN 2215-1C
G REVISED PER ECN 2294-K
HARN 9701
5J4
5J8
5J10
5J12
5J13
CABLE 6230
CABLE 2312
CABLE 6230 (FEATURE 1027)
4S1
4RC1
4CB1
HARN 6656
3TB2
3TB1
+5V
+8VR
-12VR
-12V
+24V
+24VR
+26V
+26VR
-26V
LAMPS
ACN
ACN SW
AC
AC SW
HARN 6659
501
GND
+5V
-12V
HARN 6658 SUPERSEDED BY HARN 9671
HARN 6671 (80DR)
6880 (80IDR)
CABLE 7486
FEATURE 1027
DR80-D-22
FITZGERALD
COMPONENT LOCATION AND WIRING
D 8715



FEATURE 1049-01
1S13
OUT STACK SWITCH
WHT #24 5P14-78
BLK #24 5P14-80
4298-01
1S13-3 #24 WHT
1S13-2 #24 BLK
5P14
FEATURE 1025-01
1K3
CARD COUNTER
1K1
KEY STROKE COUNTER
1K2
VERIFY CORRECT COUNTER
5J19-21 #24 RED
5J19-22 #24 BLK
5J19-5 #24 RED
5J19-6 #24 BLK
5J19-13 #24 RED
5J19-14 #24 BLK
5156-01
SHIELD
1K1-1 #24 RED
1K1-2 #24 BLK
1K2-1 #24 RED
1K2-2 #24 BLK
1K3-1 #24 RED
1K3-2 #24 BLK
5J19
FEATURE 1048-01
1S12
51 COL SW
2J26-2 #20 WHT
2J26-1 #20 BLK
2TB4-2 #20 WHT
2TB4-1 #20 BLK
2J6-2 #20 WHT
2J6-1 #20 BLK
5J8-5 #24 WHT
5J8-F #24 BLK
E636
SH 7
5J8
WHT #24 1S12-11
BLK #24 1S12-10
2J26
REG ROLL 51 COL
1S12-4 #20 BLK
1S12-1 #20 WHT
RELEASE PER ECN 1674-1C
DR80-D-23
COMPONENT LOCATION AND WIRING
D 5715
SH 9 OF 9
D 8715